

CM1240

Dual-Voltage ESD Protection Array for USB Ports

Product Description

The CM1240 is a 4-channel ESD protection array. Three channels of the CM1240 are low voltage (LV) diodes, which have a capacitance of 7 pF enabling them to protect high speed I/O ports while providing robust ESD protection. The other channel of the CM1240 is a high voltage (HV) diode which has a capacitance of 25 pF enabling it to protect power supply inputs or OLED power rails, etc.

The parts integrate avalanche-type ESD diodes, which provide a very high level of protection for sensitive electronic components that may be subjected to electrostatic discharge (ESD). The ESD protection diodes are designed and characterized to safely dissipate ESD strikes of ± 8 kV, the maximum requirement of the IEC61000-4-2 international standard. Using the MIL-STD-883 (Method 3015) specification for Human Body Model (HBM) ESD, the pins are protected for contact discharges of greater than ± 16 kV.

This device is particularly well suited for portable electronics (e.g. wireless handsets, PDAs, notebook computers) because of its small package format and easy-to-use pin assignments. In particular, the CM1240 is ideal for protecting USB or mini-USB ports operating at full speed (12 Mbps).

Features

- Four Channels of ESD Protection
- ± 8 kV ESD Protection (IEC61000-4-2, Contact Discharge)
- ± 16 kV ESD Protection (HBM)
- SOT-563 Space-saving Package
- 16 V Clamp on V_{CC}
- These Devices are Pb-Free and are RoHS Compliant

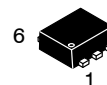
Applications

- USB and Mini-USB Applications
- I/O Port Protection for Mobile Handsets
- Wireless Handsets



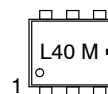
ON Semiconductor®

<http://onsemi.com>



SOT-563
SE SUFFIX
CASE 463A

MARKING DIAGRAM



L40 = Specific Device Code
M = Month Code
▪ = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping†
CM1240-F4SE	SOT-563 (Pb-Free)	5000/Tape & Reel

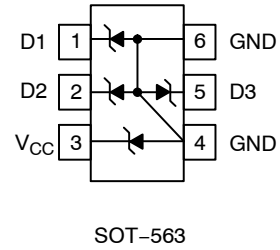
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

CM1240

Table 1. PIN DESCRIPTIONS

6-pin SOT-563 Package		
Pin	Name	Description
1	D1	Cathode Connection for Low Voltage ESD Diode
2	D2	Cathode Connection for Low Voltage ESD Diode
5	D3	Cathode Connection for Low Voltage ESD Diode
3	V _{CC}	Cathode Connection for High Voltage ESD Diode
-	NC	No Connect
-	NC	No Connect
-	NC	No Connect
-	NC	No Connect
4	GND	Anode-side Connection for All ESD Diodes
6	GND	Anode-side Connection for All ESD Diodes

PACKAGE / PINOUT DIAGRAMS



SPECIFICATIONS

Table 2. ABSOLUTE MAXIMUM RATINGS

Parameter	Rating	Units
ESD Protection (HBM)	16	kV
Pin Voltages V _{CC} to GND All Other Pins to GND	[GND - 0.3] to +13 [GND - 0.3] to +5.5	V
Storage Temperature Range	-65 to +150	°C
Lead Temperature (soldering, 10 sec)	300	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 3. STANDARD OPERATING CONDITIONS

Parameter	Rating	Units
Operating Temperature Range	-40 to +85	°C

CM1240

Table 4. ELECTRICAL OPERATING CHARACTERISTICS (Note 1)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
C_{LV}	LV Diode Capacitance at 3 Vdc; 1 MHz, 30 mVac			6		pF
C_{HV}	HV Diode Capacitance at 3 Vdc; 1 MHz, 30 mVac			25		pF
I_{LV}	LV Diode Leakage at +3.3 V reverse bias voltage			0.01	0.4	μ A
I_{HV}	HV Diode Leakage at +11 V reverse bias voltage			0.01	0.4	μ A
$V_{CL(LV)}$	LV Diode Signal Clamp Voltage: Positive Clamp, 10 mA Negative Clamp, -10 mA		5.6 -1.5	6.8 -0.8	9 -0.4	V
$V_{CL(HV)}$	HV Diode Signal Clamp Voltage: Positive Clamp, 10 mA Negative Clamp, -10 mA		13 -1.5	16 -0.8	19 -0.4	V
V_{ESD}	In-system ESD withstand voltage: Human Body Model (MIL-STD-883, method 3015) IEC 61000-4-2, contact discharge method	(Note 2)	\pm 25 \pm 12			kV
$R_{DYN(LV)}$	LV Diode Dynamic Resistance: Positive Negative			2.8 1.2		Ω
$R_{DYN(HV)}$	HV Diode Dynamic Resistance: Positive Negative			1.0 0.7		Ω

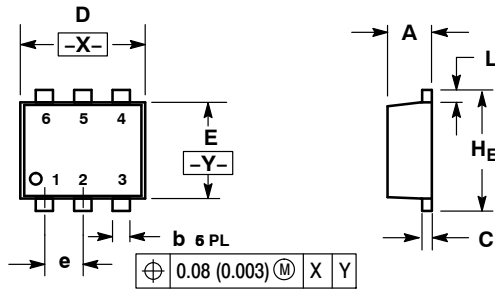
1. Guaranteed at 25°C only

2. ESD applied to input/output pins with respect to GND, one at a time. These parameters are guaranteed by design.

CM1240

PACKAGE DIMENSIONS

SOT-563, 6 LEAD CASE 463A-01 ISSUE F

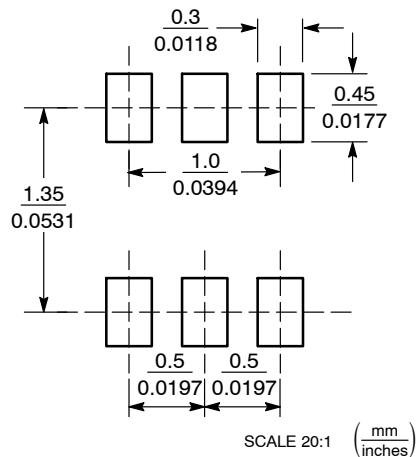


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
C	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
E	1.10	1.20	1.30	0.043	0.047	0.051
e	0.5 BSC			0.02 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
H _E	1.50	1.60	1.70	0.059	0.062	0.066

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

ON Semiconductor and are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>
For additional information, please contact your local Sales Representative