TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

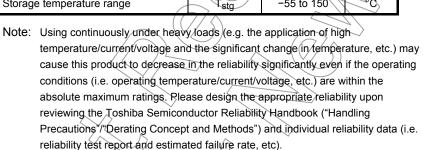
# 2SK2993

Chopper Regulator, DC–DC Converter and Motor Drive Applications

- Low drain-source ON resistance : R<sub>DS (ON)</sub> = 82 mΩ (typ.)
- High forward transfer admittance  $: |Y_{fs}| = 20 \text{ S (typ.)}$
- Low leakage current : I<sub>DSS</sub> = 100 μA (max) (V<sub>DS</sub> = 250 V)
- Enhancement mode : V<sub>th</sub> = 1.5 to 3.5 V (V<sub>DS</sub> = 10 V, I<sub>D</sub> = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	250	$(\sqrt{\cancel{3}})$
Drain-gate voltage (R <sub>GS</sub> = 20 kΩ)			V <sub>DGR</sub>	250	¥
Gate-source voltage			V <sub>GSS</sub>	±20	×
Drain current	DC	(Note 1)	۱ <sub>D</sub>	20	> A
	Pulse	(Note 1)	I <sub>DP</sub>	60	~
Drain power dissipation (Tc = 25°C)			PD	100	W
Single pulse avalanche energy (Note 2)			EAS	423	mJ
Avalanche current			IAR	20	A
Repetitive avalanche energy (Note 3)			EAR	10	mJ
Channel temperature				150	°C
Storage temperature range			Tstg	-55 to 150	C)



## **Thermal Characteristics**

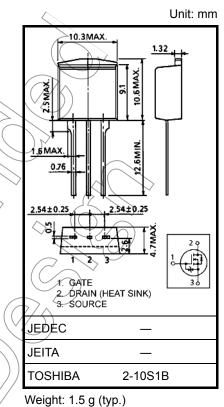
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.25	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	83.3	°C / W

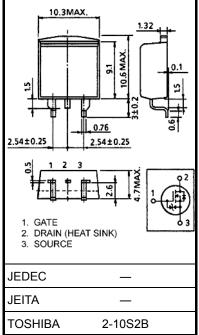
Note 1: Ensure that the channel temperature does not exceed 150°C.

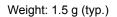
Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 1.79 mH, I<sub>AR</sub> = 20 A, R<sub>G</sub> = 25  $\Omega$ 

Note 3: Repetitive rating: pulse width limited by maximum channel temperature.

This transistor is an electrostatic-sensitive device. Please handle with caution.







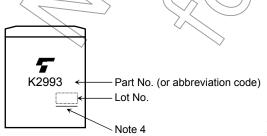
Electrical Characteristics (Ta = 25°C)

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	—	±10	μA
Drain cut-off cu	irrent	I <sub>DSS</sub>	V <sub>DS</sub> = 250 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source b	reakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	250	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	1.5	_	3.5	V
Drain-source O	N resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 10 A	$( \in )$	) 82	105	mΩ
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 10 A	10	20	_	S
Input capacitance		C <sub>iss</sub>		$\supset$	4000	_	
Reverse transfer capacitance		C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		300	_	pF
Output capacitance		Coss			1000	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \int I_{D} = 10A \\ V_{OUT} \\ R_{L} = 13\Omega$	_	15	$\langle \langle$	- ns
	Turn-on time	t <sub>on</sub>		-((	35	>	
	Fall time	t <sub>f</sub>			30	_	
	Turn-off time	t <sub>off</sub>	$V_{DD} = 130 V$ $Duty \leq 1\%, t_{W} = 10 \mu s$	2	180	_	
Total gate charg plus gate-drain	ge (gate-source )	Qg		) _	100	_	
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \approx 200 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 20 \text{ A}$	_	70	_	nC
Gate-drain ("mi	ller") charge	Q <sub>gd</sub>		_	30	—	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR		_	_	20	А
Pulse drain reverse current (Note 1)		-	_	_	60	А
Forward voltage (diode)	VDSF	$I_{DR} = 20 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-2.0	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 20 A, V <sub>GS</sub> = 0 V		300		ns
Reverse recovery charge	Qrr	dl <sub>DR</sub> / dt = 100 A / μs	_	3.3	_	μC

#### Marking

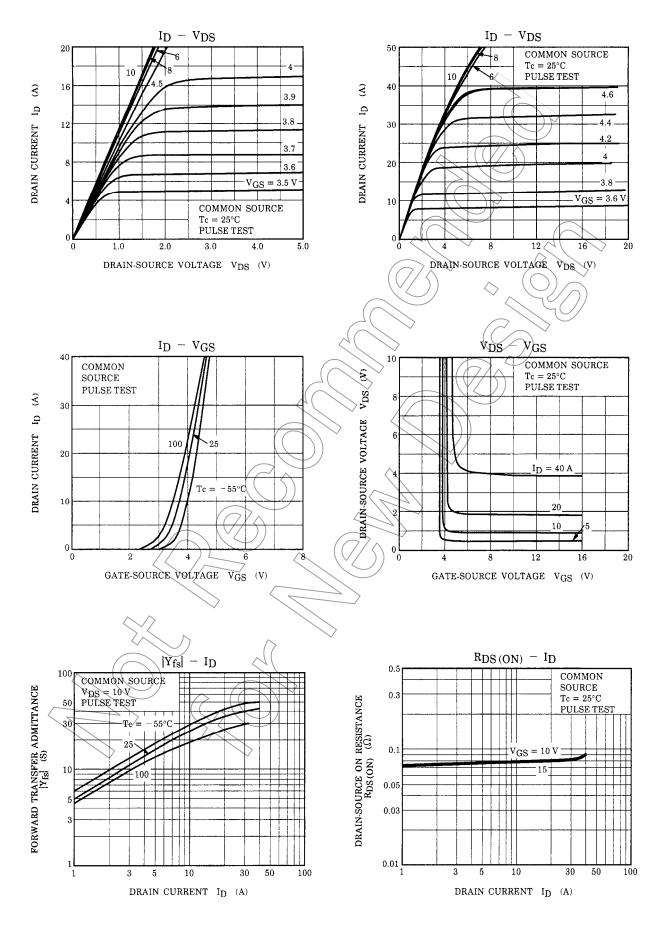


Note 4: A line under a Lot No. identifies the indication of product Labels.

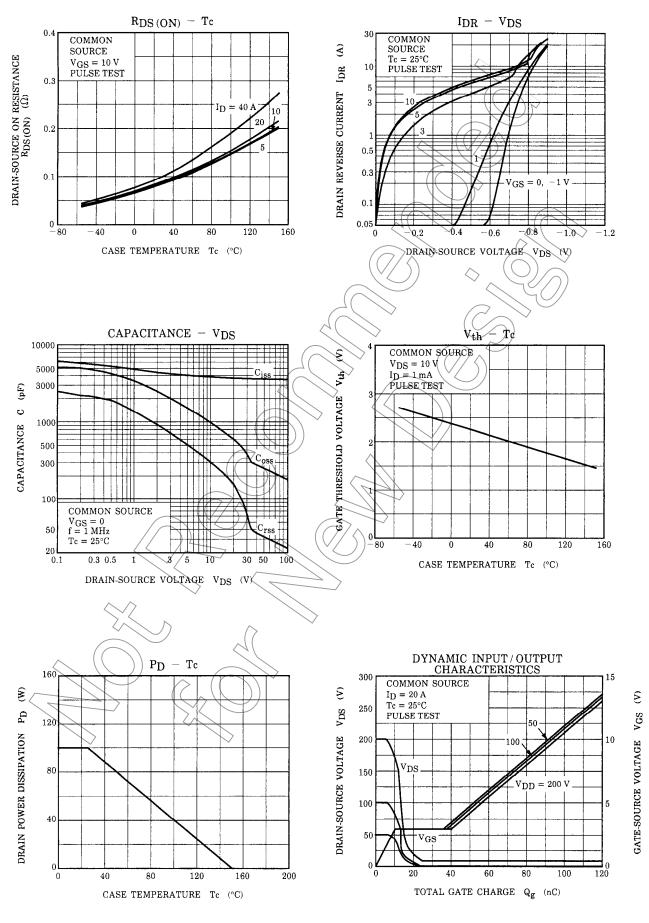
Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

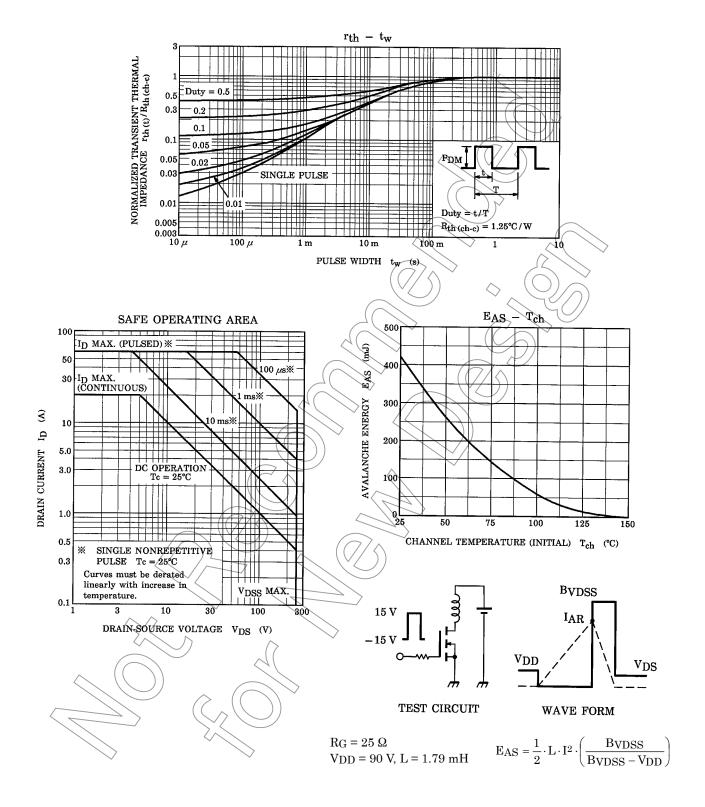
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