

Electronic overload relays

40A



Description

- Wide and adjustable current range
- Adjustable trip time (trip class 5-10-15-20-30)
- Designed suitable for use with contactors
- Directly mountable on the CGC-32, 40 contactors
- Separate mount versions are also available
- Mounting on 35mm DIN rail is possible by optional base.
- 1NO+1NC trip contacts
- Manual reset as standard (Automatic reset optional)

Extended protective functions

| | Number of sensors | 2CT | 3CT | 3CT |
|-----------|---------------------|-----------------|-----------------|--------------------|
| | Types (CGE40- ...) | (-2P, -2T, -2S) | (-3P, -3T, -3S) | (-3PR, -3TR, -3SR) |
| Functions | Overcurrent | ✓ | ✓ | ✓ |
| | Phase loss | ✓ | ✓ | ✓ |
| | Locked rotor | ✓ | ✓ | ✓ |
| | Phase unbalance | | ✓ | ✓ |
| | Phase reversed | | | ✓ |

Selection



| Mount/Connection | Sensor | Setting range | Catalog No. |
|---------------------------------|-------------------------------------|--------------------|--------------------------------------|
| Directly on a contactor | 2-sensor (2 CT) | 4 - 20A 8 - 40A | CGE40-2P - 20AN CGE40-2P - 40AN |
| | 3-sensor (3 CT) | 4 - 20A 8 - 40A | CGE40-3P - 20AN CGE40-3P - 40AN |
| | 3-sensor Reverse phase detection | 4 - 20A 8 - 40A | CGE40-3PR - 20AN CGE40-3PR - 40AN |
| | 2-sensor (2 CT) | 4 - 20A 8 - 40A | CGE40-2S - 20A CGE40-2S - 40A |
| | 3-sensor (3 CT) | 4 - 20A 8 - 40A | CGE40-3S - 20A CGE40-3S - 40A |
| | 3-sensor Reverse phase detection | 4 - 20A 8 - 40A | CGE40-3SR - 20A CGE40-3SR - 40A |
| Separate mount ① | 2-sensor (2 CT) | 4 - 20A 8 - 40A | CGE40-2T - 20A CGE40-2T - 40A |
| Cable connection with a screw ② | 3-sensor (3 CT) | 4 - 20A 8 - 40A | CGE40-3T - 20A CGE40-3T - 40A |
| | 3-sensor Reverse phase detection | 4 - 20A 8 - 40A | CGE40-3TR - 20A CGE40-3TR - 40A |
| | 2-sensor (2 CT) | 4 - 20A 8 - 40A | CGE40-2T - 20A CGE40-2T - 40A |
| | 3-sensor (3 CT) | 4 - 20A 8 - 40A | CGE40-3T - 20A CGE40-3T - 40A |
| Connection without a screw ② | 3-sensor Reverse phase detection | 4 - 20A 8 - 40A | CGE40-3TR - 20A CGE40-3TR - 40A |

Certificate

CE, ULcUL

Ordering information

Specify catalog number

Front face configuration



Current setting

0.1 - 1.5A
1 - 5A
4.4 - 22A

LED indicator

Operation status indication
- Normal operating
- Overload
- Phase unbalance
Trip cause indication
- Overcurrent
- Phase loss
- Reverse phase

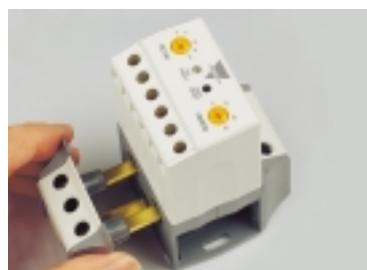
Test/Reset button

Trip time setting

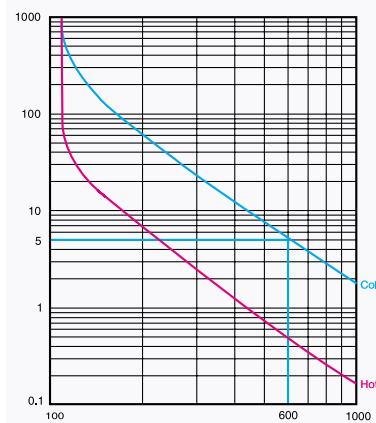
- 0 to 30 sec
- Set time is the trip time
at 6 x set current



① To mount on 35mm DIN rail use the optional base



② Cable connection part can be modified between screw connection and passing CT hole



Technical information

| | |
|----------------------------------|--|
| Relay control voltage | 100 to 260V AC 50/60Hz |
| Auxiliary contact | 3A/250VAC at resistive load 1NO(97-98) + 1NC(95-96) |
| Setting tolerance | Current \pm 5% Time \pm 5% (or \pm 0.5sec) |
| Insulation resistance | Min 100 M Ω at 500V DC |
| Impulse withstand voltage | 1.2x50 μ s 5kV (IEC1000-4-5) |
| Fast transient burst | 2kV/5min (IEC1000-4-4) |
| Ambient temperature | -25 to 70 °C for operation -30 to 80 °C for storage |
| Humidity | 30 to 90% RH |

For more information

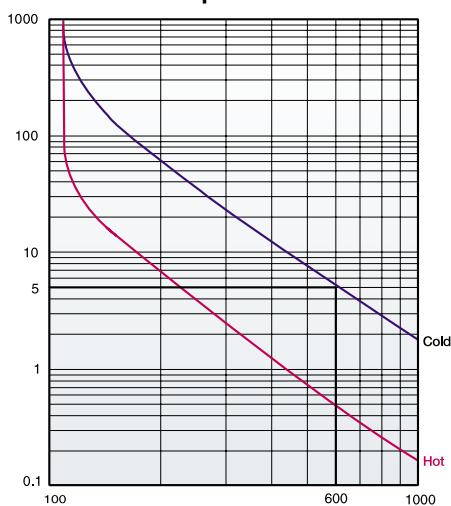
- Drawings → page 169
- Connections → page 170
- Contactors → page 40
- Starters → page 81
- Bimetallic overload relay → page 65
- Operating curves → page 137

Trip curves for electronic overload relays

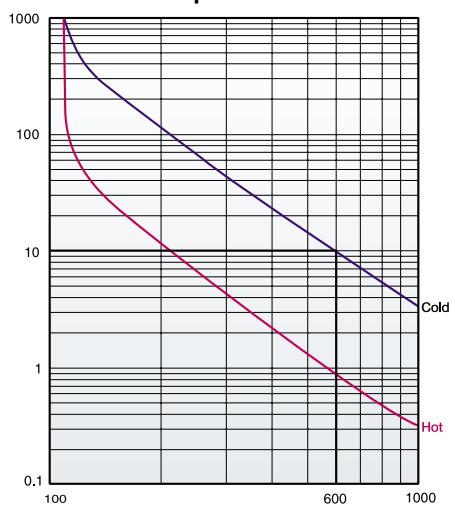


CGE

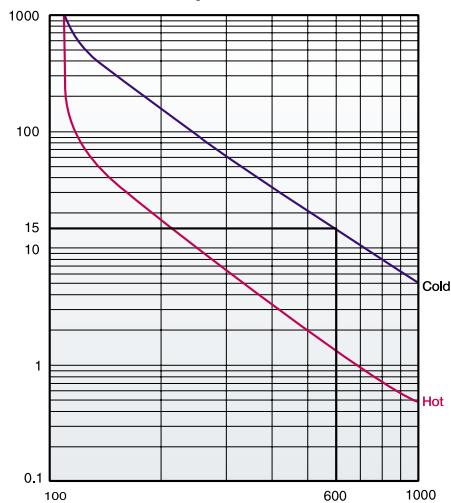
Trip class 5



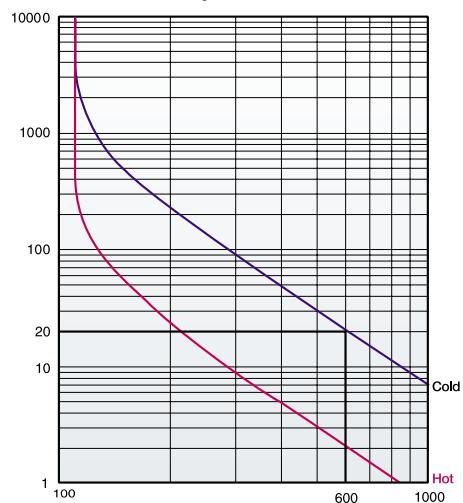
Trip class 10



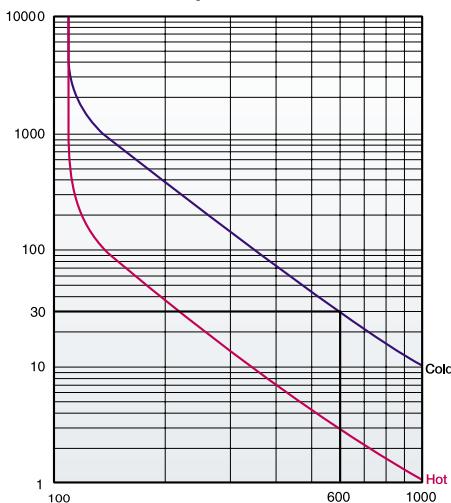
Trip class 15



Trip class 20



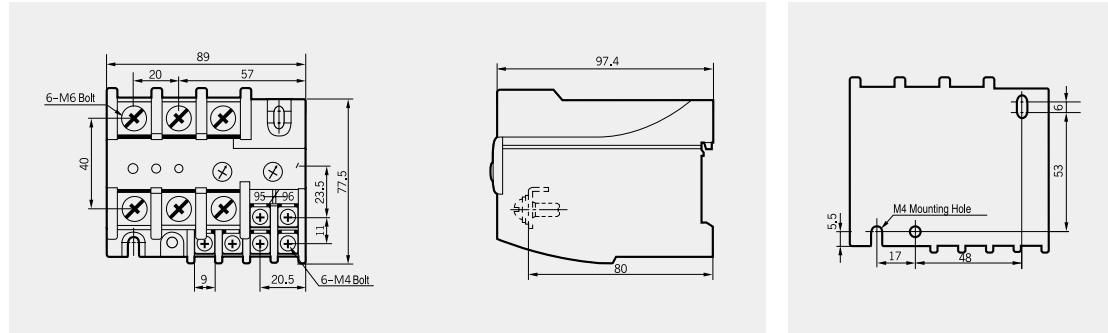
Trip class 30



CGE80-2S

CGE80-3S

CGE80-3SR



Terminal configuration : See Fig. 2

0.42kg/0.46kg

Terminal configuration

| R/1/L1 | S/3/L2 | T/5/L3 |
|---------|---------|---------|
| A1 A2 | 95 96 | 97 98 |
| U/2/T1 | V/4/T2 | W/6/T3 |

Fig. 1

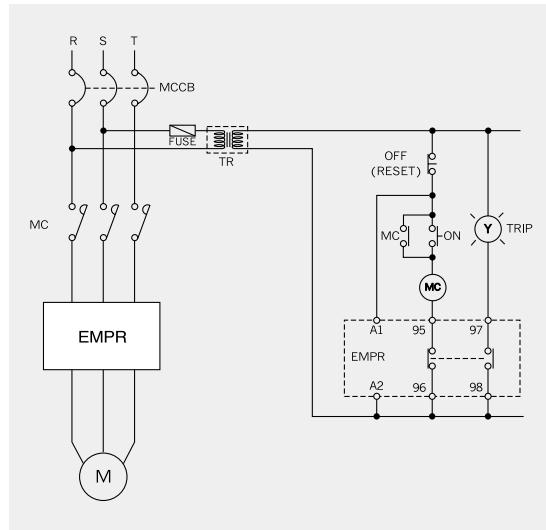
| R/1/L1 | S/3/L2 | T/5/L3 |
|---------|---------|---------|
| A1 A2 | 95 96 | 97 98 |
| U/2/T1 | V/4/T2 | W/6/T3 |

Fig. 2

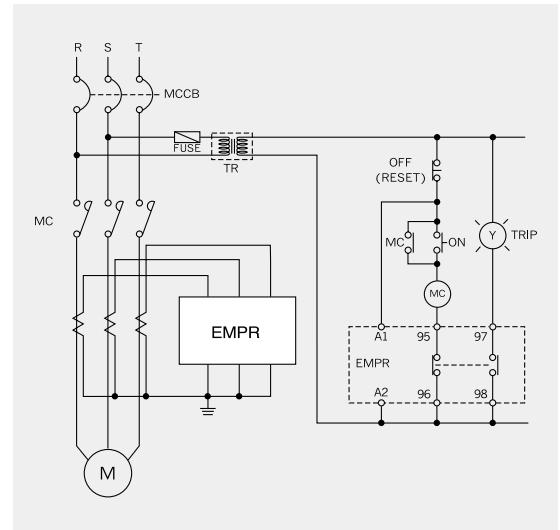
| R/1/L1 | S/3/L2 | T/5/L3 |
|---------|---------|---------|
| A1 A2 | 95 96 | 97 98 |
| U/2/T1 | V/4/T2 | W/6/T3 |

Fig. 3

Circuit diagram



Without additional CTs



In case of using additional CTs