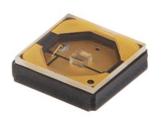




Deep UV LED - 310nm

UV CA3535 series (CUD1GF1A) CUD1GF1A





Product Brief

Description

CUD1GF1A is a deep ultraviolet light emitting diode with peak emission wavelengths from 305nm to 315nm.

The LED is encased in a ceramic package with a UV-transparent optical window.

It incorporates state of the art SMD design and low thermal resistance.

CUD1GF1A is designed for medical and analytical instrumentation, chemical and biological analysis, and horticulture .Additional applications include deep UV curing of polymers, and Vitamin D generation, and skin care.

Features and Benefits

- Deep ultraviolet LED
- Low thermal resistance
- SMT solderable
- Lead-free product
- RoHS compliant

Key Applications

- Fluorescent spectroscopy
- Chemical and biological analysis
- Horticulture
- Curing
- Vitamin D
- Skin care





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Performance Characteristics

Table 1. Electro - Optical characteristic at 20mA

 $(T_a=25^{\circ}C, RH=30\%)$

Parameter	Symbol	Value	Unit
Peak Wavelength [1]	λ_{p}	310	nm
Optical Output Power [2]	Ф _е ^[3]	1.2	mW
Forward Voltage [4]	V_{F}	5.5	V
Spectrum Half Width	Δλ	11	nm
View Angle	2O _{1/2}	120	deg.
Thermal Resistance (J to S) [5]	$R\theta_{J-S}$	21.5	ºC/W

Table 2. Absolute Maximum Rating

Parameter	Symbol	Value			Unit
Faiailletei		Min.	Тур.	Max.	Offic
Forward Current	I _F	-	-	30	mA
Power Dissipation	P_{D}	-	-	220	mW
Operating Temperature [7]	T_{opr}	- 30	-	60	°C
Storage Temperature [7]	T_{stg}	- 40	-	100	ºC

- 1. Peak wavelength measurement tolerance: $\pm 3 \text{ nm}$
- 2. Optical Output Power measurement tolerance: \pm 10%
- 3. Φ_e is the Optical Output Power as measured with an integrated sphere
- 4. Forward voltage measurement tolerance: \pm 3%
- 5. $R\theta_{J\text{-}S}$ is the thermal resistance between chip junction and solder point
- 6. Exposure to the absolute maximum rated conditions may affect device reliability
- 7 .At extreme temperatures extra care must be taken to keep devices free from moisture



Fig 1. Spectrum, Ta=25°C, I_F=20mA

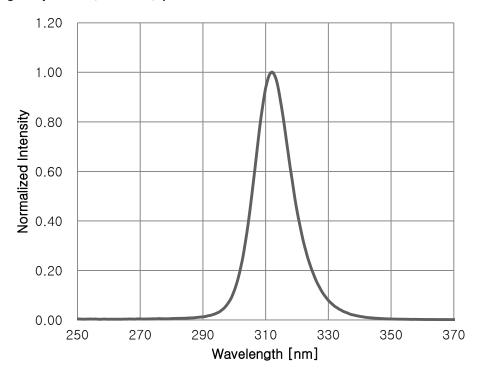


Fig 2. Forward Current vs. Forward Voltage, T_a=25°C

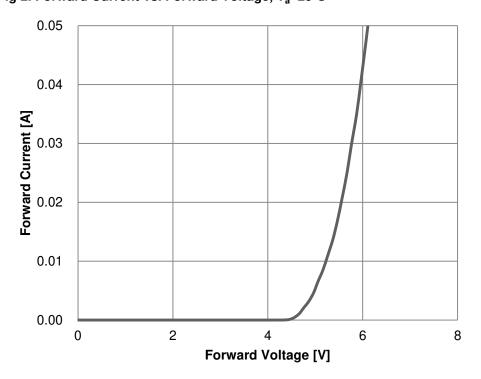




Fig 3. Relative Optical Output Power vs. Forward Current, Ta=25°C

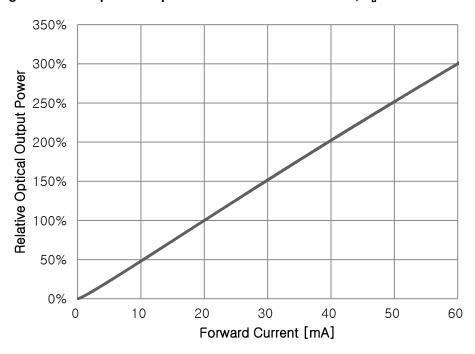


Fig 4. Peak Wavelength vs. Forward Current, T_a=25°C

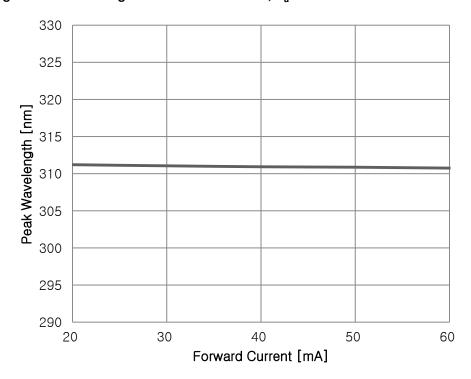




Fig 5. Relative Optical Output Power vs. Ambient Temperature, I_F=20mA

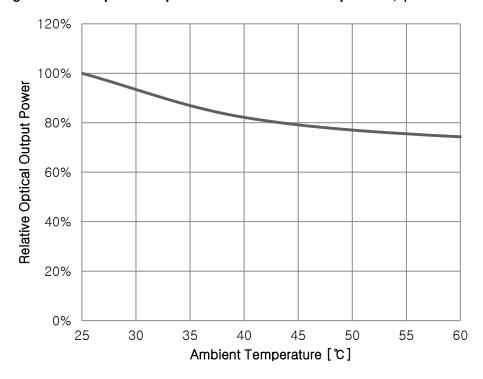


Fig 6. Peak Wavelength vs. Ambient Temperature, I_F=20mA

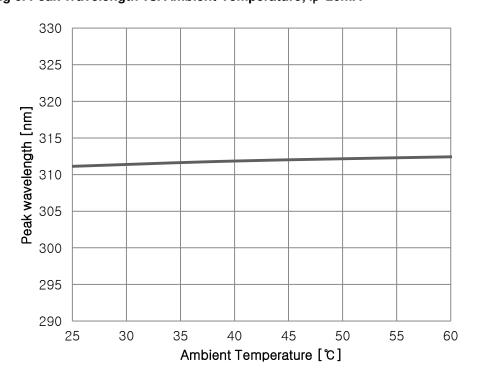




Fig 7. Forward Voltage vs Ambient Temperature, I_F=20mA

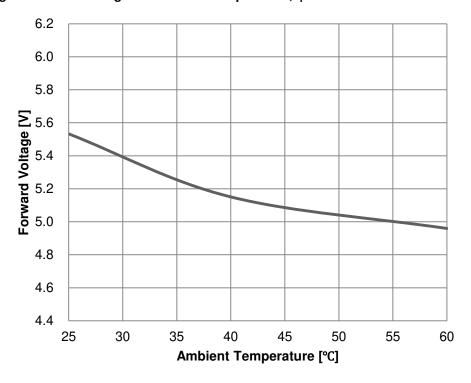
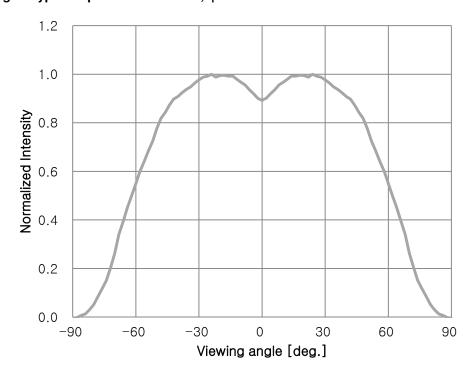


Fig 8. Typical Spatial Distribution, I_F=20mA





Binning Structure

Table 3. Binning Structure , I_F=20mA



	Y ₁ Y ₂		$Y_3Y_4Y_5$			Y_6		
	Wp [nm]		Optical Output Power [mW]			Vf [V]		
BIN	MIN	MAX	BIN	MIN	MAX	BIN	MIN	MAX
e2	305	307.5	C10	1.0	1.25	а	5.0	5.2
e3	307.5	310	C12	1.25	1.5	b	5.2	5.4
e4	310	312.5	C15	1.5	1.8	С	5.4	5.6
e5	312.5	315	C18	1.8	2.1	d	5.6	5.8
			C21	2.1	2.4	е	5.8	6.0
						f	6.0	6.2
						g	6.2	6.4
						h	6.4	6.6
						i	6.6	6.8
						j	6.8	7.0
						k	4.6	4.8
						I	4.8	5.0

Table 4. Ranks:

Binning Code	Description	Unit
Y ₁ Y ₂	Peak Wavelength	nm
Y ₃ Y ₄ Y ₅	Optical Output Power	mW
Y ₆	Forward Voltage	٧

Notes:

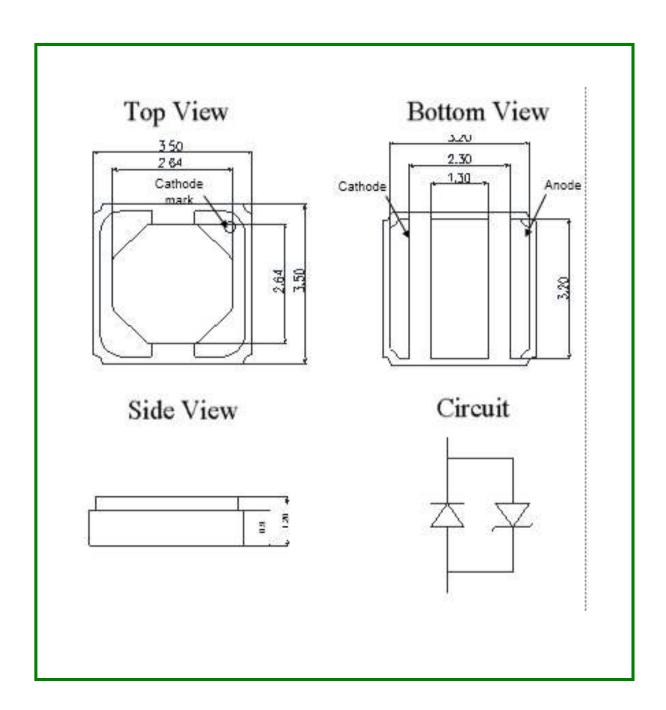
1. Peak Wavelength measurement tolerance : $\pm 3 \text{nm}$

2. Optical Output Power measurement tolerance : $\pm\ 10\%$

3. Forward Voltage measurement tolerance : $\pm 3\%$



Mechanical Dimensions

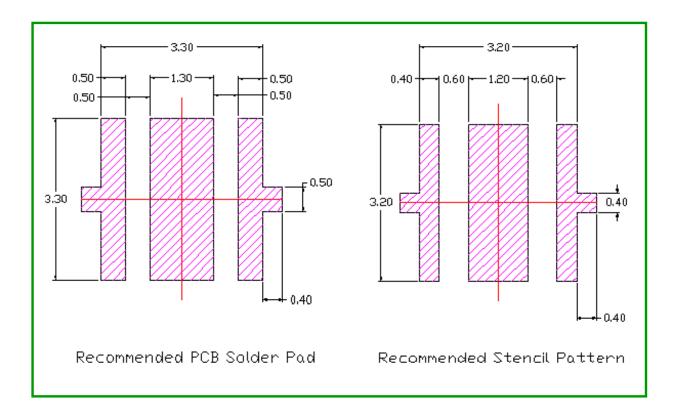


- [1] All dimensions are in millimeters
- [2] Not to scale
- [3] Undefined tolerances are ± 0.2 mm



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Recommended Solder Pad

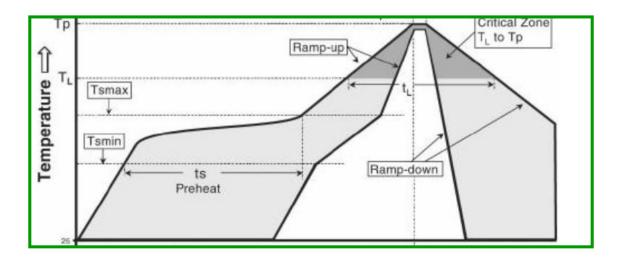


- [1] All dimensions are in millimeters
- [2] Not to scale
- [3] This drawing without tolerances is for reference only





Reflow Soldering Characteristics



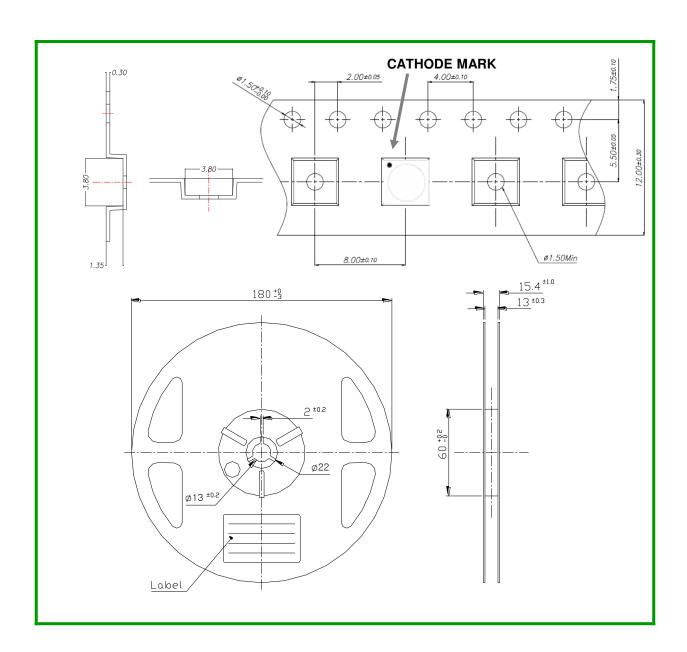
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (Ts_max to Tp)	3° C/second max.	3° C/second max.
Preheat		
- Temperature Min (Ts_min)	100 °C	150 °C
- Temperature Max (Ts_max)	150 °C	200 °C
- Time (Ts_min to Ts_max) (ts)	60-120 seconds	60-180 seconds
Time maintained above:		
- Temperature (TL)	183 °C	217 °C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature (Tp)	215°C	260°C
Time within 5°C of actual Peak Temperature (t)2	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

- * Caution
- 1. Reflow soldering should not be done more than one time.
- 2. Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
- 3. Die slug is to be soldered.
- 4. When soldering, do not put stress on the LEDs during heating.
- 5. After soldering, do not warp the circuit board.
- 6. Recommend to use a convection type reflow machine with 7 ~ 8 zones.



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Emitter Tape & Reel Packaging

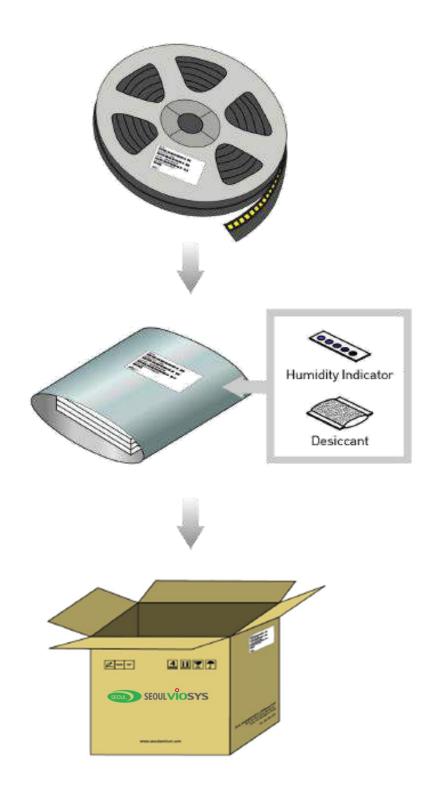


- 1.Quantity: 100pcs/Reel
- 2.Cumulative Tolerance : Cumulative Tolerance/10 pitches to be $\pm 0.2 \text{mm}$
- 3. Adhesion Strength of Cover Tape: Adhesion strength to be 10-60g when the cover tape
- is turned off from the carrier tape at the angle of 10° to the carrier tape



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Emitter Tape & Reel Packaging





Product Nomenclature

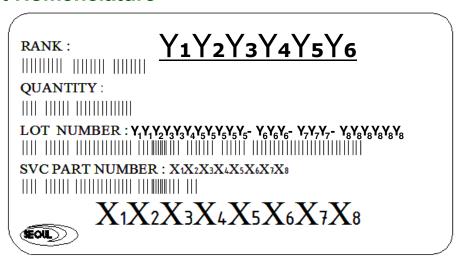


Table 5. Part Numbering System: X₁X₂X₃X₄X₅X₆X₇X₈

Part Number Code	Description	Part Number	Value
X ₁	Company	С	SVC
X ₂	UV LED	U	
X ₃ X ₄	Wavelength	D7	Deep UV 275nm
X ₅	Package Series	G	CA3535
X ₆	Lens type	F	Flat window
X ₇	Chip Q'ty	1	1 chip
		А	Ver0
X ₈	Version	В	Ver1
		С	Ver2

Table 6. Lot Numbering System: Y₁Y₁Y₂Y₃Y₃Y₄Y₅Y₅Y₅Y₅Y₅- Y₆Y₆Y₆- Y₇Y₇Y₇- Y₈Y₈Y₈Y₈Y₈Y₈Y₈Y₈

Lot Number Code	Description		
Y ₁	Year		
Y ₂	Month		
Y ₃	Day		
Y ₄	Production area		
Y ₅	Mass order		
Y ₆	Taping number		
Y ₇	Reel number		
Y ₈	Internal management number		



Reliability Test

Table 7. Test Result

Test Item	Test Condition	Note	# Failed /Tested
Room Temp. Operational Life	Ta=25°C, IF=20mA	1000hrs	0/5
Thermal shock	Ta max=120°C, Ta min=-40°C 30min dwell/transfer time : 10sec, 1 cycle=1hr	200 cycles	0/22
Resistance to Soldering	Temp=260 \pm 5°C, Time : 10 \pm 1 sec	1 time	0/10
ESD	R=1.5kΩ, C=100pF Voltage level=2kV	3 times Negative /positive	0/22

Table 8. Failure Criteria

Parameter	Symbol	Test Conditions	Max. or Min. allowable shift value
Forward Voltage	V_{F}	IF=20mA	Max. Initial measurement x 1.2
Optical Output Power	Фе	IF=20mA	Min. Initial measurement x 0.7

Notes:

1. The value is measured after the test sample is cooled down to the room temperature.



Precaution for Use

A. UV Light

- These devices are ultraviolet LEDs. During operation, the LED emits high intensity ultraviolet (UV) light, which is harmful to skin and eyes. Do not look directly into the UV light and wear protective equipment during operation.
- UV light is hazardous to skin and may cause cancer. Avoid exposure to UV light when LED is operational.
- Precautions must be taken to avoid looking directly at the UV light without the use of UV light protective glasses. Do not look directly at the front of the LED or at the LED's lens when LED is operational.
- Attach the following warning labels on products/systems that use UV LEDs.



B. Static Electricity

- Electrostatic discharge (ESD) is the defined as the release of static electricity when two
 objects come into contact. While most ESD events are considered harmless, it can be an
 expensive problem in many industrial environments during production and storage. The
 damage from ESD to an LEDs may cause the product to demonstrate unusual
 characteristics such as:
 - Increase in reverse leakage current lowered turn-on voltage
 - Abnormal emissions from the LED at low current
- The following recommendations are suggested to help minimize the potential for an ESD event.
- One or more recommended work area suggestions:
 - Ionizing fan setup
 - ESD table/shelf mat made of conductive materials
 - ESD safe storage containers
- One or more personnel suggestion options:
 - Antistatic wrist-strap
 - Antistatic material shoes
 - Antistatic clothes
- Environmental controls:
 - Humidity control (ESD gets worse in a dry environment)



Precaution for Use

- C. Operating Conditions
- In order to ensure the correct functioning of these LEDs, compliance to the maximum electrical specifications is paramount. These LEDs are particularly sensitive to any current value that exceeds the absolute maximum rating of the product. Any applied current in excess of the maximum specification will cause damage and possible complete failure of the product.
- The current flowing in a LED is an exponential function of the voltage across it. A small
 change in voltage can produce a very large change in current and lead to complete
 failure of the LED. The use of current regulated drive circuits are recommended for these
 products.
- Any attempt to drive these UV LEDs with a voltage source instead of a current source will cause damage and possible complete failure of the product.
- These devices are not designed to be used under negative bias.
- · This device is not to be used in any type of fluid such as water, oil, organic solvent, etc.
- These LEDs are susceptible to heat generation. Use care to design end product with adequate thermal management to ensure that LEDs do not exceed maximum recommended temperatures. Operating LEDs at temperatures in excess of specification will result in damage and possible complete failure of the product. When the LEDs are in operation the maximum current should be decided after measuring the package temperature.
- The driving circuit must be designed to allow forward voltage only when it is ON or OFF.
 If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.
- EOS (Electrical Over Stress) EOS is defined as damage that may occur when an
 electronic device is subjected to a current or voltage that is beyond the maximum
 specification limits of the device. The effects from an EOS event can be noticed through
 product performance like:
 - Changes to the performance of the LED package (If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker or show severe performance degradation.)
 - o Changes to the light output of the luminaire from component failure
 - o Components on the board not operating at determined drive power
 - Failure of performance from an entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures.
- It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes vary significantly, but there are some common signs that will indicate an EOS event has occurred:
 - Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
 - Damage to the bond pads located on the emission surface of the LED package (shadowing can be noticed around the bond pads while viewing through a microscope)
 - Anomalies noticed in the encapsulation and phosphor around the bond wires. This damage usually appears due to the thermal stress produced during the EOS event.



Precaution for Use

- C. Operating Conditions Continued
- To help minimize the damage from an EOS event Seoul Viosys recommends utilizing:
 - A surge protection circuit
 - o An appropriately rated over voltage protection device
 - A current limiting device

D. Storage

- To avoid moisture penetration, we recommend storing UV LEDs in a dry box with a desiccant. The recommended temperature and relative humidity are between 5°C and 30°C and below 50% respectively.
- LEDs must be stored properly to maintain the device. If the LEDs are stored for 3 months or more after being shipped from SETI, a sealed container with a nitrogen atmosphere should be used for storage.
- Replace the remained LEDs into the moisture-proof bag and reseal the bag after work to avoid those LEDs being exposed to moisture. Prolonged exposure to moisture can adversely affect the performance of the LEDs.
- If the package has been open for more than 168hr or the color of the desiccant changes, components should be dried for 10-12hr at $60\pm5^{\circ}$ C.
- The conditions of resealing should be as follows: Temperature between 5 and 40°C and relative humidity less than 30%

E. Handling Precautions

- VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate the LED packages and affect LED performance and lifetime.
 Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.
- When attaching LEDs, do not use adhesives that outgas organic vapor.
- Soldering should be done as soon as possible after opening the moisture-proof bag.
- · Do not rapidly cool device after soldering.
- Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.
- Components should not be mounted on warped (non coplanar) portions of a PCB.
- The UV LED is protected by a lens which requires careful handling
 - Avoid touching glass lens parts especially with sharp tools such as tweezers
 - Avoid leaving fingerprints on glass lens parts.
 - When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that excessive mechanical pressure on the surface must be prevented.



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CA3535 - CUD1GF1A

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• Do not handle this product with acid or sulfur material in sealed space.

F. Cleaning

If cleaning is required, isopropyl alcohol or water with immediate drying is recommended.
 Do not use ultrasonic cleaning.





Company Information

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Company Information

Seoul Viosys (www.seoulviosys.com) manufactures light emitting diodes (LEDs) with a full range of UV wavelengths from UVC to UVA (under 400nm) for Industrial Curing, Air/Water Purification, Disinfection and Home appliance.

The company is one of the world leading UV LED supplier, holding more than 4,000 patents globally, while offering various kinds of LED technologies and application-solutions in High power UV LED, UV sensor, UV LED Lamp and variety of UV LED sourced Applications.

The company's broad product portfolio includes hybrid modules for unique applications such as UV disinfection, deodorization, UV purification as well as customized modules for your Application.

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