



HIGH PF, OFFLINE DIMMABLE LED DRIVER WITH INTERNAL HIGH-VOLTAGE MOSFET

Description

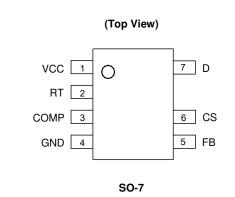
The AL1697 is a high performance, high power factor, high efficiency and high precision buck-boost LED driver for mains dimmable LED lamp applications. The AL1697 topology provides an accurate output current over wide line and load regulation. The wide switching frequency operates at boundary conduction mode (BCM) to ease EMI/EMC design, and testing to meet the latest regulatory standards.

The AL1697 with built in MOSFET solution reduces the bill of material (BOM) cost by eliminating the need of auxiliary winding and an external high voltage MOSFET. The AL1697 has extensive protection features to enhance system safety and reliability. The AL1697 starts reducing output current when the driver's temperature is higher than the built-in thermal foldback protection trigger point.

The AL1697 applies to a wide range of dimmers including leading edge and trailing edge dimmer. Some can achieve deep dimming down to 1%. The AL1697 dimming curve is compliant with the NEMA SSL6 standard.

The AL1697 is available in SO-7 package, which provides an extra pin spacing between the high voltage MOSFET's drain and low voltage pins to increase electricity isolation.

Pin Assignments



Applications

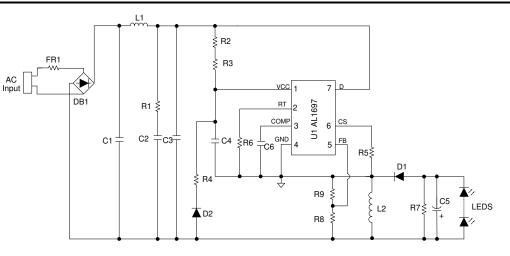
- Mains Dimmable LED Lamps
- Offline LED Power Supply Driver

Features

- Three Internal High-Voltage MOSFET Options:
 - R_{DSON} 2.8Ω(3A /400V), 4Ω(2A/600V) and 1.8Ω(4A /670V) for Super-Junction
- Tight Current Sense Tolerance : ± 3%
- Low Startup Current: 130µA
- Low Operation Current: 170µA (Switching Frequency at 4kHz)
- Single Winding Inductor
- Wide Range of Dimmer Compatibility
- NEMA SSL6 Dimming Curve Compliant
- Internal Protections
 - Under Voltage Lockout (UVLO)
 - Leading-Edge Blanking (LEB)
 - Cycle-by-cycle Over Current Protection (OCP)
 - Output Open/Short Protection (OVP/OSP)
 - Thermal Foldback Protection (TFP)
 - Over-Temperature Protection (OTP)
- SO-7 Package
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 - See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



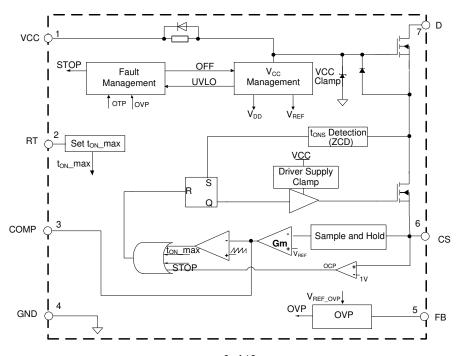
Typical Applications Circuit



Pin Descriptions

Pin Number	Pin Name	Function			
1	VCC	Power Supply Voltage			
2	RT	Resistor Set the System'S Maximum t _{ON}			
3	COMP	Compensation for Current Control			
4	GND	Ground			
5	FB	Feedback for LED Open Protection Voltage			
6	CS	Current Sensing			
7	D	Drain of the Internal High Voltage MOSFET			

Functional Block Diagram





Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.) (Note 4)

Symbol	Parameter	Rating	Unit	
V _{CC}	Power Supply Voltage	18	V	
	Voltage on Drain Pin (AL1697-30BAS7-13)	400	V	
V _D	Voltage on Drain Pin (AL1697-20CS7-13)	600	V	
	Voltage on Drain Pin (AL1697-40DS7-13)	670	V	
	Continuous Drain Current $T_c = +25$ °C (AL1697-30BAS7-13)	3	А	
I _{DS}	Continuous Drain Current $T_c = +25$ °C (AL1697-20CS7-13)	2	А	
	Continuous Drain Current $T_c = +25$ °C, (AL1697-40DS7-13)	4	А	
V _{CS}	Voltage on CS Pin	-0.3 to 7	V	
V _{RT}	Voltage on RT Pin	-0.3 to 7	V	
V _{FB}	Voltage on FB Pin	-0.3 to 7	V	
TJ	Operating Junction Temperature	-40 to +150	°C	
T _{STG}	Storage Temperature	-65 to +150	°C	
T _{LEAD}	Lead Temperature (Soldering, 10 seconds) +260		°C	
PD	Power Dissipation ($T_A = +50^{\circ}$ C) (Note 5)		W	
θ_{JA}	Thermal Resistance (Junction to Ambient) (Note 5) 123		°C/W	
θJC	Thermal Resistance (Junction to Case) (Note 5) 19		°C/W	
_	ESD (Human Body Model)	2,000	V	

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability. All voltages unless otherwise stated and measured with respect to GND.

5. Device mounted on 1"x1" FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Мах	Unit
T _A	Ambient Temperature (Note 6)	-40	+105	°C
V _{CC}	Operating V _{CC} Voltage	10	$V_{CC_CLAMP}(Min)$	V

Note: 6. The device may operate normally at +125°C ambient temperature under the condition not trigger temperature protection.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

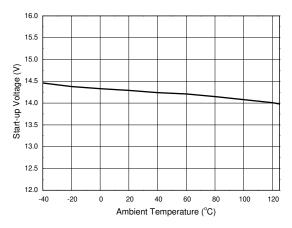
Symbol	Parameter	Condition	Min	Тур	Max	Unit	
UVLO							
V _{TH (ST)}	Startup Voltage	_	_	14.5	-	V	
	Minimal Operating Voltage	After Turn On	_	8.5	_	V	
VCC CLAMP	V _{CC} Clamp Voltage	I _{CC} =1mA	14	15.5	_	V	
Standby Current				1	1	I	
I _{ST}			_	130	_	μA	
I _{CC (OPR)}	Operating Current	Before Start Úp Switching Frequency at 4kHz	_	170	_	μA	
Source Driver							
R _{DS(ON)LV}	Low Voltage MOSFET On-State Resistance (Note 7)	-	_	1	_	Ω	
High Voltage and Supe	r-Junction MOSFET	•					
		AL1697-30BAS7-13	-	2.8	3.4		
R _{DS(ON)HV}	Drain-Source On-State Resistance	AL1697-20CS7-13	_	4	5.5	Ω	
		AL1697-40DS7-13	-	1.8	2.5	1	
		AL1697-30BAS7-13	400	_	_	v	
V _{DS}	Drain-Source Breakdown Voltage	AL1697-20CS7-13	600	_	_		
		AL1697-40DS7-13	670	700	_	1	
I _{DSS}	Drain-Source Leakage Current	AL1697-30BAS7-13	_	_	1	1	
		AL1697-20CS7-13	-	-	1	μΑ	
		AL1697-40DS7-13	-	-	1		
RT				•	•		
V_{RT_REF}	Reference Voltage of RT pin	_	-	0.5	-	V	
Current Sense			1				
V _{CS_CLAMP}	CS Clamp Voltage	-	_	1	-	V	
V _{REF}	Internal Current Loop Control Reference	-	0.388	0.4	0.412	V	
ton_min	Minimum t _{ON}	-	-	550	-	ns	
ton_max	Maximum t _{ON}	$R_T=51k\Omega, V_{COMP}=4V$	_	3.6	_	μs	
t _{OFF_MIN}	t _{OFF_MIN} Minimum t _{OFF} (Note 7)		_	4	_	μs	
toff_max	Maximum t _{OFF}	-	-	260	_	μs	
FB							
V _{FB}	Feedback Voltage	_	3.76	4.0	4.24	V	
I _{FB}	Feedback Pin Input Leakage Current	V _{FB} = 2V	-	4	-	μA	
Error Amplifier		1					
G _M	G _M Trans-Conductance	-	-	25	-	μA/V	
ISOURCE	Amplifier Source Current	-	_	10	-	μA	
I _{SINK}	Amplifier Sink Current	_	_	9	-	μA	
Thermal Foldback and	Over Temperature Protection (OTP)						
T _{FOLD}	Foldback Temperature (Note 7)	_	_	+145	_	°C	
_	Shutdown Temperature (Notes 7 & 8)	_	_	+160	_	°C	

7. These parameters, although guaranteed by design, are not tested in production.
 8. The device will latch off when OTP happens, recovered after power cycle and the device won't operate normally at this temperature.

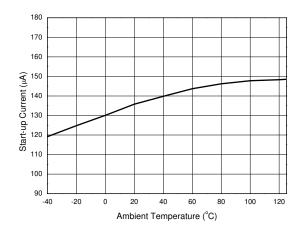


Performance Characteristics (Note 9)

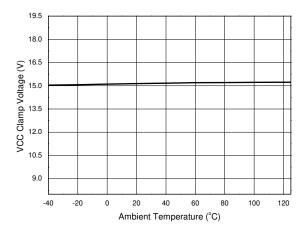
Start-up Voltage vs. Ambient Temperature



Start-up Current vs. Ambient Temperature

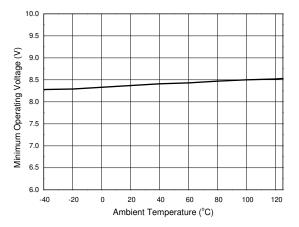


VCC Clamp Voltage vs. Ambient Temperature

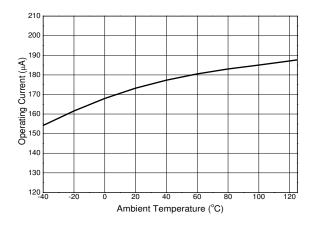


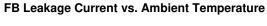
Note: 9. These electrical characteristics are tested under DC condition. The ambient temperature is equal to the junction temperature of the device.

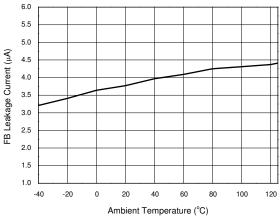
Minimum Operating Voltage vs. Ambient Temperature



Operating Current vs. Ambient Temperature









Functional Description and Application Information

Convertor Operation

The AL1697 is a single stage, single winding, high efficiency, and high power factor LED driver converter for dimmable LED lamp applications. It is available in three internal High-Voltage MOSFET options for 3A /400V, 2A/600V and 4A/670V with Super-Junction which reduce the overall LED driver solution size of the LED lamp. Three different MOSFET options are targeted at optimizing the solutions of different LED chain lengths and powers.

High power factor (PF) is achieved using constant on-time mode; coupled with simple closed loop of constant current control the AL1697 achieves good line and load regulation. The AL1697 adopts source-driver technique to decrease the system operating current. It uses a novel method to detect the t_{OFF} time which results in the removal for the need of an auxiliary winding for (V_{CC} supply and) detecting the t_{OFF} time, which enables the AL1697 to have an extremely low bill material (BOM) solution. The AL1697 operates at boundary conduction mode (BCM) which can ease EMI design.

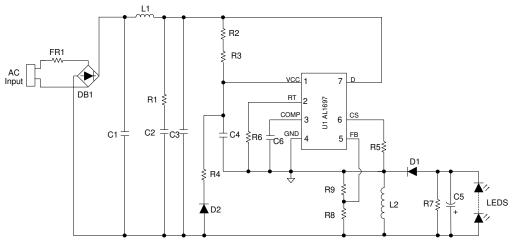


Figure 1. Typical Application Circuit

Start-up and Supply Voltage

Before start-up, the VCC capacitor C4 is charged by the startup resistors (R2, R3) from the high voltage mains. When the start-up voltage is reached, the AL1697 starts switching. During normal operation, the VCC supply is provided by start-up resisters (R2, R3) and the output voltage (V_{OUT}) rectified by one diode (D2). In this way the system can provide VCC supply at low dimming angle.

The AL1697 has an internal VCC clamp voltage (typical 15.5V), which is limited by one internal active Zener diode.

When VCC voltage drops to below the $V_{OPR(MIN)}$, switching is stop. So the device can operate normally when the voltage on VCC pin is between $V_{OPR(MIN)}$ and VCC clamp voltage.

Protections

Under Voltage Lockout (UVLO)

When the voltage on the VCC pin drops to below $V_{OPR(MIN)}$, the IC stops switching. The IC can restart when the voltage on VCC exceeds the startup voltage ($V_{TH(ST)}$).

Leading-Edge Blanking (LEB)

To prevent false detection of the peak current of the inductor, a blanking time following switch-on is designed. When the internal switch turns on, a short current spike can occur because of the capacitive discharge of the voltage over the drain and source. It is disregarded during the LEB time (t_{ON MIN}).

Cycle-by-cycle Over Current Protection (OCP)

The AL1697 has a built-in peak current detector. It triggers when the voltage on CS pin reaches the peak level V_{CS_CLAMP} . The R5 is connected to the CS pin to sense the current of the inductor. The maximum peak current ($I_{PEAK(MAX)}$) of the inductor can be calculated as below:



Functional Description and Application Information (Cont.)

The detection circuit is activated after the LEB time. When the detection circuit sense the CS voltage is higher than 1V, the IC will turn off the switching to limit the output current. It automatically provides protection for the maximum LED current during operation. A propagation delay exists between over current detection and actual source-switch off, so the actual peak current is a little higher than the OCP level set by the R5.

Over-Voltage Protection and Output-Open Protection (OVP)

The output voltage is sensed by the FB pin, which provides an over-voltage protection (OVP) function. When the output is open or large transient happens, the output voltage will exceed the rated value (R8, R9). When the voltage exceeds V_{FB} , the over-voltage is triggered and the IC will discharge V_{CC} . When the V_{CC} is below the UVLO threshold voltage, IC will restart and the V_{CC} capacitor is charged again by start-up resistance. If the over voltage condition still exists, the system will work in hiccup mode.

Output-Short Protection (OSP)

When LED is shorted, the device cannot detect the toFF time, and the device controls the system operation at 4kHz low frequency.

Thermal Foldback Protection (TFP)

AL1697 has a thermal foldback function and adopts self-adaptive control method, which can prevent the system breaking down caused by high temperature. The overheating temperature is set at +145°C typical, when the junction temperature of the IC is higher than +145°C typical, the device will linearly decrease the internal reference voltage to decrease the output current. As a result of this feature, the device can control the system's output power at high ambient temperature, to control the quantity of heat of the system. This enhances the safety of the system at high temperature.

The thermal foldback waveform is shown below:

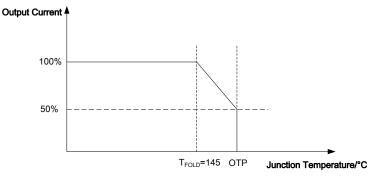


Figure 2. Thermal Foldback Waveform

Over-Temperature Protection (OTP)

The AL1697 has OTP protection function. When the junction temperature reach to +160°C typical, the IC will trigger an over-temperature protection condition which causes the device to shut down and latched. Once OTP has triggered, the system will resume after the system's AC source supply has been reset and power up.

Design Parameters

-

Setting the Current Sense Resistor R5

The AL1697 adopts boundary conduction mode, the output current is calculated as below,

$$I_{O_MEAN} = \frac{1}{\pi} \cdot \int_{0}^{\pi} \frac{1}{2} \cdot I_{PEAK} \cdot \sin(\theta) \cdot \frac{t_{OFF}}{t_{ON} + t_{OFF} + t_{DELAY}} d\theta \qquad (2)$$

Where,

 $I_{\text{PEAK}} \text{ is the peak current of the inductance} \\ t_{\text{ON}} \text{ is the internal MOSFET on time} \\ t_{\text{OFF}} \text{ is the freewheel diode D1 conduction time} \\ t_{\text{DELAY}} \text{ is typical } 0.15 \mu \text{s}$



Functional Description and Application Information (Cont.)

The AL1697 is a closed loop constant current control with the relationship between output current and current sense voltage follows this equation

$$V_{REF} = \frac{1}{\pi} \cdot \int_{0}^{\pi} I_{PEAK} \cdot \sin(\theta) \cdot \mathbf{R5} \cdot \frac{t_{OFF}}{t_{ON} + t_{OFF} + t_{DELAY}} d\theta \qquad (3)$$

Where,

 V_{REF} is the internal reference, typical 0.4V. R5 is the current sense resistor

So we can get the output current equation as below,

$$I_{O_{-MEAN}} = \frac{1}{2} \cdot \frac{V_{REF}}{R5} \tag{4}$$

Inductance Selection (L2)

In buck-boost structure, the peak current of the inductance can be calculated as below

$$I_{PEAK} = \frac{\pi \cdot V_{REF}}{R5 \cdot \int_{0}^{\pi} \sin(\theta) \cdot \frac{\sqrt{2} \cdot V_{IN_RMS} \cdot \sin(\theta)}{\sqrt{2} \cdot V_{IN_RMS} \cdot \sin(\theta) + Vo} d\theta}$$
(5)

Where,

 V_{IN_RMS} is the input voltage's RMS value V_O is the system output voltage

The AL1697 controls the system operating at boundary conduction mode which results in its operating frequency not being constant. To set the minimum switching frequency f_{MIN} at the crest of the minimum AC input.

$$L2 = \frac{\sqrt{2}V_{IN_RMS} \cdot V_O}{I_{PEAK} \cdot (\sqrt{2}V_{IN_RMS} + V_O) \cdot f_{MIN}}$$
(6)

According to the Faraday's Law, the winding number of the inductance can be calculated by:

$$N_{L2} = \frac{L2 \cdot I_{PEAK}}{A_e \cdot B_m} \tag{7}$$

Where,

 A_e is the core effective area.

 B_m is the maximum magnetic flux density.

t_{ON_MAX} Setting

In order to get a good dimmer compatibility and a good dimming depth, the device sets a t_{ON_MAX} by one external resistor R_T (R6). And the t_{ON_MAX} time has the below equation:

$$t_{ON_MAX} = \frac{3.3 \cdot C_{REF}}{\frac{V_{RT_REF}}{10 \cdot \text{R6}} + 0.5uA}$$
(8)

Where

 V_{RT_REF} is the internal RT pin 0.5V's reference. C_{REF} is the internal 1.5pF capacitor.



Functional Description and Application Information (Cont.)

Dimming Control

AL1697 is a closed loop control device, the dimming function is realized by t_{ON_MAX} limited when dimmer is connected in. When the dimmer is at the largest conduction angle, the device still has the adjustability to control the output current constant before COMP voltage is adjusted to the maximum 4V, so for most of the dimmer, the output current is almost the same with the no dimmer condition at the largest conduction angle. If the conduction angle is decreased, the COMP pin voltage will continue to increase quickly till to the maximum level (typical 4V), the device will output t_{ON_MAX} to limit system's output current. The t_{ON_MAX} is set by RT pin connected with one resistor, so the dimming depth can be adjusted by RT resistor (R6).

Before the AL1697 enters t_{ON_MAX} mode, the AL1697 keeps the output current constant the same as no dimmer condition. When enter t_{ON_MAX} mode, we can get the following equation:

$$I_{PEAK_DIM} = \frac{V_{IN_RMS} \cdot Sin(\theta) \cdot t_{ON_MAX}}{L2}$$
(9)

From the buck-boost output current equation, we can get the output current when dimming:

Where, α is the dimmer conduction angle.

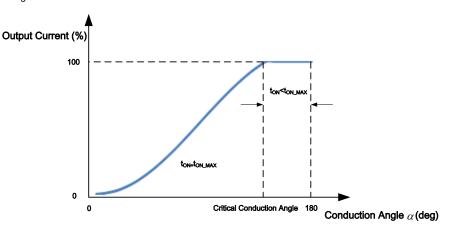
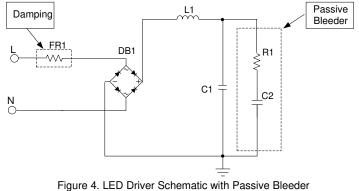


Figure 3. Dimming Curve

Dimmer Compatibility

Passive Bleeder Design

The passive bleeder is designed to supply latching and holding current to eliminate dimmer misfire and flicker.



9 of 13 www.diodes.com



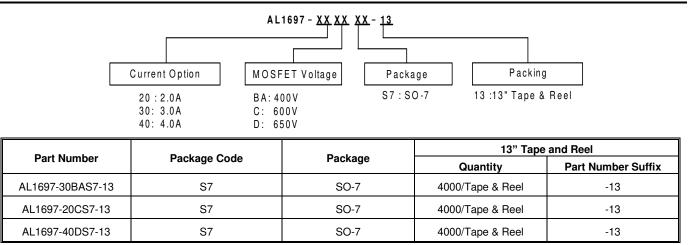
Functional Description and Application information (Cont.)

The passive bleeder includes a capacitor (C2, in hundreds of nF) to provide latching current. A resistor (R1) is necessary to dampen the current spike. Because a large C2 will affect the PF, THD and efficiency, the value of the capacitor (C2) should be selected accordingly. Generally, 100nF/400V to 330nF/400V is recommended. R1 is used to limit the latching current, If R1 is too large, the latching current is not enough and the TRIAC dimmer will misfire causing LED flicker. If R1 is too small, it will result in greater power dissipation. Generally speaking, a 200Ω to $2K\Omega$ resistor is selected for R1.

Passive Damping Design

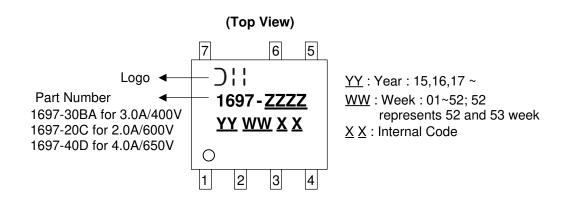
FR-1 is the damper for reducing the spike current caused by quick charging of C2 at firing. In General, FR-1 is selected from 20Ω to 100Ω for low line like $120V_{AC}$ application, and 51Ω to 200Ω for high line like $230V_{AC}$ application.

Ordering Information



Marking Information

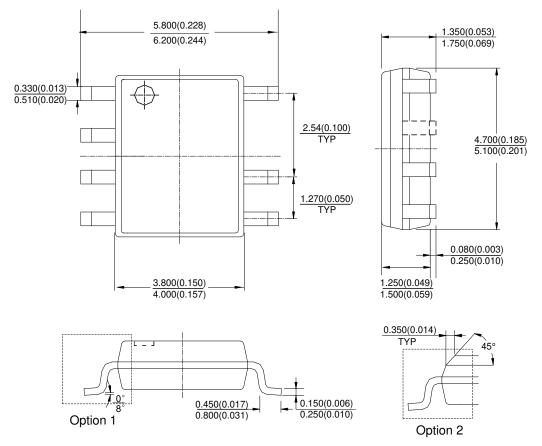
(1) SO-7





Package Outline Dimensions (All dimensions in mm.)

(1) Package Type: SO-7

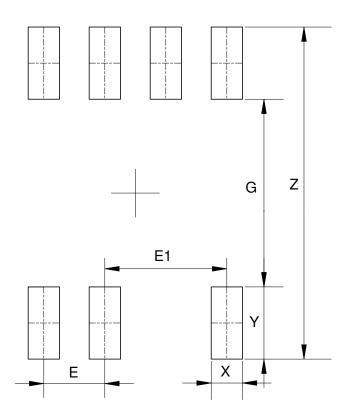


Note: Eject hole, oriented hole and mold mark is optional.



Suggested Pad Layout

(1) Package Type: SO-7



Dimensions	Z	G	Х	Y	E	E1
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050	2.540/0.100



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2017, Diodes Incorporated

www.diodes.com