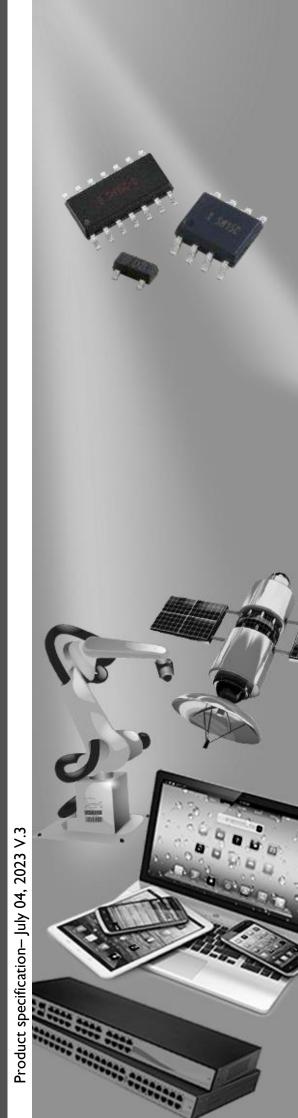


DATA SHEET

PROTECTION DEVICES
INDUSTRIAL / CONSUMER
UAD8C05L01

RoHS compliant & Halogen free





Electrostatic Discharged Protection Devices (ESD) Data Sheet

Description

The UAD8C05L01 includes back-to-back TVS diodes fabricated in a proprietary silicon avalanche technology to provide protection for electronic equipment that may experience destructive electrostatic discharge (ESD). These robust diodes can safely absorb repetitive ESD strikes up to the maximum level specified in the IEC61000-4-2 international standard without performance degradation.

The back-to-back configuration provides symmetrical ESD protection for data lines when AC signals are present.

Features

- IEC61000-4-2 ESD 20KV Air, 20KV contact compliance
- SOD882 surface mount package
- Working voltage: 5V
- Low leakage current
- Low operating and clamping voltages
- Lead Free/RoHS compliant
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: B1

Applications

- USB 3.0/USB 2.0
- MHL/MIPI/MDDI
- HDMI, Video Port, eSATA
- Set Top Boxes, Game Consoles
- Smart Phones
- External Storage
- Ultrabooks, Notebooks
- Tablets, eReaders

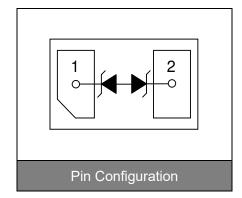
Maximum Ratings

Rating	Symbol	Value	Unit	
ESD voltage (Contact discharge)	V	±20	147	
ESD voltage (Air discharge)	V_{ESD}	±20	kV	
Storage & operating temperature range	T _{STG} ,T _J	-55~+150	$^{\circ}$	



Contact: ±20kV Air: ±20kV





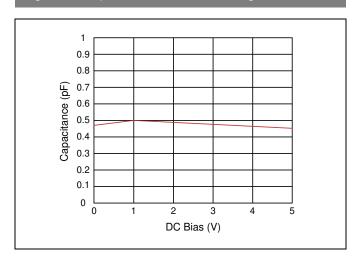


Electrical Characteristics (TJ=25°C)

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Reverse stand-off voltage	V_{RWM}				5	V
Reverse breakdown voltage	V_{BR}	I _{BR} =1mA	7			V
Reverse leakage current	I _R	V _R =5V			1	μΑ
Clamping voltage (tp=8/20µs)	Vc	I _{PP} =1A		13		V
Clamping voltage (tp=8/20µs)	V _C	I _{PP} =3A		20		V
Peak pulse current (tp=8/20µs)	I _{PP}				3	Α
Off state junction capacitance	Сл	0Vdc,f=1MHz		0.4		pF

Typical Characteristics Curves

Figure 1. Capacitance vs. Bias Voltage



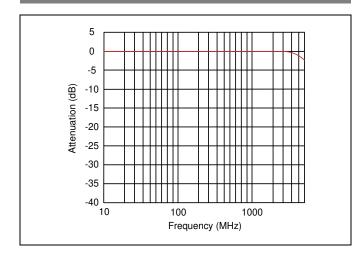


Figure 2. Insertion Loss (S21) I/O to GND

Figure 3. Pulse Waveform

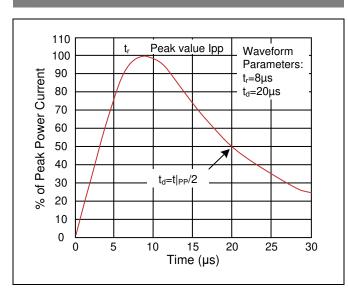
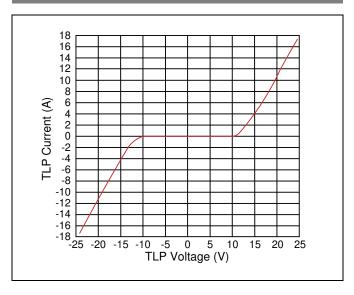
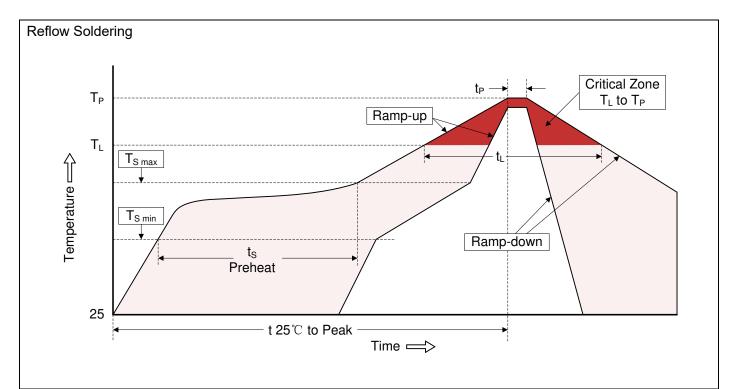


Figure 4. Transmission Line Pulsing (TLP) Plot



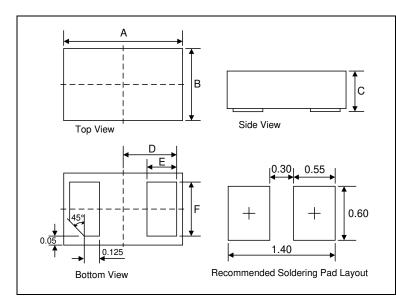
Recommended Soldering Conditions



Recommended Conditions

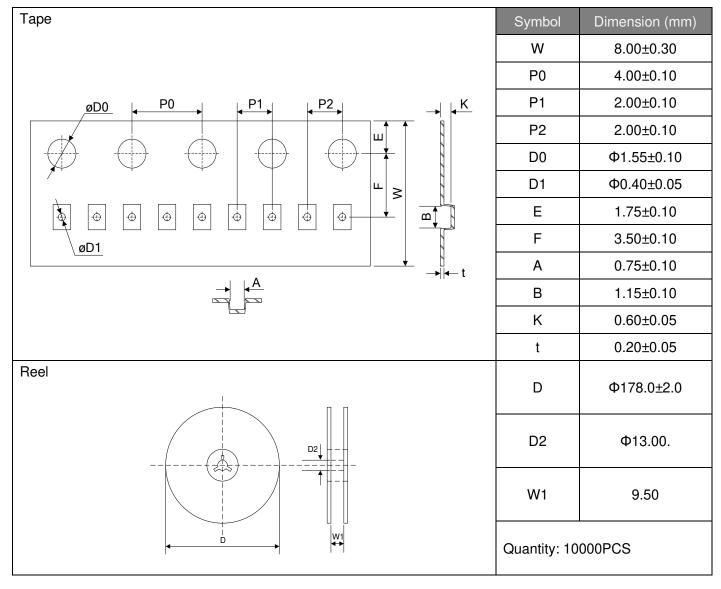
Profile Feature	Pb-Free Assembly
Average ramp-up rate (T _L to T _P)	3°C/second max.
Preheat -Temperature Min (T _{S min})	150℃
-Temperature Max (T _{S max}) -Time (min to max) (t _S)	200°C 60-180 seconds
T _{S max} to T _L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T_L) -Time (t_L)	217°C 60-150 seconds
Peak Temperature (T _P)	260°C
Time within 5°C of actual Peak Temperature (t _P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (SOD882)



	Dimension (mm)				
Symbol	Millimeters		Incl	hes	
	Min.	Max.	Min.	Max.	
Α	0.95	1.05	0.037	0.041	
В	0.55	0.65	0.022	0.026	
С	0.32	0.55	0.013	0.022	
D	0.45		0.018		
E	0.20	0.30	0.008	0.012	
F	0.45	0.55	0.018	0.022	

Packaging





Circuit Protection Components

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