

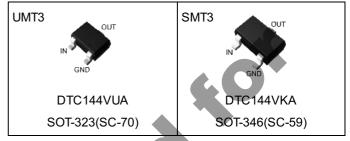
NPN 100mA 50V Digital Transistors (Bias Resistor Built-in Transistors)

Parameter	Value
V _{CC}	50V
I _{C(MAX.)}	100mA
R ₁	47kΩ
R_2	10kΩ

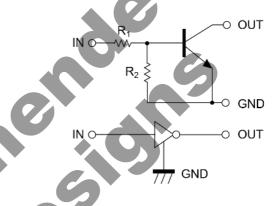
Features

- 1) Built-In Biasing Resistors
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary PNP Types: DTA144V series
- 6) Lead Free/RoHS Compliant.

Outline



•Inner circuit



Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit

Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTC144VUA	UMT3	2021	T106	180	8	3000	166
DTC144VKA	SMT3	2928	T146	180	8	3000	E66

● Absolute maximum ratings (T_a = 25°C)

Parameter			Values	Unit	
Supply voltage			V _{CC} 50		
Input voltage			-10 to 40	V	
Output current			30	mA	
Collector current			100	mA	
Dawar diasinatian	DTC144VUA	P _D *2	200	mW	
Power dissipation	DTC144VKA	P _D -	200		
Junction temperature		T _j	150	°C	
Range of storage temperature		T _{stg}	-55 to +150	°C	

● Electrical characteristics (T_a = 25°C)

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Parameter	Symbol	Conditions	Values			Unit
r al al nietei	Symbol	Conditions	Min.	Тур.	Max.	Offic
Input voltage	V _{I(off)}	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	1	V
Input voltage	V _{I(on)}	$V_0 = 0.3V$, $I_0 = 2mA$	6	-	-	V
Output voltage	V _{O(on)}	$I_{O}/I_{I} = 10mA/0.5mA$	-	0.1	0.3	V
Input current	I _I	V ₁ = 5V	ı	-	0.16	mA
Output current	I _{O(off)}	$V_{CC} = 50V, V_{I} = 0V$	-	-	0.5	μA
DC current gain	G _I	$V_{O} = 5V, I_{O} = 5mA$	33	-	ı	-
Input resistance	R₁	-	32.9	47	61.1	kΩ
Resistance ratio	R ₂ /R ₁	-	0.17	0.21	0.26	-
Transition frequency	f _T *1	V _{CE} = 10V, I _E = -5mA, f = 100MHz	-	250	-	MHz

^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a reference footprint

● Electrical characteristic curves (T_a =25°C)

Fig.1 Input voltage vs. output current (ON characteristics)

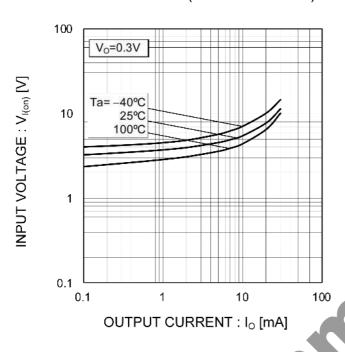


Fig.2 Output current vs. input voltage (OFF characteristics)

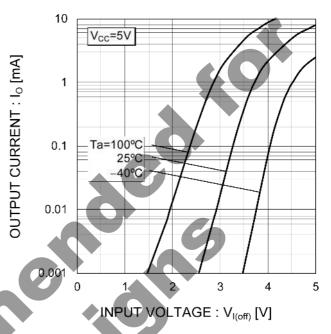


Fig.3 Output current vs. output voltage

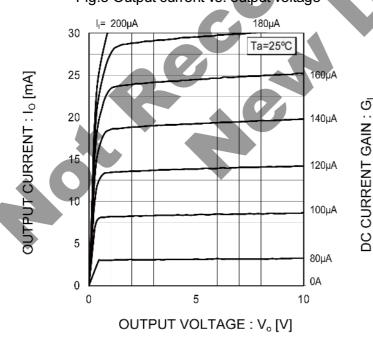
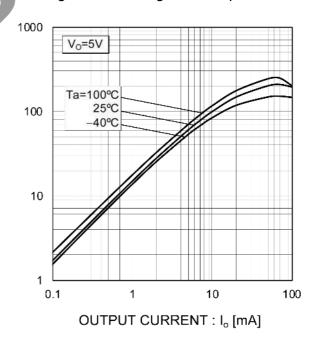
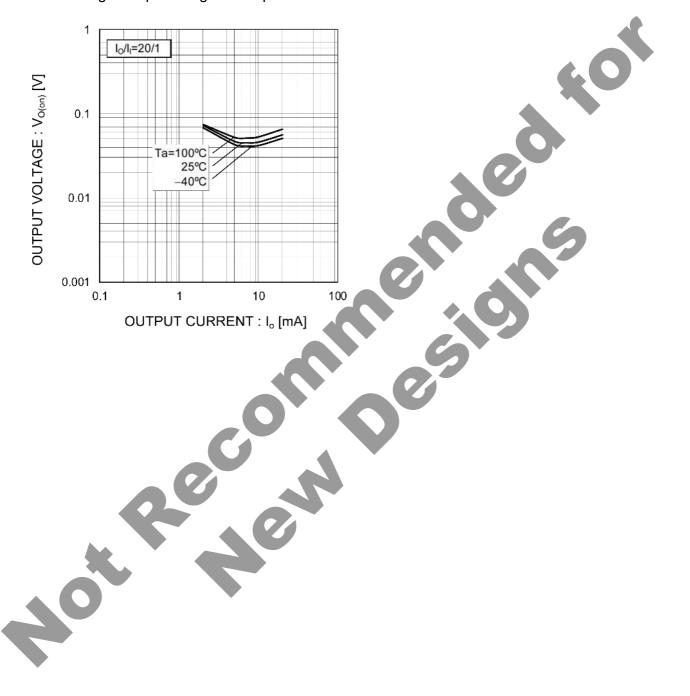


Fig.4 DC current gain vs. output current



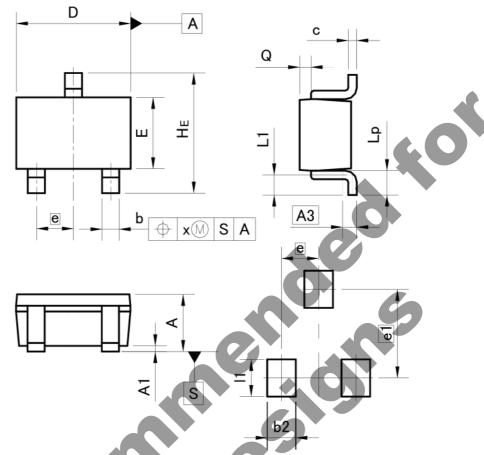
● Electrical characteristic curves (T_a =25°C)

Fig.5 Output voltage vs. output current



Dimensions

UMT3



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	0.80	1.00	0.031	0.039	
A1	0.00	0.10	0.000	0.004	
A3	0.	25	0.0	10	
b	0.15	0.30	0.006	0.012	
С	0.10	0.20	0.004	0.008	
D	1.90	2.10	0.075	0.083	
E	1.15	1.35	0.045	0.053	
е	0.65		0.026		
HE	2.00	2.20	0.079	0.087	
L1	0.20	0.50	0.008	0.020	
Lp	0.25	0.55	0.010	0.022	
Q	0.10	0.30	0.004	0.012	
×	-	0.10	_	0.004	

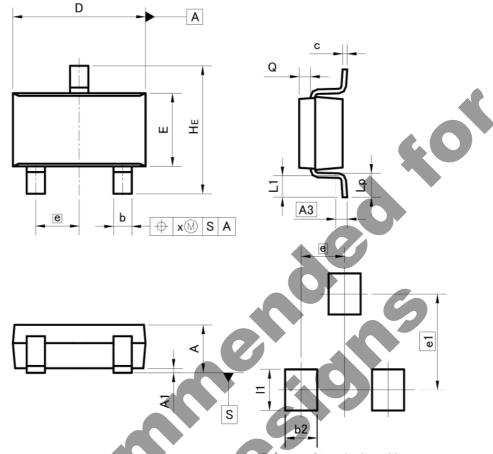
DIM	MILIMETERS		INCHES		
	MIN	MAX	MIN	MAX	
b2	_	0.50		0.020	
e1	1.55		0.0	061	
11	-	0.65	_	0.026	

Dimension in mm/inches



Dimensions

SMT3



Pattern of terminal position areas
[Not a recommended pattern of soldering pads]

DIM	MILIME	TERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
A	1.00	1.30	0.039	0.051	
A1	0.00	0.10	0.000	0.004	
A3	0.2	5	0.0	010	
b	0.35	0.50	0.014	0.020	
С	0.09	0.25	0.004	0.010	
D	2.80	3.00	0.110	0.118	
E	1.50	1.80	0.059	0.071	
е	0.9	95	0.037		
HE	2.60	3.00	0.102	0.118	
L1	0.30	0.60	0.012	0.024	
Lp	0.40	0.70	0.016	0.028	
Q	0.20	0.30	0.008	0.012	
x	227	0.10	7	0.004	
У	45	0.10	1 <u>111</u>	0.004	
	MILIMETERS		INC	HES	
DIM	MIN	MAX	MIN	MAX	
b2	_	0.60	14	0.024	
e1	2.10		0.0	083	
	2.10			-	

Dimension in mm/inches

e1



0.035

0.90

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