

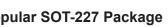
APL502J

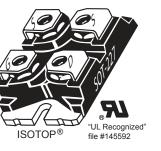
500V **52A** 0.090Ω

MOSFET FAR

Linear Mosfets are optimized for applications operating in the Linear region where concurrent high voltage and high current can occur at near DC conditions (>100 msec).

- Higher FBSOA
- Popular SOT-227 Package
- Higher Power Dissipation SOA Rated
- RoHS Compliant





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MAXIMUM RATINGS

All Ratings: $T_{C} = 25^{\circ}C$ unless otherwise specified.

Symbol	Parameter	APL502J	UNIT			
V _{DSS}	Drain-Source Voltage	500	Volts			
I _D	Continuous Drain Current @ T _C = 25°C	52	Amps			
I _{DM}	Pulsed Drain Current ①	208				
V _{GS}	Gate-Source Voltage Continuous	±30	Volts			
V _{GSM}	Gate-Source Voltage Transient	±40	VOIIS			
P	Total Power Dissipation @ T _C = 25°C	568	Watts			
P _D	Linear Derating Factor	4.55	W/°C			
T _J ,T _{STG}	Operating and Storage Junction Temperature Range	-55 to 150				
Τ _L	Lead Temperature: 0.063" from Case for 10 Sec.	300				
I _{AR}	Avalanche Current $^{igl(1)}$ (Repetitive and Non-Repetitive)	52	Amps			
E _{AR}	Repetitive Avalanche Energy $^{igl(1)}$	50	mJ			
E _{AS}	Single Pulse Avalanche Energy ④	3000	1115			
STATIC ELECTRICAL CHARACTERISTICS						

Symbol	Characteristic / Test Conditions / Part Number	MIN	TYP	MAX	UNIT
BV _{DSS}	Drain-Source Breakdown Voltage (V_{GS} = 0V, I_{D} = 250 μ A)	500			Volts
I _{D(ON)}	On State Drain Current $^{(2)}(V_{DS} > I_{D}(ON) \times R_{DS}(ON) Max, V_{GS} = 15V)$	52			Amps
R _{DS(ON)}	Drain-Source On-State Resistance ^② (V _{GS} = 15V, 26A)			0.09	Ohms
I _{DSS}	Zero Gate Voltage Drain Current (V_{DS} = 500V, V_{GS} = 0V)			25	μA
	Zero Gate Voltage Drain Current (V_{DS} = 400V, V_{GS} = 0V, T_{C} = 125°C)			250	
I _{GSS}	Gate-Source Leakage Current ($V_{GS} = \pm 30V, V_{DS} = 0V$)			±100	nA
V _{GS(TH)}	Gate Threshold Voltage $(V_{DS} = V_{GS}, I_{D} = 2.5 \text{mA})$	2		4	Volts

CAUTION: These Devices are Sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed.

DYNAMIC CHARACTERISTICS

Symbol	Characteristic	Test Conditions	MIN	TYP	MAX	UNIT
C _{iss}	Input Capacitance	V _{GS} = 0V		7600	9000	
C _{oss}	Output Capacitance	V _{DS} = 25V		1280	1810	pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz		620	930	
t _{d(on)}	Turn-on Delay Time	V _{GS} = 15V		13	26	
t _r	Rise Time	$V_{DD} = 0.5 V_{DSS}$		24	48	ns
t _{d(off)}	Turn-off Delay Time	I _D = 52A @ 25°C		58	87	
t _f	Fall Time	R _G = 0.6Ω		14	17	

THERMAL CHARACTERISTICS

Symbol	Characteristic	MIN	ТҮР	MAX	UNIT
R _{ejc}	Junction to Case			.22	°C/W
V _{Isolation}	RMS Voltage (50-60 Hz Sinusoidal Waveform From Terminals to Mounting Base for 1 Min.)	2500			Volts
W _T	Package Weight		1.03		oz
			29.2		g
Torque	Maximum Torque for Device Mounting Screws and Electrical Terminations.			10	lb•in
				1.1	N•m
SOA1	Safe Operating Area V_{DS} = 400 V, I _{DS} = 0.75A, t = 20 sec., T _C = 60°C	300			Watts

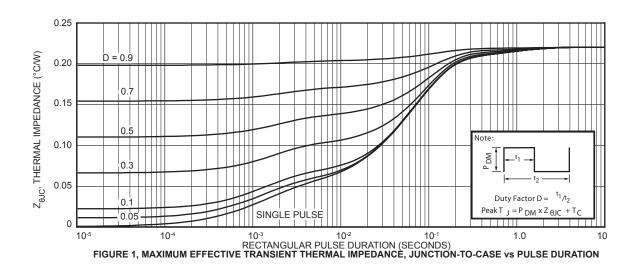
 $^{\textcircled{1}}$ Repetitive Rating: Pulse width limited by maximum junction temperature.

 $\stackrel{(3)}{=}$ See MIL-STD-750 Method 3471 $\stackrel{(4)}{=}$ Starting T_i = +25°C, L = 2.22mH, R_G = 25 Ω , Peak I_L = 52A

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⁽²⁾ Pulse Test: Pulse width < 380 μ S, Duty Cycle < 2%

Microsemi reserves the right to change, without notice, the specifications and information contained herein.



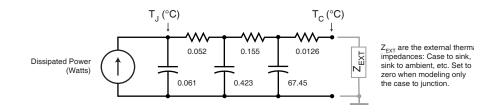
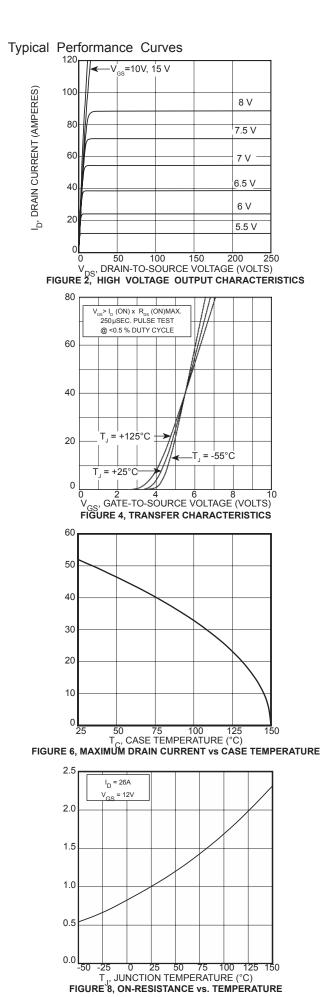
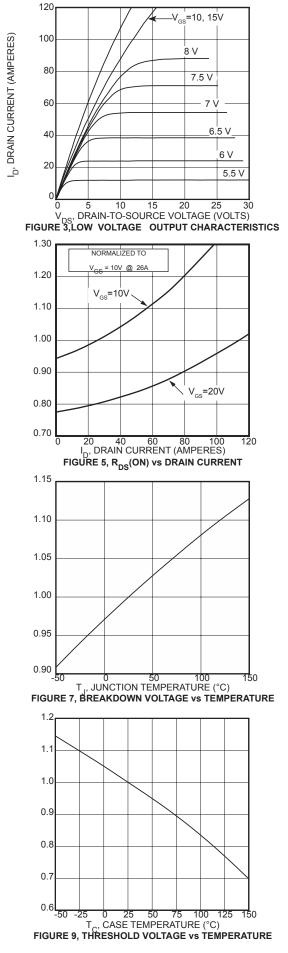
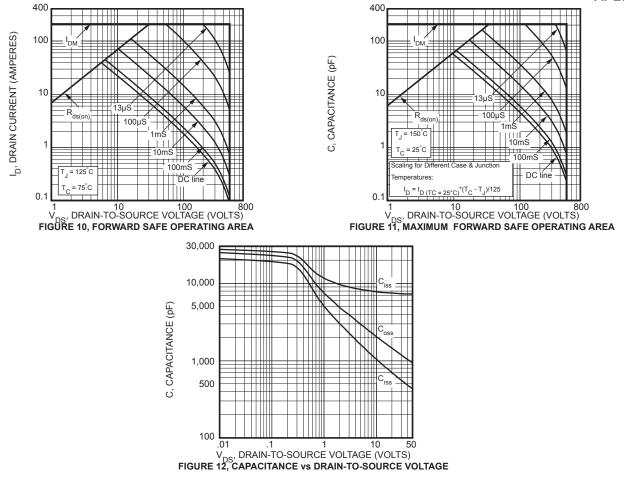


FIGURE 1a, TRANSIENT THERMAL IMPEDANCE MODEL

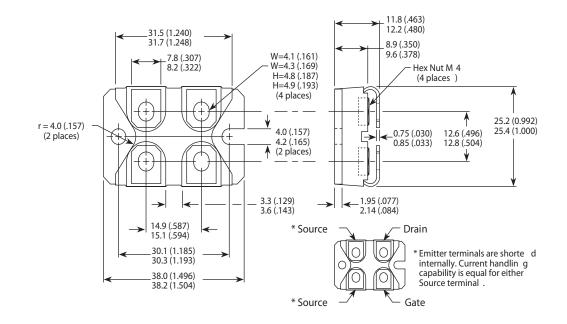
L







SOT-227 (ISOTOP®) Package Outline



Dimensions in Millimeters

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