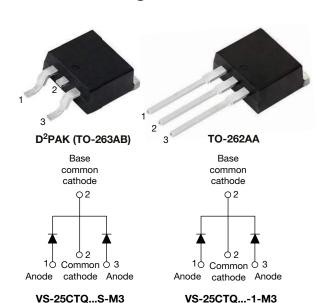
## VS-25CTQ...S-M3, VS-25CTQ...-1-M3 Series

Vishay Semiconductors

HALOGEN

FREE

# High Performance Schottky Rectifier, 2 x 15 A



PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 15 A					
$V_{R}$	35 V, 40 V, 45 V					
V <sub>F</sub> at I <sub>F</sub>	0.50 V					
I <sub>RM</sub> typ.	70 mA at 125 °C					
T <sub>J</sub> max.	150 °C					
E <sub>AS</sub>	20 mJ					
Package	D <sup>2</sup> PAK (TO-263AB), TO-262AA					
Circuit configuration	Common cathode					

#### **FEATURES**

- 150 °C T<sub>J</sub> operation
- · Center tap configuration
- · Very low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION**

The VS-25CTQ... center tap Schottky rectifier series has been optimized for very low forward voltage drop, with moderate leakage. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS								
SYMBOL	CHARACTERISTICS	VALUES	UNITS					
I <sub>F(AV)</sub>	Rectangular waveform	30	Α					
V <sub>RRM</sub>	Range	35 to 45	V					
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	990	Α					
V <sub>F</sub>	15 A <sub>pk</sub> , T <sub>J</sub> = 125 °C (per leg)	0.50	V					
$T_J$	Range	-55 to +150	°C					

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-25CTQ035S-M3 VS-25CTQ035-1-M3	VS-25CTQ040S-M3 VS-25CTQ040-1-M3	VS-25CTQ045S-M3 VS-25CTQ045-1-M3	UNITS	
Maximum DC reverse voltage	$V_R$	35	40	45	V	
Maximum working peak reverse voltage	$V_{RWM}$	33	40	45	V	



# VS-25CTQ...S-M3, VS-25CTQ...-1-M3 Series

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ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDI	TEST CONDITIONS					
Maximum average forward current See fig. 5	I <sub>F(AV)</sub>	50 % duty cycle at T <sub>C</sub> = 102 °C	30					
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse  10 ms sine or 6 ms rect. pulse  Following any rated load condition and with rated V <sub>RRM</sub> applied		990	Α			
non-repetitive surge current per leg See fig. 7	I <sub>FSM</sub>			250				
Non-repetitive avalanche energy per leg	E <sub>AS</sub>	$T_J = 25  ^{\circ}\text{C},  I_{AS} = 3  \text{A},  L = 4.40  \text{mH}$		20	mJ			
Repetitive avalanche current per leg	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>B</sub> typical		3	Α			

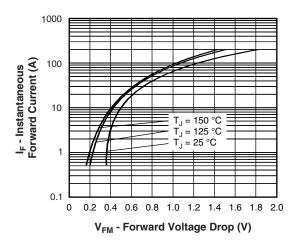
ELECTRICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS					
		15 A	T <sub>.1</sub> = 25 °C	0.56				
Maximum forward voltage drop per leg See fig. 1	V <sub>FM</sub> <sup>(1)</sup>	30 A	- Ij=25 C	0.71	V			
	VFM (1)	15 A	T 105 %C	0.50				
		30 A	- T <sub>J</sub> = 125 °C	0.64				
Maximum reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	1.75	mΛ			
per leg	IRM (")	T <sub>J</sub> = 125 °C	V <sub>R</sub> = nateu v <sub>R</sub>	110	mA mA			
Typical reverse leakage current	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 125 °C	$T_J = 125 ^{\circ}\text{C}$ $V_R = \text{Rated } V_R$		mA			
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal range 100 kHz to 1 MHz), 25 °C		900	pF			
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 m	8.0	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	10 000	V/µs				

#### Note

 $<sup>^{(1)}\,</sup>$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		-55 to 150	°C			
Maximum thermal resistance junction to case per leg	Maximum thermal resistance, junction to case per leg		DC operation See fig. 4	3.25				
Maximum thermal resistance, junction to case per package		R <sub>thJC</sub>	DC operation	1.63	°C/W			
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.50				
Approximate weight				2	g			
Approximate weight				0.07	oz.			
Mounting torque	minimum			6 (5)	kgf · cm			
Mounting torque -	maximum			12 (10)	(lbf · in)			
Moulting daying			Case style D <sup>2</sup> PAK (TO-263AB)	25CTQ 25CTQ 25CTQ	040S			
warking device	Marking device		Case style TO-262AA	25CTQ( 25CTQ( 25CTQ(	040-1			

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1000 I<sub>R</sub> - Reverse Current (mA) 100  $T_J = 125$  °C 10 T<sub>1</sub> = 100 °C T<sub>.1</sub> = 75 °C 0.1  $T_J = 50 \, ^{\circ}C$ 0.01 = 25 °C 0.001 5 25 30 40 45 10 15 20 35 V<sub>R</sub> - Reverse Voltage (V)

Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

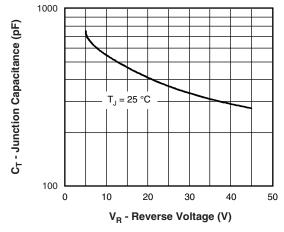


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

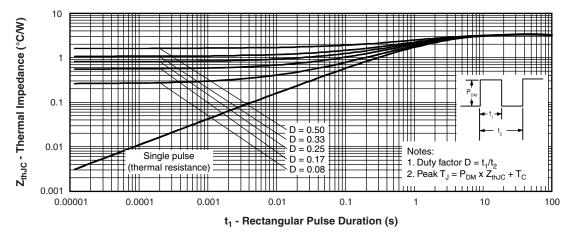


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics (Per Leg)

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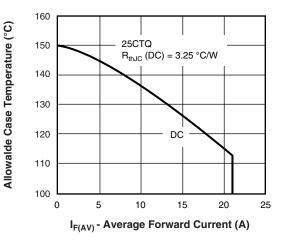


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

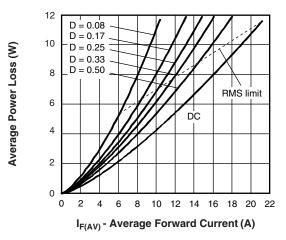


Fig. 6 - Forward Power Loss Characteristics (Per Leg)

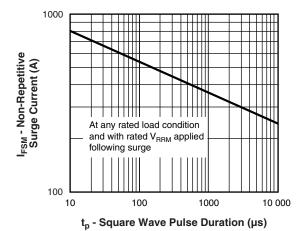


Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

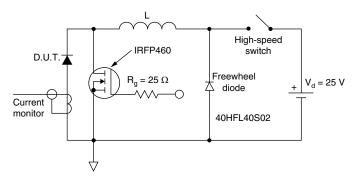


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

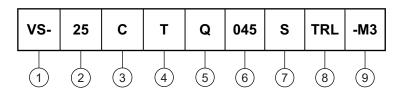
<sup>1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}$ ;  $Pd = forward power loss = I_{F(AV)} \times V_{FM} at (I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R (1 - D)$ ;  $I_R$  at  $V_{R1} = 80 \%$  rated  $V_R$ 

# VS-25CTQ...S-M3, VS-25CTQ...-1-M3 Series

## Vishay Semiconductors

#### **ORDERING INFORMATION TABLE**

**Device code** 



Vishay Semiconductors product

2 - Current rating (25 A)

**3** - Circuit configuration: C = common cathode

**4** - T = TO-220

5 - Schottky "Q" series

9716S 035 = 35 V 040 = 40 V

- Voltage ratings

045 = 45 V

•  $S = D^2PAK (TO-263AB)$ 

• -1 = TO-262AA

None = tube

8

• TRL = tape and reel (left oriented - for D<sup>2</sup>PAK only)

• TRR = tape and reel (right oriented - for D<sup>2</sup>PAK only)

9 - -M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION							
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION					
VS-25CTQ035S-M3	50	Antistatic plastic tubes					
VS-25CTQ035STRL-M3	800	13" diameter plastic tape and reel					
VS-25CTQ035STRR-M3	800	13" diameter plastic tape and reel					
VS-25CTQ040S-M3	50	Antistatic plastic tubes					
VS-25CTQ040STRL-M3	800	13" diameter plastic tape and reel					
VS-25CTQ040STRR-M3	800	13" diameter plastic tape and reel					
VS-25CTQ045S-M3	50	Antistatic plastic tubes					
VS-25CTQ045STRL-M3	800	13" diameter plastic tape and reel					
VS-25CTQ045STRR-M3	800	13" diameter plastic tape and reel					

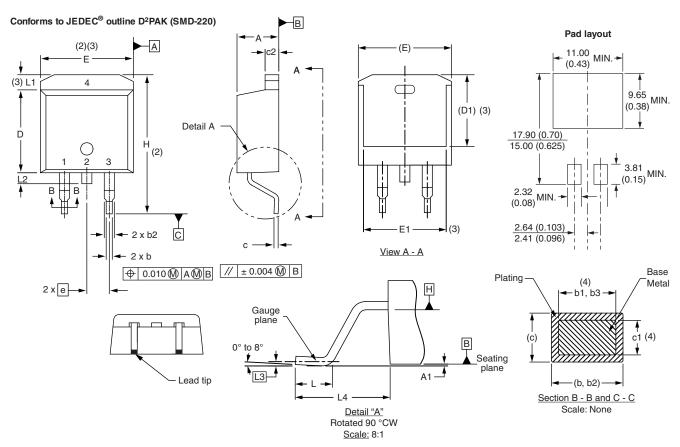
LINKS TO RELATED DOCUMENTS							
Dimensions	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?96164					
Differsions	TO-262AA	www.vishay.com/doc?96165					
Dout moving information	D <sup>2</sup> PAK (TO-263AB)	www.vishay.com/doc?95444					
Part marking information	TO-262AA	www.vishay.com/doc?95443					
Packaging information		www.vishay.com/doc?96424					
SPICE model		www.vishay.com/doc?95285					



## Vishay Semiconductors

### D<sup>2</sup>PAK

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		INCHES		NOTES	S SYMBOL		MILLIM	ETERS	INC	HES	NOTES
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOIES	NOIES	STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			Е	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	) BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

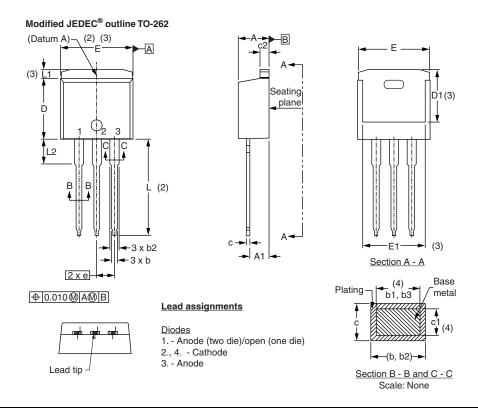
#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch
- (7) Outline conforms to JEDEC® outline TO-263AB

## Vishay Semiconductors

### **TO-262**

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	CHES	NOTES
STIVIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	2.03	3.02	0.080	0.119	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.10	0 BSC	
L	13.46	14.10	0.530	0.555	
L1	-	1.65	-	0.065	3
L2	3.36	3.71	0.132	0.146	

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- 5) Controlling dimension: inches
- (6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum), D1 (minimum) and L2 where dimensions derived the actual package outline

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