# APT15D120BG

# Datasheet Ultrafast Soft Recovery Rectifier Diode

Final October 2018





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## **1 Revision History**

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

## 1.1 Revision C

Revision C was published in October 2018. The new template and format was applied. The following is a summary of the changes in revision C of this document.

- Product image was updated.
- Product features were updated. For information, see Product Overview (see page 2).
- The operating and storage temperature range was changed from 150 °C to 175 °C, see Absolute Maximum Ratings (see page 3).
- The lead thickness in the package outline drawing was updated. For more information, see Package Outline Drawing (see page 8).

### 1.2 Revision B

Revision B was published in November 2009. RoHS (G) designation was added to the datasheet title.

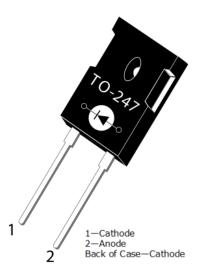
### 1.3 Revision A

Revision A was published in August 2004. It is the first publication of this document.



# 2 Product Overview

This section outlines the product overview for the APT15D120BG device.



### 2.1 Features

The following are key features of the APT15D120BG device.

- Ultrafast recovery times
- Soft recovery characteristics
- Low forward voltage
- Low leakage current
- RoHS compliant

## 2.2 Benefits

The following are benefits of the APT15D120BG device.

- Low switching losses
- Low noise (EMI) switching
- Cooler operation
- Higher reliability systems
- Increased system power density

### 2.3 Applications

The APT15D120BG device is designed for the following applications.

- Power factor correction (PFC)
- Anti-parallel diode
  - Switchmode power supply
- InvertersFreewheeling diode
  - Motor controllers
  - Converters
  - Inverters
- Snubber diode



# 3 Electrical Specifications

This section shows the electrical specifications for the APT15D120BG device.

### 3.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the APT15D120BG device.

All ratings: Tc = 25 °C unless otherwise specified.

#### **Table 1 • Absolute Maximum Ratings**

Symbol	Parameter	Ratings	Unit
VR	Maximum DC reverse voltage	1200	V
Vrrm	Maximum peak repetitive reverse voltage		
VRWM	Maximum working peak reverse voltage		
IF(AV)	Maximum average forward current (Tc = 98 °C, duty cycle = 0.5)	15	А
F(RMS)	RMS forward current	24	_
IFSM	Non-repetitive forward surge current (T <sub>J</sub> = 45 °C, 8.3 ms)	110	
Tı , Tstg	Operating and storage temperature range	–55 to 175	°C
Tι	Lead temperature for 10 seconds	300	

The following table shows the thermal and mechanical characteristics of the APT15D120BG device.

#### Table 2 • Thermal and Mechanical Characteristics

Symbol	Characteristic/Test Conditions	Min	Тур	Max	Unit
Rejc	Junction-to-case thermal resistance			1.18	°C/W
Reja	Junction-to-ambient thermal resistance			40	C/W
Wt	Package weight		0.22		OZ
			6.2		g
	Maximum mounting torque, 6-32 or M3 screw			10	lbf-in
				1.1	N-m

#### **3.2** Electrical Performance

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The following table shows the static characteristics of the APT15D120BG device.

#### Table 3 • Static Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
M	Forward voltage	IF = 15 A		2.0	2.5	- V
Vf		IF = 30 A		2.3		- V
		IF = 15 A, TJ = 125 °C		1.8		_
Irm	Maximum reverse leakage current	$V_R = V_R$ rated			250	μΑ
		V <sub>R</sub> = V <sub>R</sub> rated, T <sub>J</sub> = 125 °C			500	-
Cj	Junction capacitance	V <sub>R</sub> = 200 V		17		pF



### 3.3 Dynamic Characteristics

The following table shows the dynamic characteristics of the APT15D120BG device.

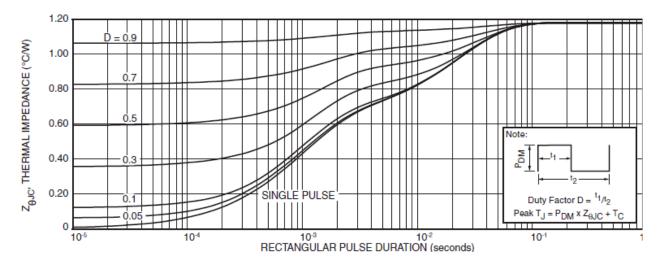
#### Table 4 • Dynamic Characteristics

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
trr	Reverse recovery time	I⊧ = 1 A di⊧/dt = −100 A/μs		32		ns
		V <sub>R</sub> = 30 V T <sub>J</sub> = 25 °C				
trr	Reverse recovery time	$I_{F} = 15 \text{ A}$ diF/dt = -200 A/µs $V_{R} = 800 \text{ V}$ Tc = 25 °C		260		ns
Qrr	Reverse recovery charge			480		nC
Irrm	Maximum reverse recovery current			4		А
trr	Reverse recovery time	$I_{F} = 15A$ $di_{F}/dt = -200 \text{ A}/\mu s$ $V_{R} = 800 \text{ V}$ $T_{C} = 125 \text{ °C}$		370		ns
Qrr	Reverse recovery charge			1300		nC
Irrm	Maximum reverse recovery current			9		А
trr	Reverse recovery time	I <sub>F</sub> = 15 A di <sub>F</sub> /dt = -1000 A/μs V <sub>R</sub> = 800 V T <sub>c</sub> = 125 °C		140		ns
Qrr	Reverse recovery charge			2000		nC
Irrm	Maximum reverse recovery current			28		А

## 3.4 Typical Performance Curves

This section shows the typical performance curves for the APT15D120BG device.







# Figure 2 • Forward Current vs. Anode-to-Cathode Voltage (V)

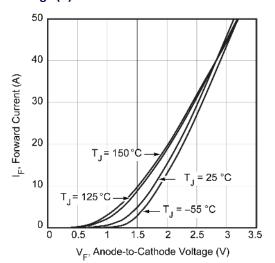
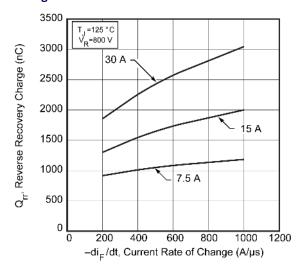


Figure 4 • Reverse Recovery Charge vs. Current Rate of Change



# Figure 3 • Reverse Recovery Time vs. Current Rate of Change

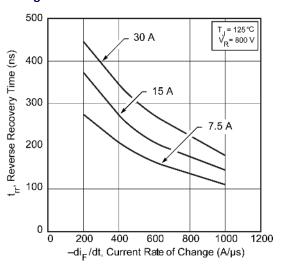
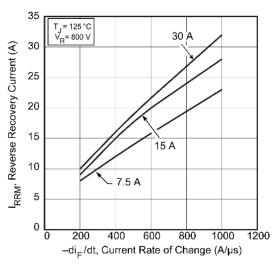
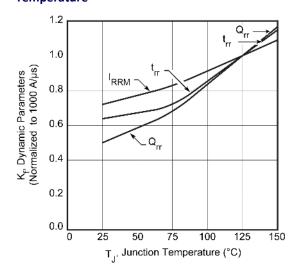


Figure 5 • Reverse Recovery Current vs. Current Rate of Change

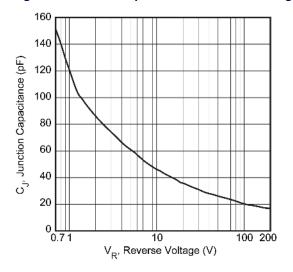




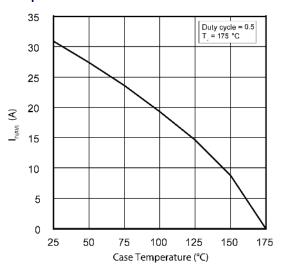
# Figure 6 • Dynamic Parameters vs. Junction Temperature



#### Figure 8 • Junction Capacitance vs. Reverse Voltage



# Figure 7 • Maximum Average Forward Current vs. Case Temperature

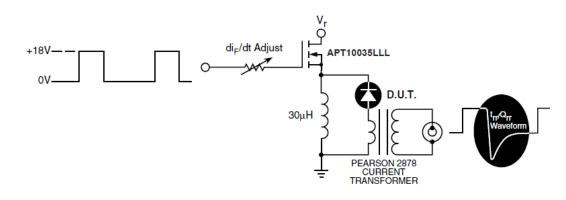




### 3.5 Reverse Recovery Overview

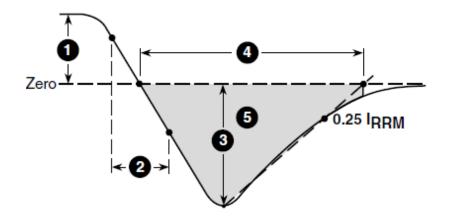
The following illustration shows the diode test circuit for the APT15D120BG device.

#### Figure 9 • Diode Test Circuit



The following illustration shows the diode reverse recovery waveform and definitions for the APT15D120BG device.

#### Figure 10 • Diode Reverse Recovery Waveform and Definitions



- 1. IF—Forward conduction current
- 2. di<sup>F</sup>/dt—Rate of diode current change through zero crossing
- 3. IRRM—Maximum reverse recovery current
- 4. trr—Reverse recovery time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through IRRM and 0.25•IRRM passes through zero
- 5. Qrr-Area under the curve defined by IRRM and trr



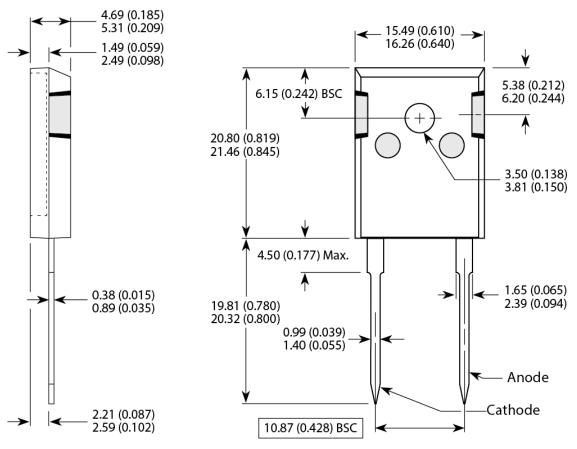
# 4 Package Specification

This section outlines the package specification for the APT15D120BG device.

#### 4.1 Package Outline Drawing

The following figure shows the package outline drawing of the APT15D120BG device. Dimensions are in millimeters and (inches).

#### Figure 11 • Package Outline Drawing







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