

# 1ch VIDEO DRIVER WITH SHORT-to-BATTERY PROTECTION

#### **■FEATURES**

- •Operating Voltage 2.65 to 3.45V
- •Operating Temperature -40 to +125°C
- •Short-to-Battery Protection Circuit of up to 18V
- •Output Capacitor is unnecessary
- •6dB Amp. , 75Ω Driver
- •LPF Characteristics
- 0dB at 6.75MHz -40dB at 27MHz
- •CMOS Technology •Package Outline

DFN8-U1 (ESON8-U1 2mm\*2mm)

#### ■GENERAL DESCRIPTION

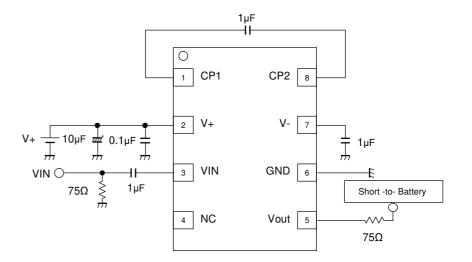
NJU71091 is 1ch video driver that built in short-to -battery protection circuit of up to 18V. Output capacitor is unnecessary because it built in charge-pump circuit. Therefore, NJU71091 can protect from more than voltage of IC's operating voltage, and is suitable to CAR CAMERA, CAR AV system and so on.

#### ■APPLICATION

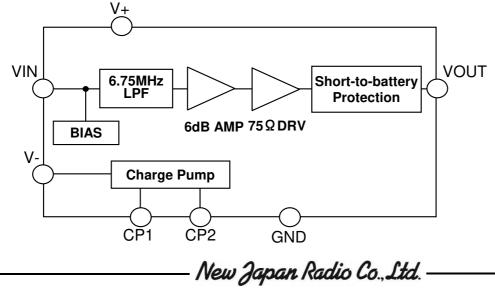
#### •Car Camera

Car Navigation

#### APPLICATION CIRCUIT (At short-to-battery measurement)



## ■EQUIVALENT CIRCUIT BLOCK DIAGRAM

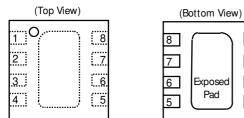




#### Built in short -to- battery protection circuit video driver

Output type	Part No.
Differential	NJU71094-T1

## **■PIN CONFIGURATION**



(1		,
8		1
7		2
6	Exposed Pad	3
5	Pad	4

PIN NO.	SYMBOL	DESCRIPTION
1	CP1	Flying Capacitor Terminal
2	V+ Power Supply Terminal	
3	VIN	Video Signal Input Terminal
4	N.C.	-
5	VOUT	Video Signal Output Terminal
6	GND	GND Terminal
7	V-	Flying Capacitor Terminal
8	CP2	Flying Capacitor Terminal

## Exposed Pad:

Connect the Exposed Pad on land of float, Or connect to be the same potential as the IC of the V- terminal.

#### **MARK INFORMATION**



#### **■ORDERING INFORMATION**

PART NUMBER	PACKAGE OUTLINE	RoHS	HALOGEN- FREE	TERMINAL FINISH	MARKING	WEIGHT (mg)	MOQ(pcs)
NJU71091KU1-T1	ESON8-U1	Yes	Yes	Sn-2Bi	71091T	5.3	3,000

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## ■ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	3.5	V
Power Dissipation (Ta=25°C) <sup>(4)</sup>	PD	1500 (1)	mW
Operating Temperature Range	T <sub>opr</sub>	-40 to 125	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to 150	°C

(1) Mounted on glass epoxy board. (101.5×114.5×1.6mm: based on EIA/JEDEC standard, 4Layers FR-4, with Exposed Pad) (For 4Layers: Applying 99.5×99.5mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5)

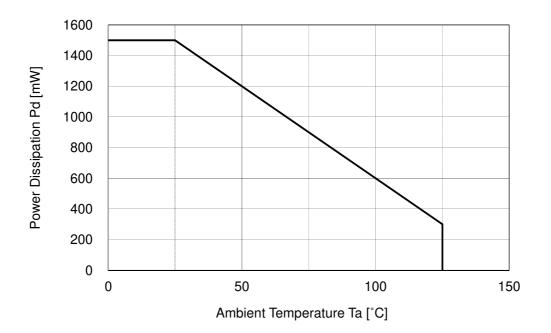
#### ■RECOMMENDED OPERATING CONDITIONS

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V+	2.65 to 3.45	V
VIN Input Voltage 1	VIN1	-1.0 to 1.0 (2)	V
VIN Input Voltage 2	VIN2	-0.9 to 0.9 (3)	V

(2) V+=2.65 to 3.15V

(3) 3.15V<V+≤3.45V

#### ■POWER DISSIPATION vs. AMBIENT TEMPERATURE





SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	•				
	No input signal	-	14	27	
lcc	No input signal,			07	mA
	Ta=-40°C to 125°C	-	-	27	
stics				1	
	Vin=100kHz, THD=1%,	3.6	-	-	
Vom	Vin=100kHz, THD=1%,				Vp-p
	Ta=-40°C to 125°C	3.6	-	-	
	Vin=100kHz, 1.0Vp-p	- 0			
	Sine wave	5.6	6.0	6.4	
Gv	Vin=100kHz, 1.0Vp-p				dB
	Sine wave, Ta=-40°C to 125°C	5.6	-	6.4	
	Vin=6.75MHz/1MHz, 1.0Vpp	-1.0	0	1.0	
Gf6.75M	Vin=6.75MHz/1MHz, 1.0Vpp				- dB
	Ta=-40°C to 125°C	-1.0	-	1.0	
Gf27M	Vin=27MHz/100kHz, 1.0Vpp	-	-40	-24	
	Vin=27MHz/100kHz, 1.0Vpp				
	Ta=-40°C to 125°C	-	-	-24	
DG	Vin=1.0Vp-p,				%
		-	0.9	-	
55	Vin=1.0Vp-p,				
DP	10step Video signal	-	1.0	-	deg
	RL=75Ω, 1.0Vpp,				
SN		-	70	-	dB
Nswpl		-	4.0	7.0	mVp-p
	Connected 75 $\Omega$ to Vout(pin5)	-	-	18	
Vstbm	~ · · · ·			10	V
	Ta=-40°C to 125°C	-	-	18	
	Connected $75\Omega$ to Vout(pin5)	-	-	6.0	
Vth	Connected $75\Omega$ to Vout(pin5)	-	-	60	V
	Ta=-40°C to 125°C			0.0	
lstb	Vout=18V, Connected 75 $\Omega$ to Vout(pin5)	-	2.0	-	mA
	Icc stics Vom Gv Gf6.75M Gf27M DG DP SN DP SN Nswpl Vstbm	$\begin{tabular}{ c c c c c } \hline No input signal & No input signal, Ta=-40°C to 125°C & \end{tabular}{line text{stics}} & \begin{tabular}{ c c c c c c c } \hline Vom & Vin=100kHz, THD=1%, Ta=-40°C to 125°C & \end{tabular}{line text{stics}} & ta$	$\begin{tabular}{ c c c c c c } \hline No input signal & - & & & & & & & & & & & & & & & & & $	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$

## ■ELECTRICAL CHARACTERISTICS (Ta=25°C, V<sup>+</sup>=3.0V, RL=150Ω, unless otherwise specified)

(4) Maximum input voltage of destination of output resistance  $75\Omega$ 

(5) It is become protect mode at more than this voltage against input voltage of destination of output resistance 750hm.

(6) If supply voltage is OFF, you must not input the short-to-battery voltage. IC is broken in the worst case.

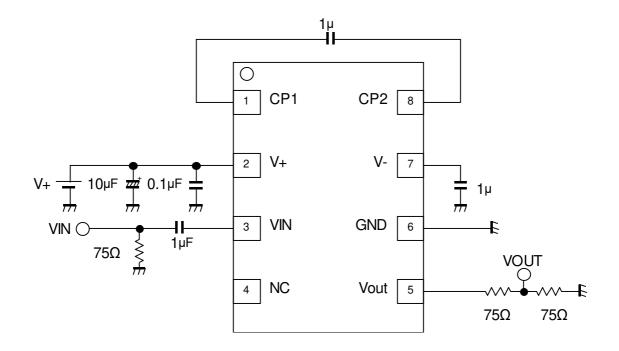
(7) Guaranteed range of Short to Battery voltage is 6V to 18V.

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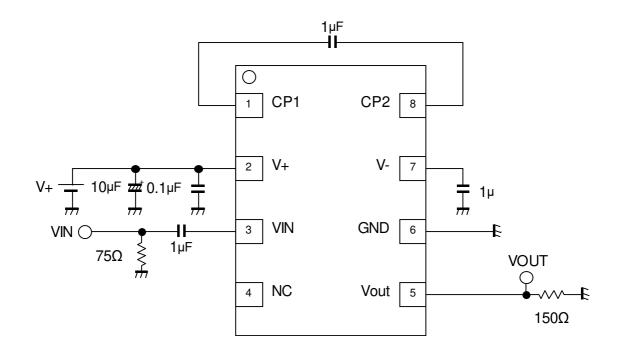
## ∎TEST CIRCUIT 1

(Supply current, Differential gain, Differential phase, Switching noise level, Detect protected Input voltage)



## ∎TEST CIRCUIT 2

(Maximum output level, Voltage gain, Frequency characteristics)

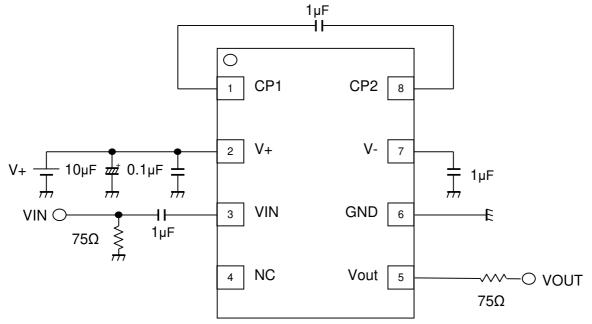


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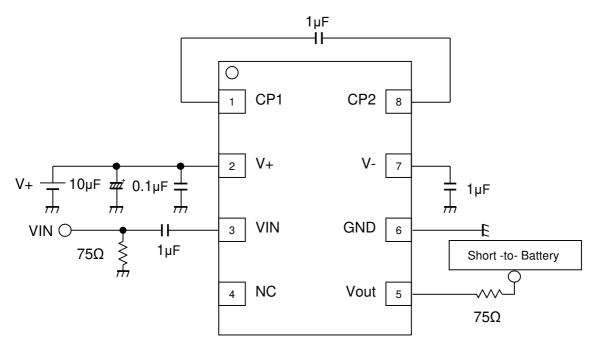
## ■APPLICATION CIRCUIT 1(Standard)



#### Exposed Pad:

Connect the Exposed Pad on land of float, or connect to be the same potential as the IC of the V- terminal.

### ■APPLICATION CIRCUIT 2 (At short-to-battery)



#### Exposed Pad:

Connect the Exposed Pad on land of float, or connect to be the same potential as the IC of the V- terminal.

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## **TERMINAL FUNCTION**

PINNo.	PINNAME	FUNCTION	EQUIVALENTCIRCUIT	DC VOLTAGE
1	CP1	Flying Capacitor Terminal		_
2	V+	V+ Power Supply	-	-
3	VIN	Video Signal Input Terminal	V+ 200 V+ 200 V-	οv
4	N.C.	-	-	-
5	VOUT	Video Signal Output Terminal	10k VOUT	ov

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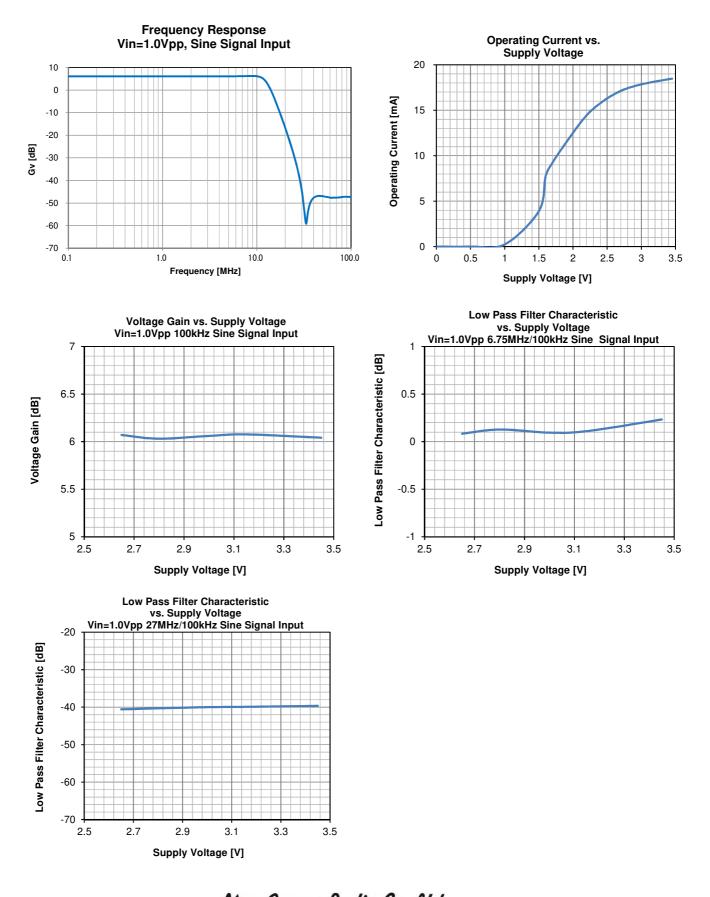
## **TERMINAL FUNCTION**

PINNo.	PINNAME	FUNCTION	EQUIVALENTCIRCUIT	DC VOLTAGE
6	GND	Ground	-	-
7	V-	Flying Capacitor Terminal	-	-
8	CP2	Flying Capacitor Terminal	GND 	-

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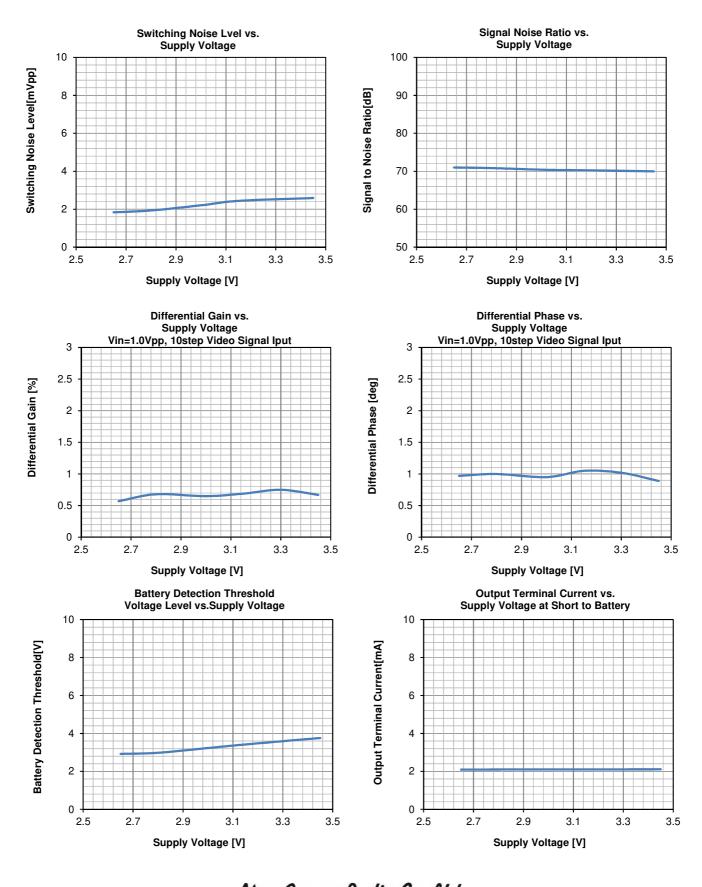
### ■TYPICAL CHARACTERISTICS



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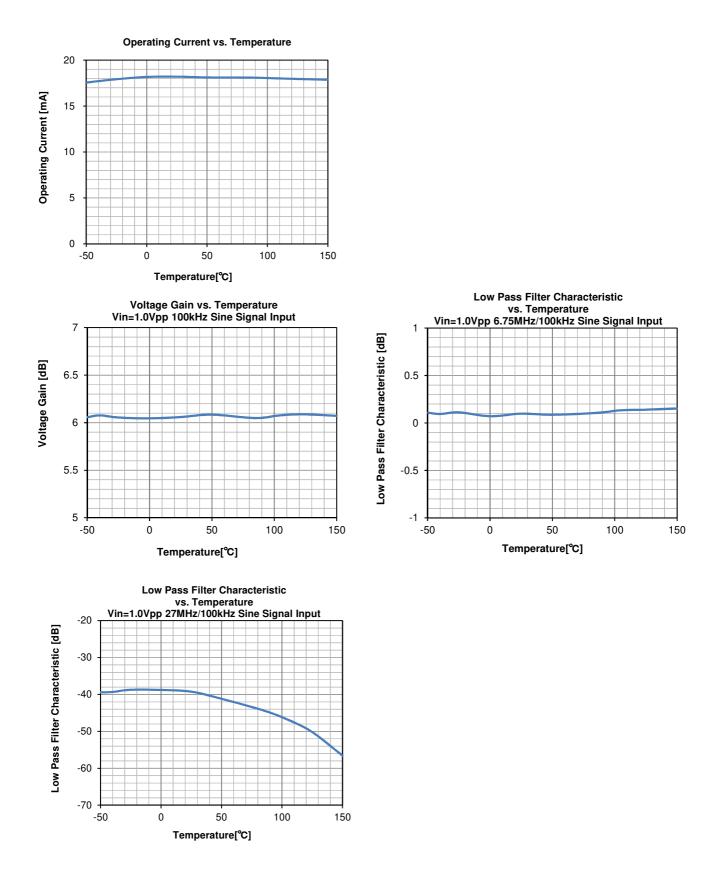
### ■TYPICAL CHARACTERISTICS



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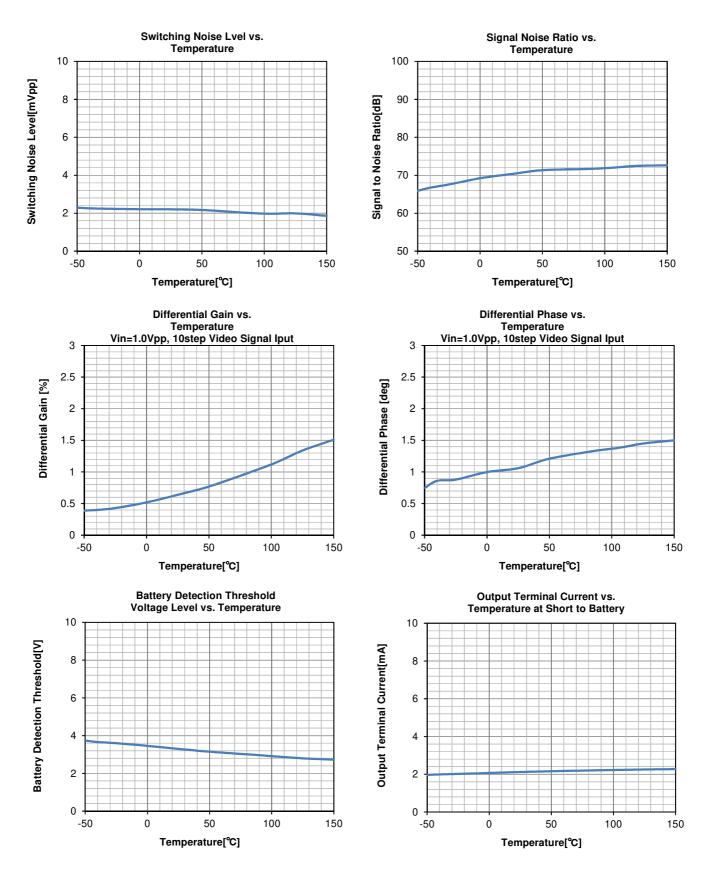
### **TYPICAL CHARACTERISTICS**



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### ■TYPICAL CHARACTERISTICS

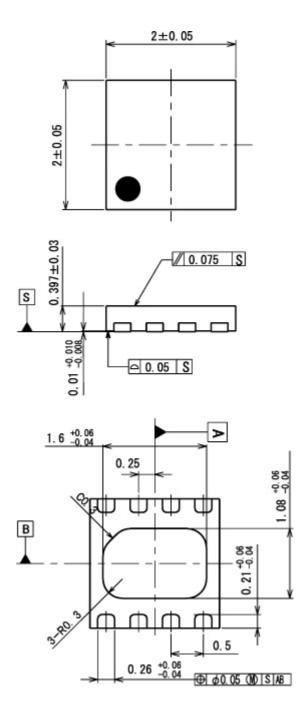


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■PACKAGE OUTLINE

## DFN8-U1 (ESON8-U1)

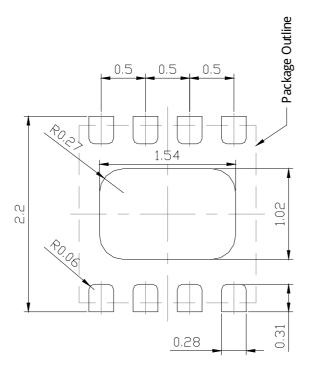


UNIT : mm

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## **SOLDER FOOT PRINT**



Unit : mm

Note : These solder foot print dimensions are just examples. When designing PCB, please estimate the pattern carefully.

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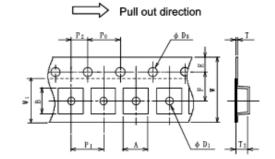
#### ■PACKING SPECIFICATION

### **General Description**

NJRC delivers ICs in 4 methods, plastic tube container, two kinds of Taping, tray and vinyl bag packing. Except adhesive tape treated anti electrostatic and contain carbon are using as the ESD ( Electrostatic Discharge Damage ) protection.

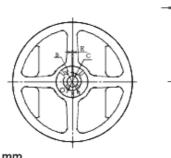
DFN(ESON) Emboss Taping (TE3)

Symbol	DFN8-U1(ESON8-U1)	Remark
A	2.25±0.05	Bottom size
в	2.25±0.05	Bottom size
Do	1.5+0.1/-0	
D1	0.5±0.1	
E	1.75±0.1	
F	3.5 ±0.05	
P <sub>0</sub>	4.0 ±0.1	
P <sub>1</sub>	4.0 ±0.1	
P2	2.0 ±0.05	
т	0.25±0.05	
T <sub>2</sub>	0.75	
w	8.0 ±0.2	
W1	5.5	Thickness 0.1MAX

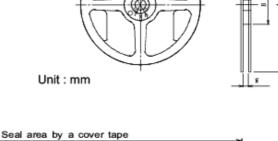


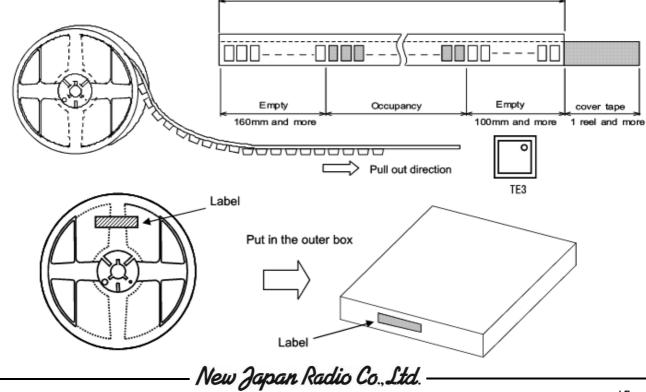
Unit : mm

Symbol	DFN8-U1(ESON8-U1)
А	φ180 +0/-1.5
В	φ 60 +1/-0
с	φ13.0±0.2
D	φ21.0±0.8
E	2.0±0.5
w	9.0 +0.3/-0
W1	1.2
Contents	3,000pcs





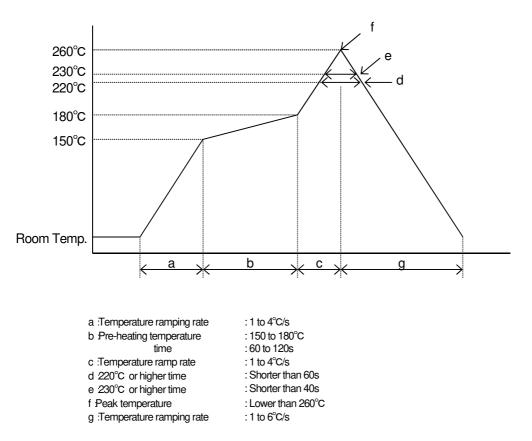






#### ■RECOMMENDED MOUNTING METHOD

\* Recommended reflow soldering procedure



The temperature indicates at the surface of mold package.

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## [CAUTION]

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