

Product Features

- Single-chip, PCIe 3.1 @ 2.5GT/s to 10/100/1000 Ethernet controller
- PCIe Low Power Sub States LPSS L1.1 (snooze) & L1.2 (off)
- IEEE 1588-2008 Programmable Time Compare output (Eg: 1PPS)
- IEEE 1588-2008 Capture input
- JTAG via integrated 1149.1 compliant TAP
- Accessible GPIO