



150V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BVDSS RDS(ON) Max		I _D Max T _C = +25°C	
150V	35mΩ @ V _{GS} = 10V	46A	

Description and Applications

This new generation MOSFET features low on-resistance and fast switching, making it ideal for high efficiency power management applications.

- Power Management Functions
- DC-DC Converters
- Backlighting

Features and Benefits

- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low R_{DS(ON)} Minimizes Power Losses
- Low Q_q Minimizes Switching Losses
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative. https://www.diodes.com/quality/product-definitions/

Mechanical Data

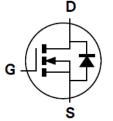
- Package: TO220AB
- Package Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Terminal Connections: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe Solderable per MIL-STD-202, Method 208 (©3)
- Weight: 2.24 grams (Approximate)

TO220AB

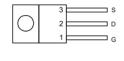








Equivalent Circuit



Top View Pin Out Configuration

Ordering Information (Note 4)

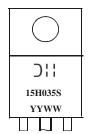
Top View

Part Number	Dookogo	Packing		
Part Number	Раскаде	Qty.	Carrier	
DMT15H035SCT	TO220AB	50pcs	Tube	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



Dil = Manufacturer's Marking
15H035S = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 21 = 2021)
WW = Week Code (01 to 53)



Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage	VDSS	150	V		
Gate-Source Voltage		Vgss	±20	V	
Continuous Durin Coursett VIII 40V (Nata 5)	T _C = +25°C		46	٨	
Continuous Drain Current, V _{GS} = 10V (Note 5)	Tc = +70°C	l _D	37	А	
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	IDM	184	Α		
Maximum Continuous Body Diode Forward Current (Note 5)	Is	46	Α		
Pulsed Body Diode Continuous Current (10µs Pulse, Duty Cycle	Ism	184	Α		
Avalanche Current, L = 1mH (Note 8)	IAS	17	Α		
Avalanche Energy, L = 1mH (Note 8)		Eas	144.5	mJ	

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)		PD	2.2	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _θ JA	55	°C/W
Total Power Dissipation (Note 5)		PD	166	W
Thermal Resistance, Junction to Case (Note 5)		R _в јс	0.75	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

Electrical Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BVDSS	150	_	_	V	$V_{GS} = 0V$, $I_D = 10mA$	
Zero Gate Voltage Drain Current	IDSS	_	_	1	μΑ	V _{DS} = 120V, V _{GS} = 0V	
Gate-Source Leakage	lgss	_	_	±100	nA	$V_{GS} = \pm 20V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)			_			·	
Gate Threshold Voltage	V _{GS(TH)}	2	2.8	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance	RDS(ON)	_	22	35	mΩ	V _G S = 10V, I _D = 20A	
Diode Forward Voltage	V _{SD}	_	0.9	1	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	1600	_		V _{DS} = 75V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	160	_	pF		
Reverse Transfer Capacitance	Crss	_	4.2	_		I = IIVIMZ	
Gate Resistance	Rg	_	0.3	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Qg	_	25	_		V _{DS} = 75V, I _D = 4.1A,	
Gate-Source Charge	Qgs	_	9	_	nC		
Gate-Drain Charge	Q_{gd}	_	6	_		V _G S = 10V	
Turn-On Delay Time	td(on)	_	16.6	_		$V_{DS} = 75V, V_{GS} = 10V,$ $I_{D} = 4.1A, R_{g} = 6\Omega$	
Turn-On Rise Time	tr	_	15.3	_			
Turn-Off Delay Time	t _{D(OFF)}	_	26.5	_	ns		
Turn-Off Fall Time	tF	_	13.7	_			
Reverse Recovery Time	trr	_	53	_	ns	1 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4 4	
Reverse Recovery Charge	Qrr		47	_	nC	I _F = 4.1A, di/dt = 100A/μs	

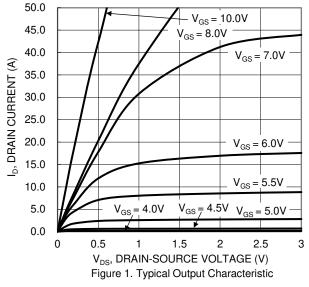
Notes: 5. Thermal resistance from junction to soldering point (on the exposed drain pad).

^{6.} Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
7. Short duration pulse test used to minimize self-heating effect.

^{8.} Guaranteed by design. Not subject to product testing.







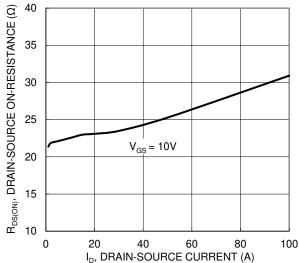


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

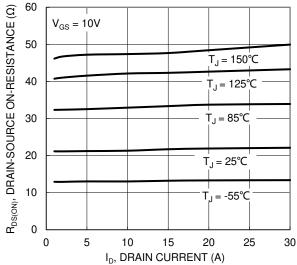
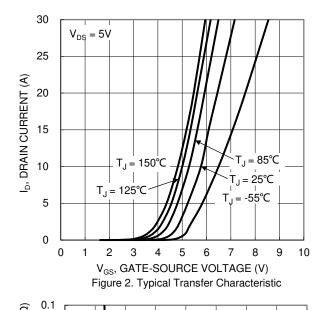


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature



 $R_{DS(ON)}$, DRAIN-SOURCE ON-RESISTANCE (Ω) 0.09 0.08 0.07 0.06 0.05 0.04 $I_D = 20A$ 0.03 0.02 0.01 0 6 8 10 12 18 20 4 14 16 V_{GS}, GATE-SOURCE VOLTAGE (V) Figure 4. Typical Transfer Characteristic

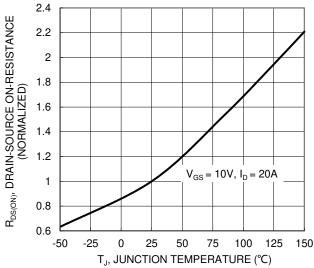


Figure 6. On-Resistance Variation with Junction Temperature



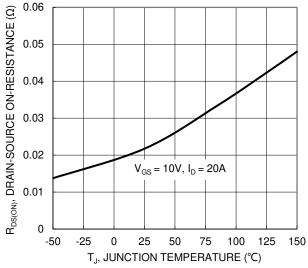


Figure 7. On-Resistance Variation with Junction Temperature

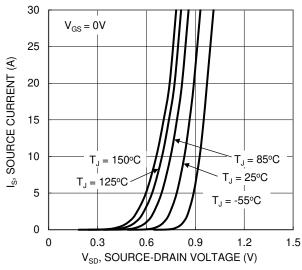
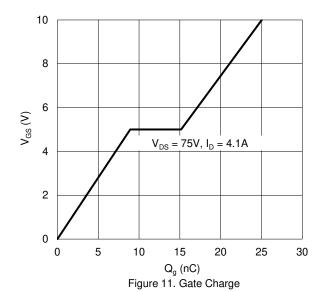


Figure 9. Diode Forward Voltage vs. Current



5 V_{GS(TH)}, GATE THRESHOLD VOLTAGE (V) 4.5 4 3.5 $I_D = 1mA$ 3 2.5 $_{D} = 250 \mu A$ 2 1.5 1

0.5

-50

-25

0

25

T_J, JUNCTION TEMPERATURE (°C) Figure 8. Gate Threshold Variation vs. Junction Temperature

50

75

100

125

150

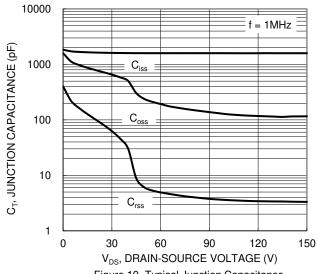
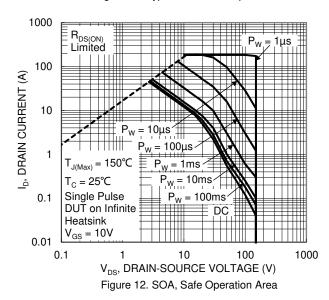


Figure 10. Typical Junction Capacitance





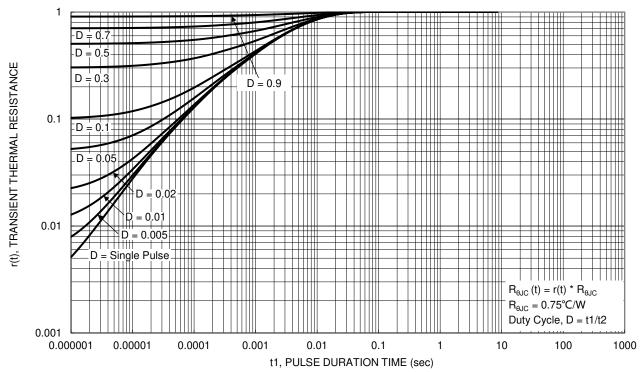


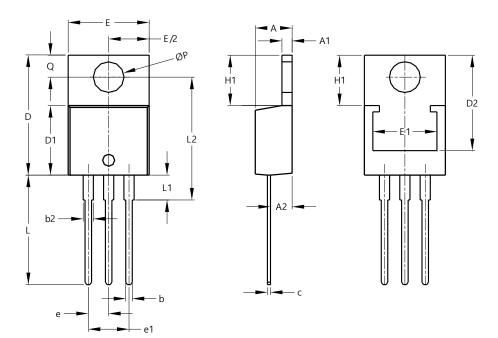
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

TO220AB



TO220AB						
Dim	Min	Max	Тур			
Α	3.56	4.82	-			
A 1	0.51	1.39	-			
A2	2.04	2.92	-			
b	0.39	1.01	0.81			
b2	1.15	1.77	1.24			
С	0.356	0.61	-			
D	14.22	16.51	-			
D1	8.39	9.01	-			
D2	11.45	12.87	-			
е	-	-	2.54			
e1	-	-	5.08			
Е	9.66	10.66	-			
E1	6.86	8.89	-			
H1	5.85	6.85	-			
L	12.70	14.73	-			
L1	-	4.42	-			
L2	15.80	17.51	16.00			
Р	3.54	4.08	-			
Q	2.54	3.42	-			
All Dimensions in mm						

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance.



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