# VEMD4010X01

## **Vishay Semiconductors**





## LINKS TO ADDITIONAL RESOURCES



## DESCRIPTION

VEMD4010X01 is a high speed and high sensitive PIN photodiode. It is a miniature surface mount device (SMD) with a 0.42 mm<sup>2</sup> sensitive area.

## FEATURES

Silicon PIN Photodiode

- Package type: surface-mount
- Package form: 0805
- Dimensions (L x W x H in mm): 2 x 1.25 x 0.7
- Radiant sensitive area (in mm<sup>2</sup>): 0.42
- Operating temperature range:  $T_{OP} = -40$  °C to +110 °C
- Angle of half sensitivity:  $\phi = \pm 55^{\circ}$
- Floor life: 168 h, MSL 3, according to J-STD-020
- Lead (Pb)-free reflow soldering
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **APPLICATIONS**

- High speed photo detector
- Photo interrupters
- Automotive sensors

PRODUCT SUMMARY			
COMPONENT	I <sub>ra</sub> (μΑ) at E <sub>e</sub> = 1 mW/cm <sup>2</sup> , $\lambda$ = 950 nm, V <sub>R</sub> = 5 V	φ <b>(°)</b>	λ <sub>0.5</sub> (nm)
VEMD4010X01	2.4	± 55	550 to 1040

Note

• Test conditions see table "Basic Characteristics"

ORDERING INFORMATION				
ORDERING CODE	PACKAGING	REMARKS	PACKAGE FORM	
VEMD4010X01	Tape and reel	MOQ: 3000 pcs, 3000 pcs/reel 0805		

#### Note

• MOQ: minimum order quantity

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage		V <sub>R</sub>	20	V
Operating temperature range		T <sub>amb</sub>	-40 to +110	°C
Storage temperature range		T <sub>stg</sub>	-40 to +110	°C
Soldering temperature	According to reflow solder profile Fig. 8	T <sub>sd</sub>	260	°C

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e

RoHS

COMPLIANT HALOGEN

FREE

GREEN (5-2008)

# VEMD4010X01



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<b>BASIC CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Forward voltage	I <sub>F</sub> = 50 mA	V <sub>F</sub>	-	1.1	1.3	V
Reverse dark current	$V_{R} = 5 V, E = 0$	I <sub>ro</sub>	-	1	3	nA
Diode capacitance	V <sub>R</sub> = 0 V, f = 1 MHz, E = 0	CD	-	7	-	pF
	$V_{R} = 5 V, f = 1 MHz, E = 0$	CD	-	2.5	-	pF
Short circuit current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	l <sub>k</sub>	-	2.2	-	μA
Open circuit voltage	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	Vo	-	318	-	mV
Temperature coefficient of $I_k$	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$	TK <sub>lk</sub>	-	0.1	-	%/K
Reverse light current	$E_e = 1 \text{ mW/cm}^2$ , $\lambda = 950 \text{ nm}$ , $V_R = 5 \text{ V}$	I <sub>ra</sub>	1.9	2.4	3.1	μA
Angle of half sensitivity		φ	-	± 55	-	0
Wavelength of peak sensitivity		λρ	-	910	-	nm
Range of spectral bandwidth		λ <sub>0.5</sub>	-	550 to 1040	-	nm
Rise time	$V_R$ = 5 V, $R_L$ = 1 k $\Omega$ , $\lambda$ = 820 nm	t <sub>r</sub>	-	100	-	ns
Fall time	$V_R$ = 5 V, $R_L$ = 1 k $\Omega$ , $\lambda$ = 820 nm	t <sub>f</sub>	-	100	-	ns

## BASIC CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

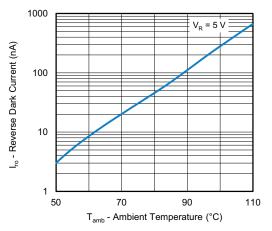


Fig. 1 - Reverse Dark Current vs. Ambient Temperature

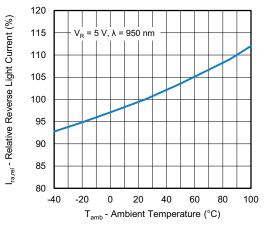


Fig. 2 - Relative Reverse Light Current vs. Ambient Temperature

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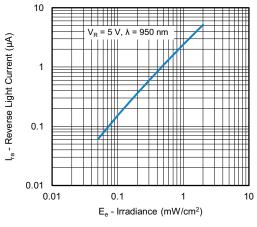


Fig. 3 - Reverse Light Current vs. Irradiance

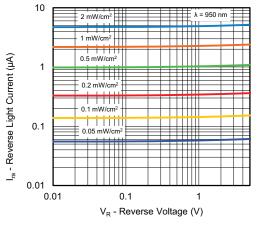


Fig. 4 - Reverse Light Current vs. Reverse Voltage

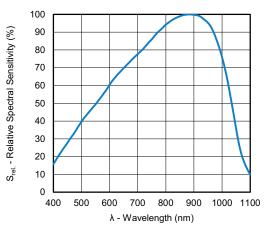


Fig. 5 - Relative Spectral Sensitivity vs. Wavelength

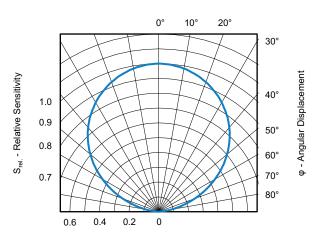


Fig. 6 - Relative Sensitivity vs. Angular Displacement

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## **REFLOW SOLDER PROFILE**

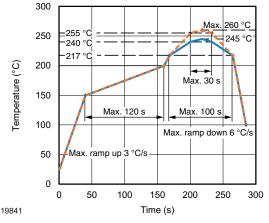


Fig. 7 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020

## **PACKAGE DIMENSIONS** in millimeters

#### DRYPACK

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

## FLOOR LIFE

Floor life (time between soldering and removing from MBB) must not exceed the time indicated on MBB label:

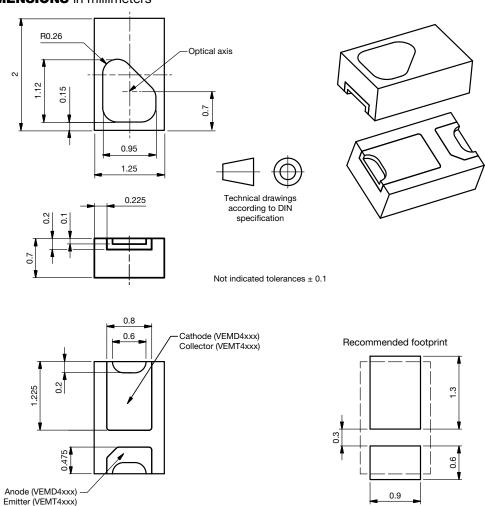
Floor life: 168 h

Conditions:  $T_{amb} < 30\ ^\circ C,\ RH < 60\ \%$ 

Moisture sensitivity level 3, according to J-STD-020.

### DRYING

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-033D or label. Devices taped on reel dry using recommended conditions 192 h at 40 °C (+ 5 °C), RH < 5 %.



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Rev. 1.4, 07-Nov-2022

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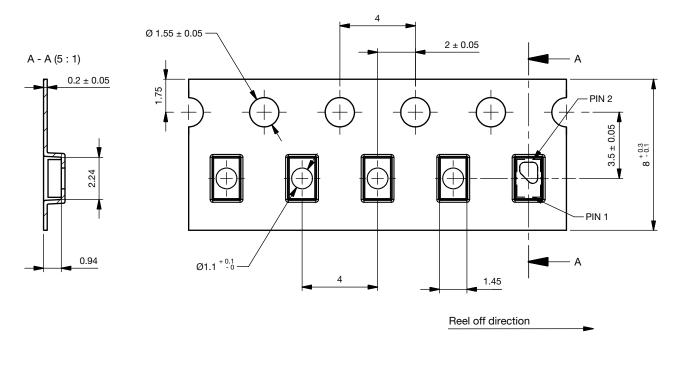
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## **BLISTER TAPE DIMENSIONS** in millimeters



TYPE	PIN 1	PIN 2
VEMD4xxx	Anode	Cathode

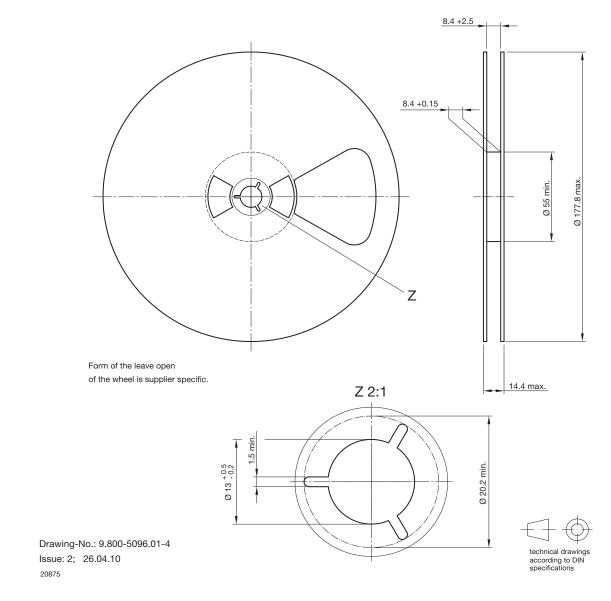
Drawing-No.: 9.700-5411.0-4 Issue: 1\_A; 11.10.2022





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## **REEL DIMENSIONS** in millimeters



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