SPDT Absorptive Switch
DC - 44 GHz

MASW-011198
Rev. V1

Features

- Ultra Wideband: 9 kHz to 44 GHz
- Insertion Loss:
  - 1.1 dB @ 15 GHz
  - 1.5 dB @ 30 GHz
  - 1.8 dB @ 44 GHz
- Isolation:
  - 60 dB @ 15 GHz
  - 58 dB @ 30 GHz
  - 43 dB @ 44 GHz
- Input P1dB: 28 dBm
- Input IP3: 52 dBm
- Return Loss at Each RF Port: 18 dB
- Power Handling Including Hot Switching: 26 dBm
- No Low Frequency Spurious
- Compatible with 1.8, 2.5, and 3.3V CMOS Logic
- 3 mm, 20 Lead Laminate Package
- RoHS* Compliant

Applications

- Multi Market
- ISM

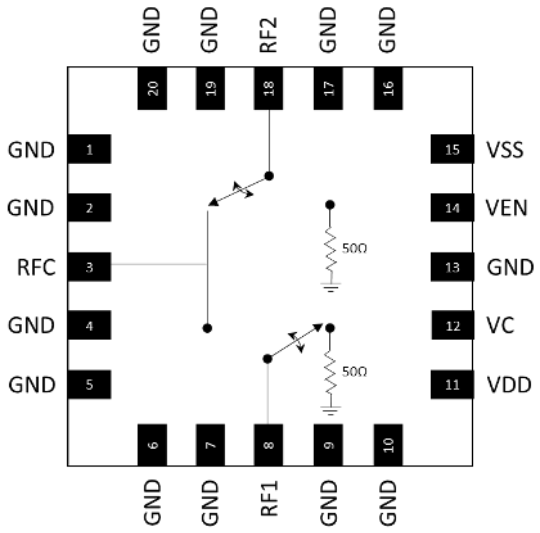
Description

The MASW-011198 is an absorptive, ultra wideband single pole double throw (SPDT) switch with 1.8 dB of insertion loss at 44 GHz. The RF output ports are terminated in 50 Ω in the isolated paths. The power handling capability is 26 dBm. The input and output return losses are typically 18 dB. The logic levels are compatible with standard 1.8, 2.5, or 3.3 V CMOS. Required bias supplies are +3.3 V & -3.3 V.

The MASW-011198 is designed for wideband applications such as Test and Measurement, Aerospace and Defense, Cellular infrastructure (5G millimeter-wave), military radios, radars, microwave radios and very small aperture terminals (VSATs).

The MASW-011198 is manufactured on a Silicon-on-Insulator process. The 3 mm laminate package is lead free and RoHS compliant.

Functional Schematic



Pin Configuration[1]

Pin #	Pin Name	Description
1,2,4-7,9,10,13 16,17,19,20	GND	Ground
3	RFC[2]	Common RF Input/Output
8	RF1[2]	RF Input/Output 1
11	VDD	+3.3V
12	VC	Control Voltage
14	VEN	Enable Voltage
15	VSS	-3.3V
18	RF2[2]	RF Input/Output 2

1. The exposed pad centered on the package bottom must be connected to RF, dc and thermal ground.
2. RF ports are dc-coupled to GND. There are no internal DC blocking capacitors. External dc blocking capacitor is not necessary if the RF line dc bias is 0V.

Ordering Information[3,4]

Part Number	Package
MASW-011198-TR0500	500 Piece Reel
MASW-011198-SMB	Sample Board

3. Reference Application Note M513 for reel size information.
4. All sample boards include 3 loose parts.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

Electrical Specifications[5]:
V_{DD} = +3.3 V, V_{SS} = -3.3 V, VC = 0 V or 1.8 V, T_{PADDLE} = 25°C, Z_0 = 50 Ω

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Insertion Loss	DC to 15 GHz 15 GHz 30 GHz 44 GHz	dB	—	0.9 1.1 1.5 1.8	— 1.5 2.0 2.5
Isolation, Between RF1 to RF2	DC to 15 GHz 15 GHz 30 GHz 44 GHz	dB	—	68 72 73 47	—
Isolation, RFC to RF1 / RF2	DC to 15 GHz 15 GHz 30 GHz 44 GHz	dB	— 55 50 40	60 60 58 43	—
RFC Return Loss	DC - 44 GHz	dB	—	18	—
RF1 / RF2 Return Loss, Thru Port	DC - 44 GHz	dB	—	18	—
RF1 / RF2 Return Loss, Isolated Port	DC - 44 GHz	dB	—	18	—
Input P0.1dB	10 MHz - 44 GHz	dBm	—	27	—
Input P1dB	10 MHz - 44 GHz	dBm	—	28	—
Input IP3	Two tone, P_{IN}/tone = +14 dBm 10 MHz - 44 GHz	dBm	—	52	—
T_{ON}	50% control to 90% RF	µs	—	0.9	—
T_{RISE}	10% to 90% RF	µs	—	0.35	—
T_{OFF}	50% control to 10% RF	µs	—	0.2	—
T_{FALL}	90% to 10% RF	µs	—	0.04	—
Voltage Supply, VDD	—	V	3.15	3.3	3.45
Voltage Supply, VSS	—	V	-3.45	-3.3	-3.15
Logic Voltage, Input Low (V_{IL})	—	V	0.0	—	0.8
Logic Voltage, Input High (V_{IH})	—	V	1.2	—	VDD
Supply Current, VDD	—	mA	—	0.3	0.5
Supply Current, VSS	—	mA	—	0.65	1.0
Logic Pin Current (VEN / VC)	Pulled down to GND with 100 kΩ resistor internally	µA	—	VC*10	—

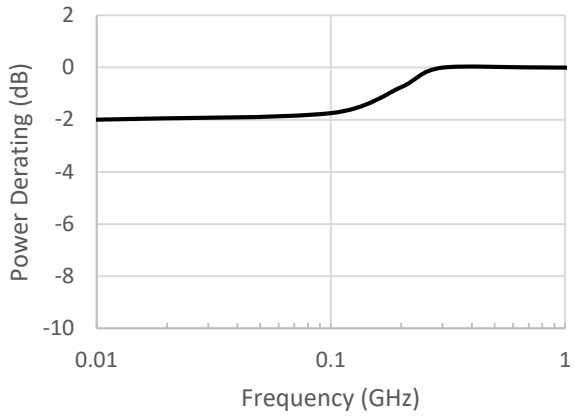
5. Device shall be aligned to recommended PCB footprint within +/- 1 mil for optimum performance.

Maximum Operating Conditions

Parameter	Maximum
Input Power, 300 MHz to 44 GHz, RFC Port[6] RF1 / RF2 Port Thru Path[6] RF1 / RF2 Port Terminated Path[6]	 26 dBm 26 dBm 24 dBm
VDD	-0.3 to +3.45 V
VSS	-3.45 to +0.3 V
VC	-0.3 to 3.45 V
Operating Temperature[7]	-40 to +105°C

6. T_{PADDLE} = +105ºC. See power derating curves for details.
7. Guarantees 10 years lifetime.

Low Frequency Power Derating Detail[6]



Absolute Maximum Ratings[8,9,10]

Parameter	Absolute Maximum
Input Power, 300 MHz to 44 GHz, RFC Port[6] RF1 / RF2 Port Thru Path[6] RF1 / RF2 Port Terminated Path[6]	 27 dBm 27 dBm 25 dBm
VDD	-0.3 to +3.6 V
VSS	-3.6 to +0.3 V
VC	-0.3 to 3.6 V
Junction Temperature	+135°C

8. Exceeding any one or combination of these limits may cause permanent damage to this device.
9. MACOM does not recommend sustained operation near these survivability limits.
10. Based on testing with input power applied for 30 seconds.

Truth Table

Enable	Control	Condition of Switch	
VEN	VC	RF1	RF2
V_{IL}	V_{IL}	Off	On
V_{IL}	V_{IH}	On	Off
V_{IH}	V_{IL}	Off	Off
V_{IH}	V_{IH}	Off	Off

Handling Procedures

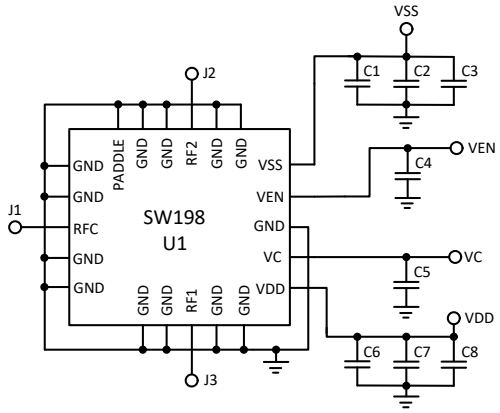
Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Parameter	Rating	Standard
Human Body Model (HBM)	Class 1C	ESDA/JEDEC JS-001
Charged Device Model (CDM)	Class TBD	ESDA/JEDEC JS-002

Application Schematic



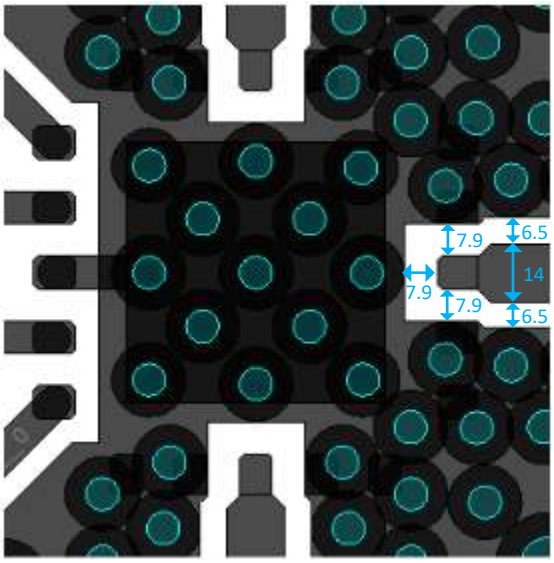
Parts List

Part	Value	Case Style
U1	MASW-011198	3 mm, 20 Lead
C1, C6	Capacitor, 10 pF, 50 V	0402
C2,C7	Capacitor, 1000 pF, 25 V	0402
C3, C8	Capacitor, 1 µF, 10 V	0402
C4, C5	Capacitor, 5 pF, 16 V	0402
J1 - J3	Southwest 1492-04A-5	End Launch

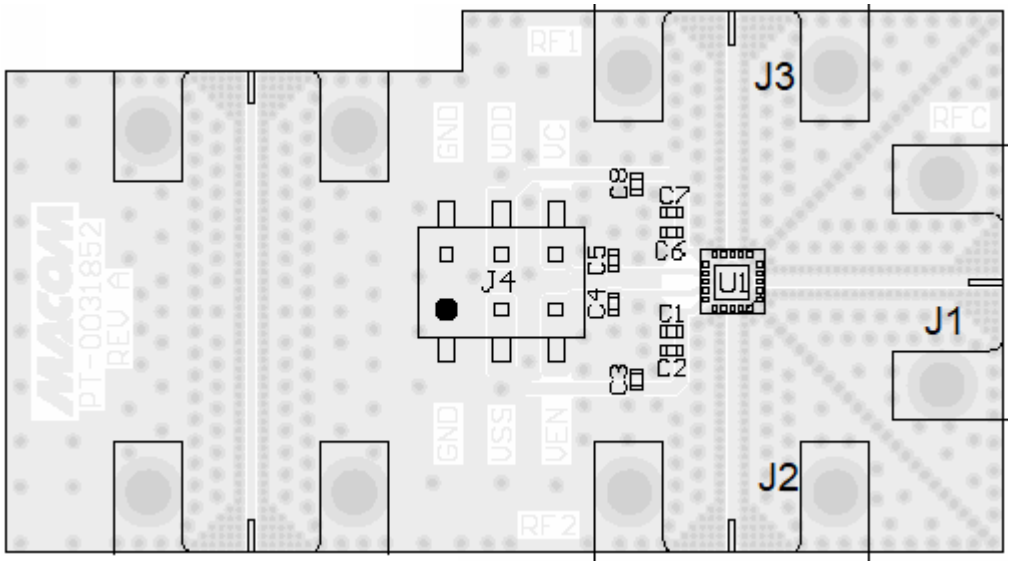
Recommended PCB Footprint

MASW-011198-SMB is a 2-layer board with 8 mil Rogers RO4003 dielectric material and 1 oz. copper on top and bottom layers. For this stack-up, the recommended PCB footprint is shown below.

The 50Ω RF transmission lines are CPWG of 14 mil width with 6.5 mil gap.

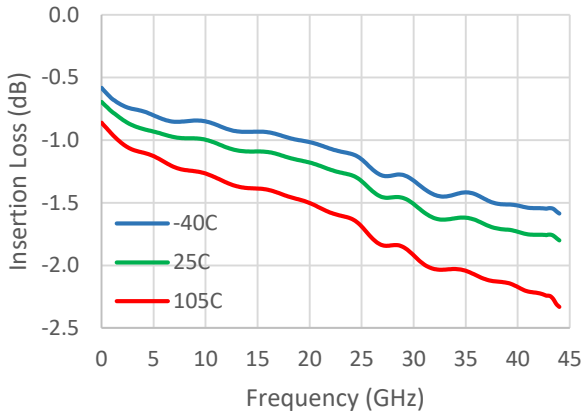


Evaluation Board Layout

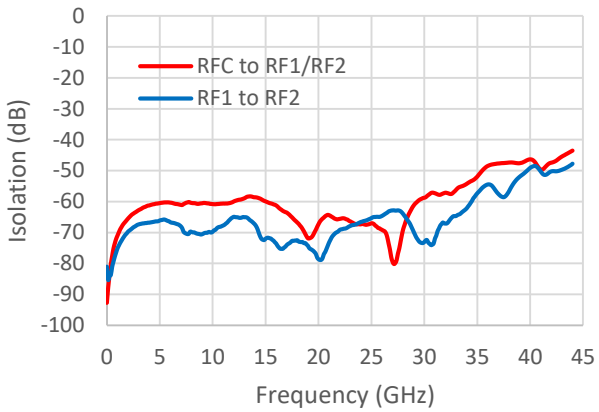


Typical Performance Curves

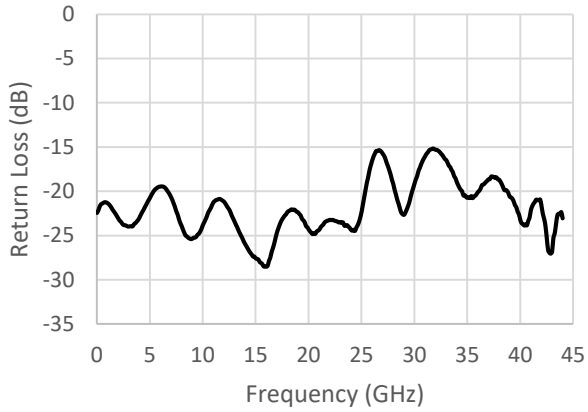
Insertion Loss[11]



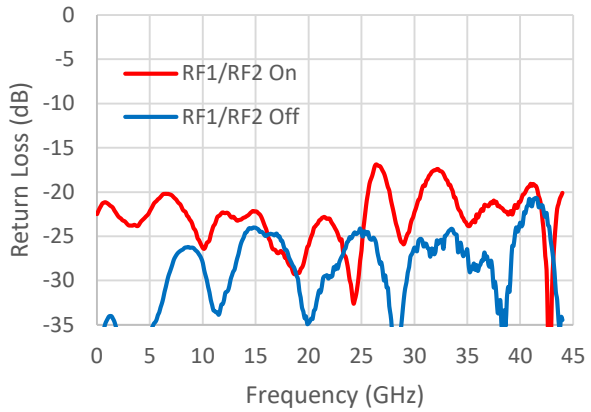
RFC to RF1 / RF2 Isolation[11]



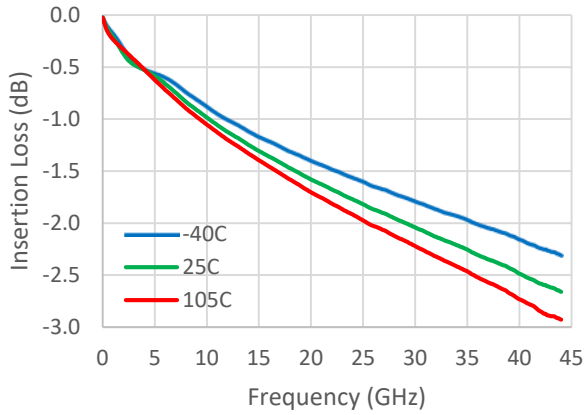
RFC Return Loss[12]



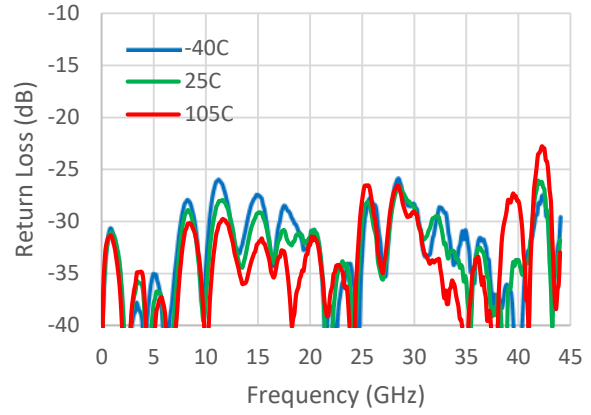
RF1 / RF2 Return Loss[12]



Evaluation Board Thru Line Insertion Loss



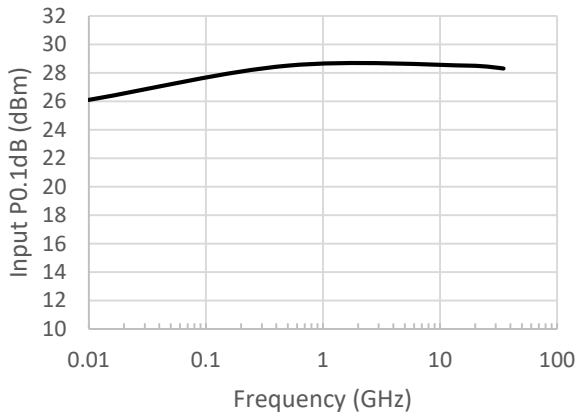
Evaluation Board Thru Line Return Loss



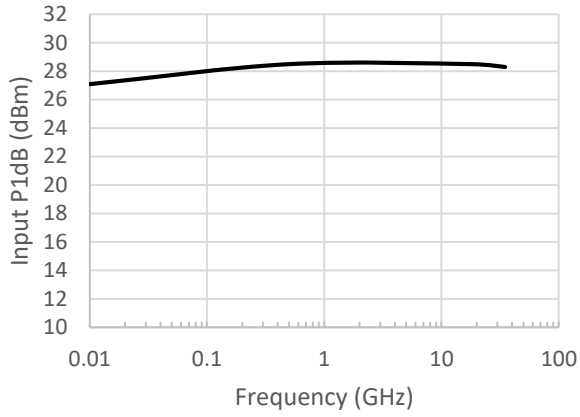
11. Insertion Loss and Isolation were measured using connectorized evaluation board, and normalized using the insertion loss of the 50 Ω thru line.
12. Return Loss were measured using connectorized evaluation board.

Typical Performance Curves

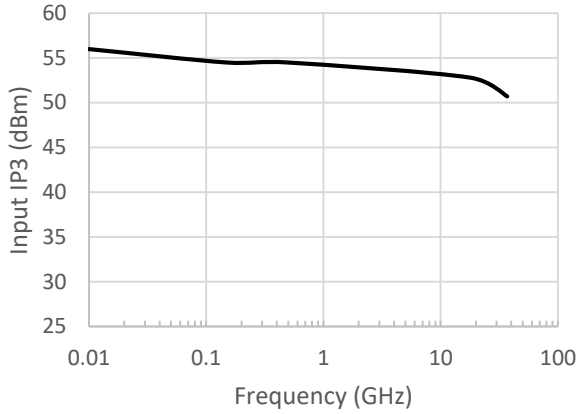
Input P0.1dB



Input P1dB

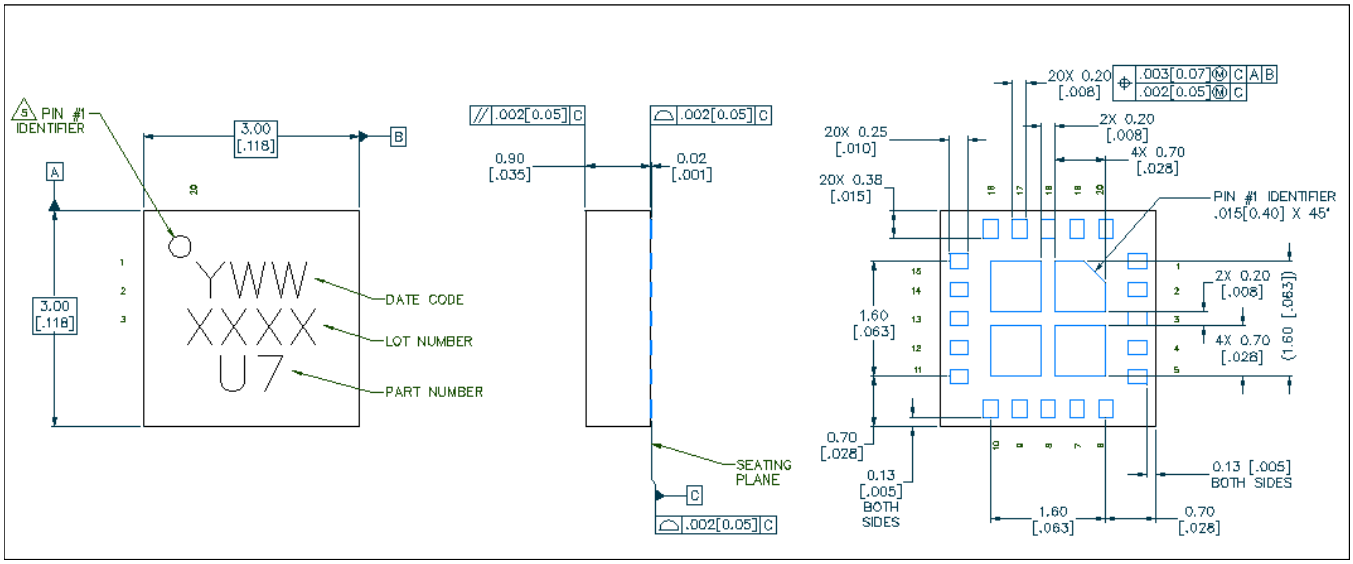


Input IP3[13]



13. Input IP3 were measured using connectorized evaluation board. The RF input power was 14 dBm per tone with spacing of 1 MHz.

Lead Free 3 mm 20-Lead Laminate Package †



† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is ENEPIG Ni 3~9 µm / Pd 0.02~0.09 µm / Au 0.03 ~ 0.12 µm.