

SPDT Absorptive Switch DC - 44 GHz



MASW-011198

Rev. V1

Features

- Ultra Wideband: 9 kHz to 44 GHz
- Insertion Loss:
 - 1.1 dB @ 15 GHz
 - 1.5 dB @ 30 GHz
 - 1.8 dB @ 44 GHz
- Isolation:
 - 60 dB @ 15 GHz
 - 58 dB @ 30 GHz
 - 43 dB @ 44 GHz
- Input P1dB: 28 dBm
- Input IP3: 52 dBm
- Return Loss at Each RF Port: 18 dB
- Power Handling Including Hot Switching: 26 dBm
- No Low Frequency Spurious
- Compatible with 1.8, 2.5, and 3.3V CMOS Logic
- 3 mm, 20 Lead Laminate Package
- RoHS* Compliant

Applications

- Multi Market
- ISM

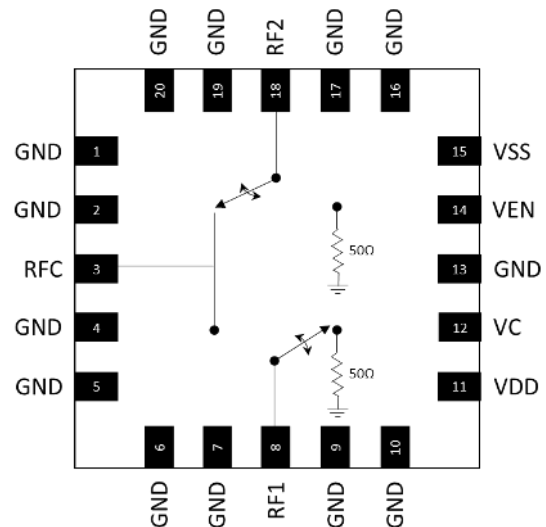
Description

The MASW-011198 is an absorptive, ultra wideband single pole double throw (SPDT) switch with 1.8 dB of insertion loss at 44 GHz. The RF output ports are terminated in 50 Ω in the isolated paths. The power handling capability is 26 dBm. The input and output return losses are typically 18 dB. The logic levels are compatible with standard 1.8, 2.5, or 3.3 V CMOS. Required bias supplies are +3.3 V & -3.3 V.

The MASW-011198 is designed for wideband applications such as Test and Measurement, Aerospace and Defense, Cellular infrastructure (5G millimeter-wave), military radios, radars, microwave radios and very small aperture terminals (VSATs).

The MASW-011198 is manufactured on a Silicon-on-Insulator process. The 3 mm laminate package is lead free and RoHS compliant.

Functional Schematic



Pin Configuration¹

Pin #	Pin Name	Description
1,2,4-7,9,10,13 16,17,19,20	GND	Ground
3	RFC ²	Common RF Input/Output
8	RF1 ²	RF Input/Output 1
11	VDD	+3.3V
12	VC	Control Voltage
14	VEN	Enable Voltage
15	VSS	-3.3V
18	RF2 ²	RF Input/Output 2

1. The exposed pad centered on the package bottom must be connected to RF, dc and thermal ground.
2. RF ports are dc-coupled to GND. There are no internal DC blocking capacitors. External dc blocking capacitor is not necessary if the RF line dc bias is 0V.

Ordering Information^{3,4}

Part Number	Package
MASW-011198-TR0500	500 Piece Reel
MASW-011198-SMB	Sample Board

3. Reference Application Note M513 for reel size information.
4. All sample boards include 3 loose parts.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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Electrical Specifications⁵:

$V_{DD} = +3.3\text{ V}$, $V_{SS} = -3.3\text{ V}$, $V_C = 0\text{ V}$ or 1.8 V , $T_{PADDLE} = 25^{\circ}\text{C}$, $Z_0 = 50\ \Omega$

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Insertion Loss	DC to 15 GHz	dB	—	0.9	—
	15 GHz			1.1	1.5
	30 GHz			1.5	2.0
	44 GHz			1.8	2.5
Isolation, Between RF1 to RF2	DC to 15 GHz	dB	—	68	—
	15 GHz			72	
	30 GHz			73	
	44 GHz			47	
Isolation, RFC to RF1 / RF2	DC to 15 GHz	dB	—	60	—
	15 GHz		55	60	
	30 GHz		50	58	
	44 GHz		40	43	
RFC Return Loss	DC - 44 GHz	dB	—	18	—
RF1 / RF2 Return Loss, Thru Port	DC - 44 GHz	dB	—	18	—
RF1 / RF2 Return Loss, Isolated Port	DC - 44 GHz	dB	—	18	—
Input P0.1dB	10 MHz - 44 GHz	dBm	—	27	—
Input P1dB	10 MHz - 44 GHz	dBm	—	28	—
Input IP3	Two tone, $P_{IN}/\text{tone} = +14\text{ dBm}$ 10 MHz - 44 GHz	dBm	—	52	—
T_{ON}	50% control to 90% RF	μs	—	0.9	—
T_{RISE}	10% to 90% RF	μs	—	0.35	—
T_{OFF}	50% control to 10% RF	μs	—	0.2	—
T_{FALL}	90% to 10% RF	μs	—	0.04	—
Voltage Supply, VDD	—	V	3.15	3.3	3.45
Voltage Supply, VSS	—	V	-3.45	-3.3	-3.15
Logic Voltage, Input Low (V_{IL})	—	V	0.0	—	0.8
Logic Voltage, Input High (V_{IH})	—	V	1.2	—	VDD
Supply Current, VDD	—	mA	—	0.3	0.5
Supply Current, VSS	—	mA	—	0.65	1.0
Logic Pin Current (VEN / VC)	Pulled down to GND with 100 k Ω resistor internally	μA	—	VC*10	—

5. Device shall be aligned to recommended PCB footprint within +/- 1 mil for optimum performance.

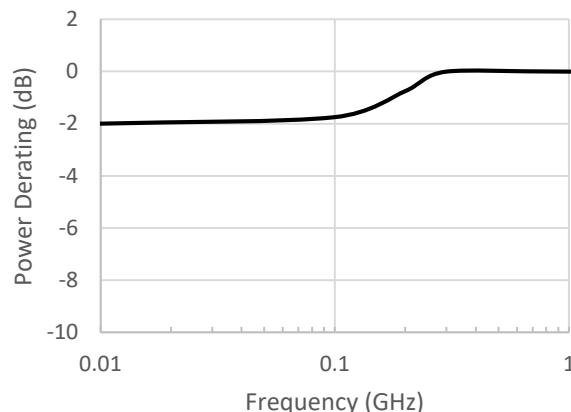
Maximum Operating Conditions

Parameter	Maximum
Input Power, 300 MHz to 44 GHz, RFC Port ⁶ RF1 / RF2 Port Thru Path ⁶ RF1 / RF2 Port Terminated Path ⁶	26 dBm 26 dBm 24 dBm
VDD	-0.3 to +3.45 V
VSS	-3.45 to +0.3 V
VC	-0.3 to 3.45 V
Operating Temperature ⁷	-40 to +105°C

6. T_{PADDLE} = +105°C. See power derating curves for details.

7. Guarantees 10 years lifetime.

Low Frequency Power Derating Detail⁶



Absolute Maximum Ratings^{8,9,10}

Parameter	Absolute Maximum
Input Power, 300 MHz to 44 GHz, RFC Port ⁶ RF1 / RF2 Port Thru Path ⁶ RF1 / RF2 Port Terminated Path ⁶	27 dBm 27 dBm 25 dBm
VDD	-0.3 to +3.6 V
VSS	-3.6 to +0.3 V
VC	-0.3 to 3.6 V
Junction Temperature	+135°C

8. Exceeding any one or combination of these limits may cause permanent damage to this device.

9. MACOM does not recommend sustained operation near these survivability limits.

10. Based on testing with input power applied for 30 seconds.

Truth Table

Enable	Control	Condition of Switch	
		RF1	RF2
V _{EN}	VC		
V _{IL}	V _{IL}	Off	On
V _{IL}	V _{IH}	On	Off
V _{IH}	V _{IL}	Off	Off
V _{IH}	V _{IH}	Off	Off

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Parameter	Rating	Standard
Human Body Model (HBM)	Class 1C	ESDA/JEDEC JS-001
Charged Device Model (CDM)	Class TBD	ESDA/JEDEC JS-002

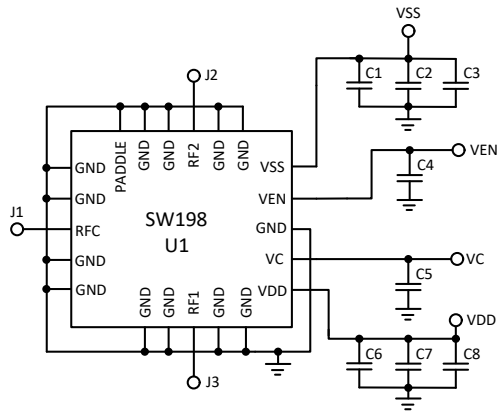
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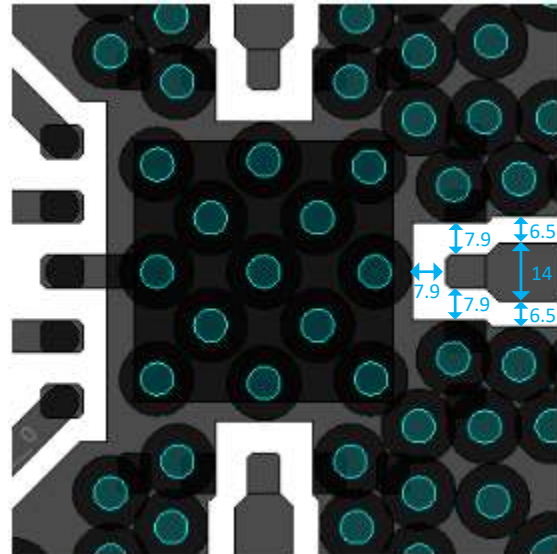
Application Schematic



Recommended PCB Footprint

MASW-011198-SMB is a 2-layer board with 8 mil Rogers RO4003 dielectric material and 1 oz. copper on top and bottom layers. For this stack-up, the recommended PCB footprint is shown below.

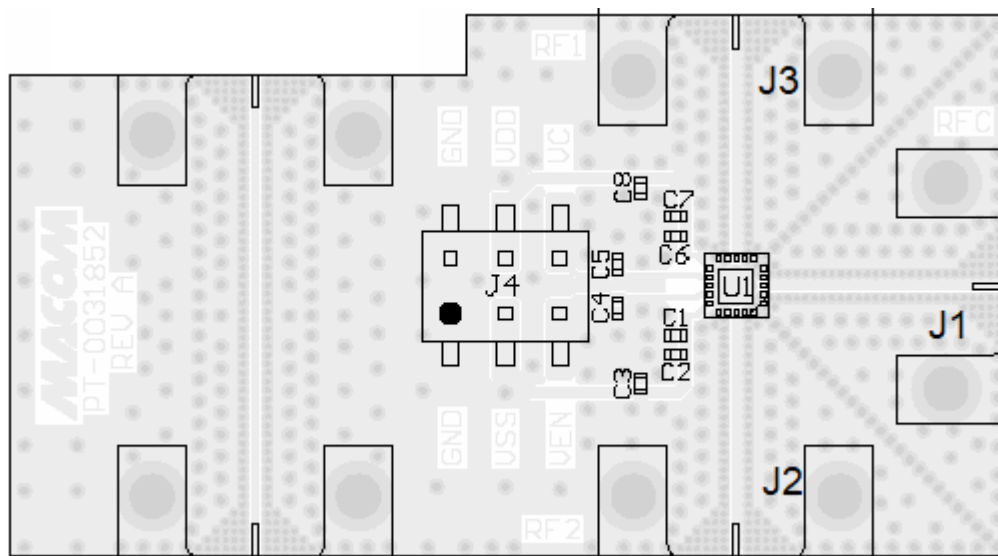
The 50Ω RF transmission lines are CPWG of 14 mil width with 6.5 mil gap.



Parts List

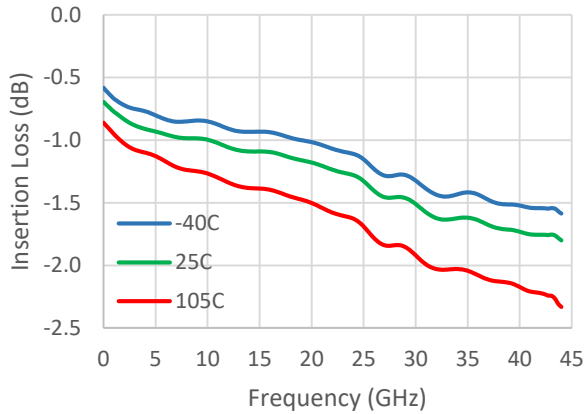
Part	Value	Case Style
U1	MASW-011198	3 mm, 20 Lead
C1, C6	Capacitor, 10 pF, 50 V	0402
C2, C7	Capacitor, 1000 pF, 25 V	0402
C3, C8	Capacitor, 1 μF, 10 V	0402
C4, C5	Capacitor, 5 pF, 16 V	0402
J1 - J3	Southwest 1492-04A-5	End Launch

Evaluation Board Layout

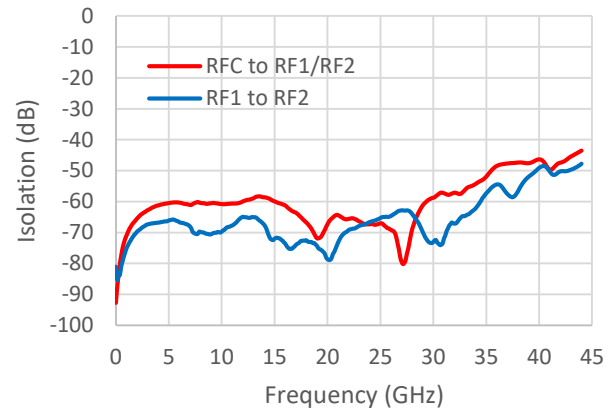


Typical Performance Curves

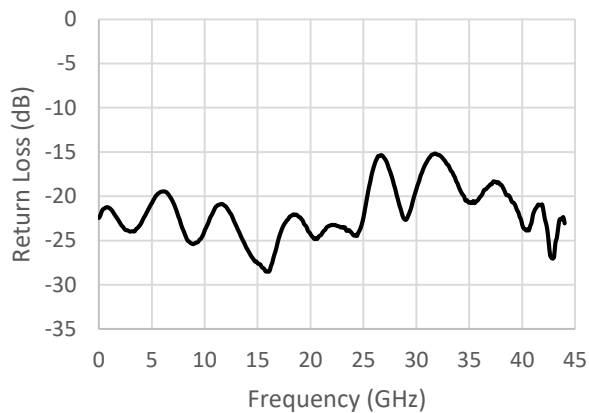
Insertion Loss¹¹



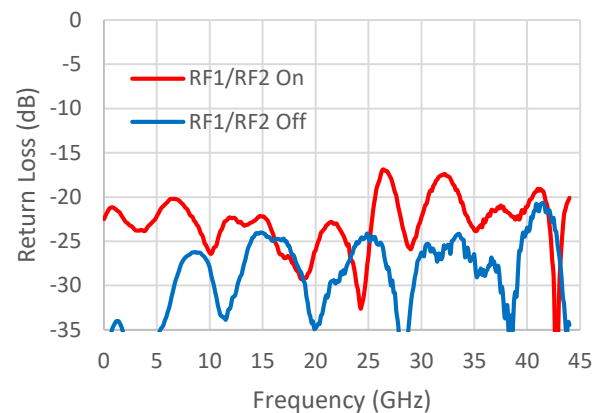
RFC to RF1 / RF2 Isolation¹¹



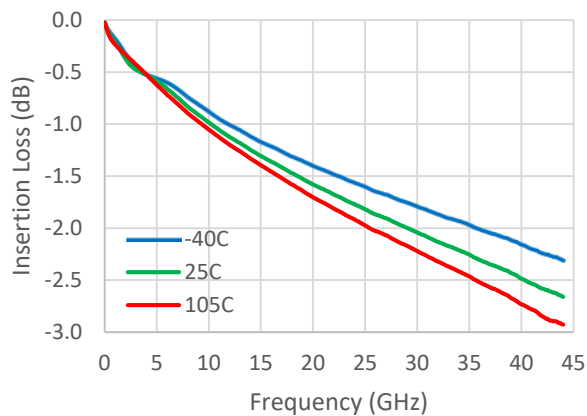
RFC Return Loss¹²



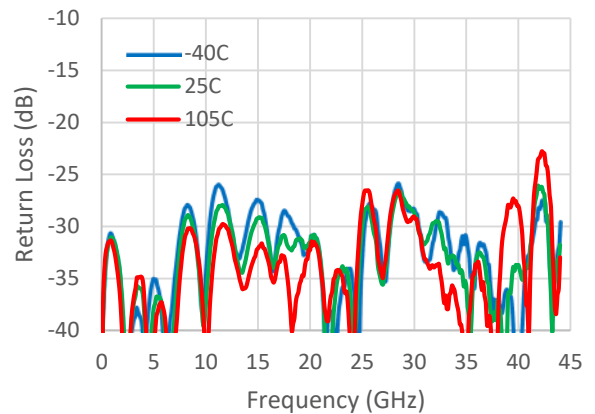
RF1 / RF2 Return Loss¹²



Evaluation Board Thru Line Insertion Loss



Evaluation Board Thru Line Return Loss



11. Insertion Loss and Isolation were measured using connectorized evaluation board, and normalized using the insertion loss of the 50 Ω thru line.

12. Return Loss were measured using connectorized evaluation board.

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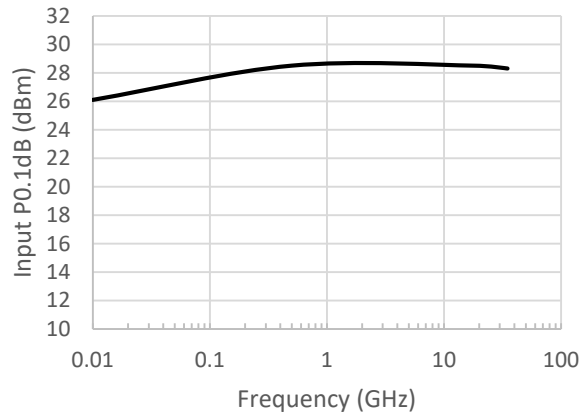


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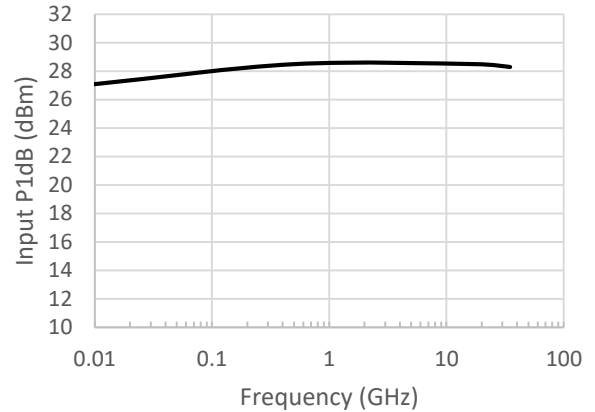
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Typical Performance Curves

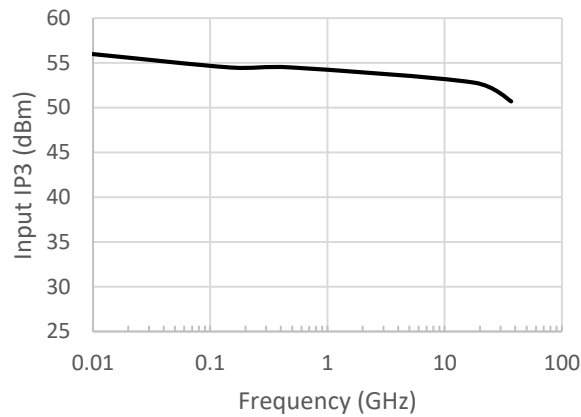
Input P0.1dB



Input P1dB



Input IP3¹³



13. Input IP3 were measured using connectorized evaluation board. The RF input power was 14 dBm per tone with spacing of 1 MHz.

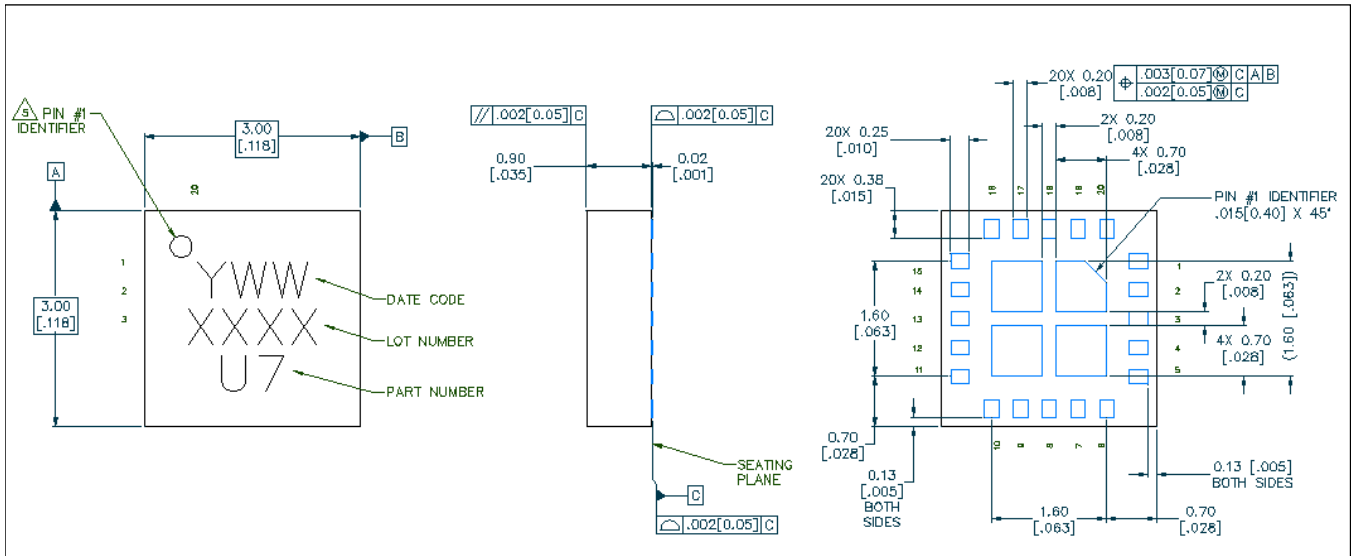
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Lead Free 3 mm 20-Lead Laminate Package †



† Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is ENEPIG Ni 3~9 µm / Pd 0.02~0.09 µm / Au 0.03 ~ 0.12 µm.

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