Unit: mm

0.4±0

2.8±0.3

1. GATE 2. SOURCE 3. DRAIN

2-3S1A

JEDEC JEITA TOSHIBA

Weight: 10 mg (typ.)

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSII)

SSM3J14T

Power Management Switch DC-DC Converters

- Suitable for high-density mounting due to compact package
- Low on Resistance: $R_{on} = 145 \text{ m}\Omega \text{ (max)} \text{ (@VGS} = -4.5 \text{ V)}$: $R_{on} = 85 \text{ m}\Omega \text{ (max)} \text{ (@VGS} = -10 \text{ V)}$
- High-speed switching

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-Source voltage		V _{DS}	-30	$(N \land$	
Gate-Source voltage		V _{GSS}	±20	V	
Drain current	DC	I _D	-2.7		
	Pulse	I _{DP} (Note 2)	-6.4	A	
Drain power dissipation		P _D t = 10 s (Note 1)	0.7	W	
Channel temperature		T _{ch} <	150	/°C	
Storage temperature range		T _{stg}	-55 to 150	J.C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in

temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on FR4 board

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t, Cu pad: } 645 \text{ mm}^2)$

Note 2: The pulse width limited by maximum channel temperature.

Marking Equivalent Circuit

Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

The Channel-to-Ambient thermal resistance Rth (ch-a) and the drain power dissipation PD vary according to the board material, board area, board thickness and pad area, and are also affected by the environment in which the product is used. When using this device, please take heat dissipation fully into account

Start of commercial production 2001-07

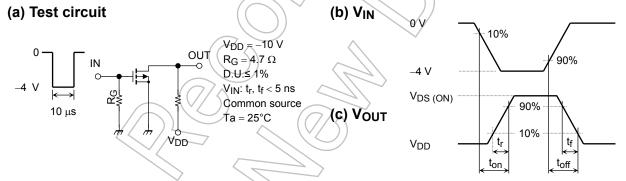
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	_	_	±1	μА	
Drain-source breakdown voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$	-30	_	_	V	
		V (BR) DSX	$I_D = -1 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	V	
Drain cut-off curren	nt	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0$			-1	μΑ	
Gate threshold volt	age	V _{th}	$V_{DS} = -5 \text{ V}, I_D = -0.1 \text{ mA}$	-0.8) /_	-2.0	V	
Forward transfer ad	dmittance	Y _{fs}	$V_{DS} = -5 \text{ V}, I_D = -1.35 \text{ A}$ (Note 3)	2.0	_	_	S	
Drain-source on resistance		R _{DS} (ON)	$I_D = -1.35 \text{ A}, V_{GS} = -10 \text{ V}$ (Note 3)	\mathcal{P}	63	85	mΩ	
			$I_D = -1.35 \text{ A}, V_{GS} = -4.5 \text{ V}$ (Note 3)		106	145		
			$I_D = -1.35 \text{ A}, V_{GS} = -4.0 \text{ V} \text{ (Note 3)}$	_	120	170		
Input capacitance		C _{iss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	413	_	pF	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	/	77	\nearrow	pF	
Output capacitance		Coss	$V_{DS} = -15 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	-6	113	> —	pF	
Switching time	Turn-on time	t _{on}	$V_{DD} = -15 \text{ V}, I_D = -1 \text{ A}$	~_(29) —	20	
	Turn-off time	t _{off}	$V_{GS} = 0$ to -4 V, $R_G = 10 \Omega$	1	29		ns	

Note 3: Pulse test

Switching Time Test Circuit





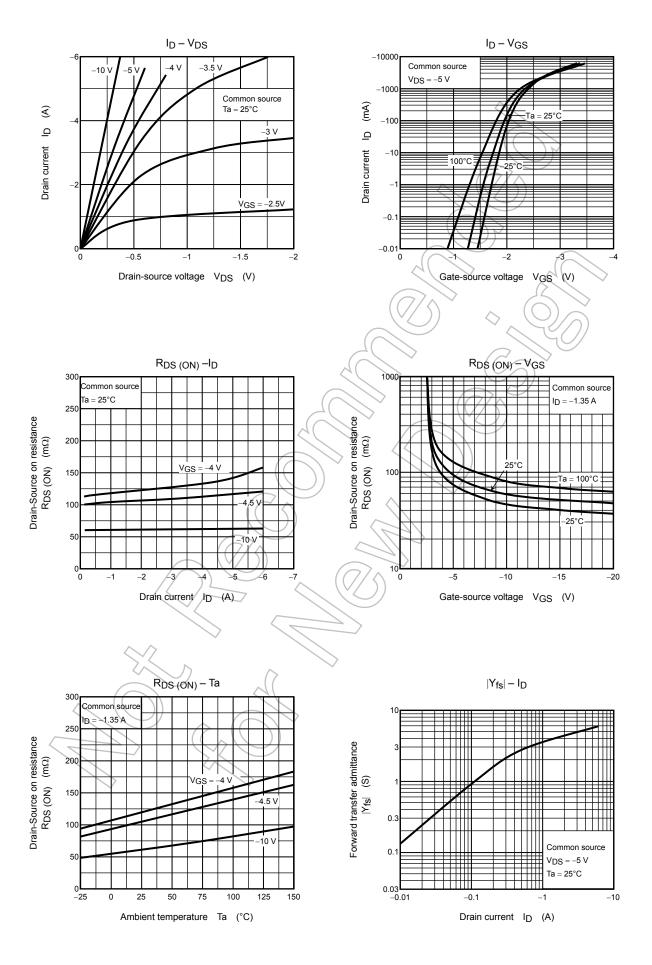
Precaution

 V_{th} can be expressed as voltage between gate and source when low operating current value is I_D = $-100~\mu A$ for this product. For normal switching operation, $V_{\rm GS}$ (on) requires higher voltage than $V_{\rm th}$ and $V_{\rm GS}$ (off) requires lower voltage than V_{th} .

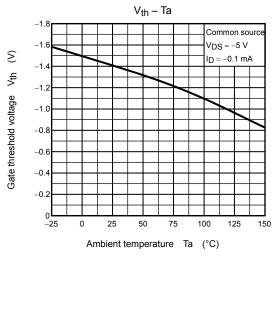
(relationship can be established as follows: V_{GS} (off) $< V_{th} < V_{GS}$ (on))

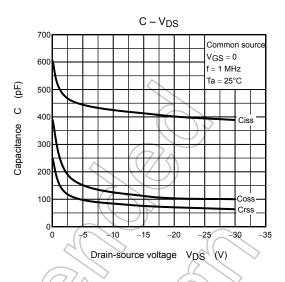
Please take this into consideration for using the device.

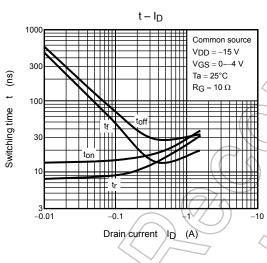
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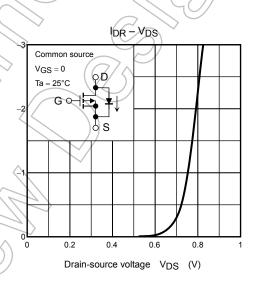


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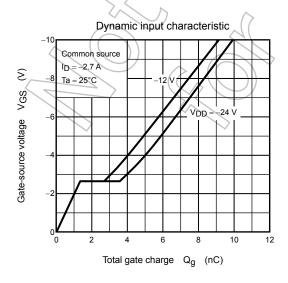


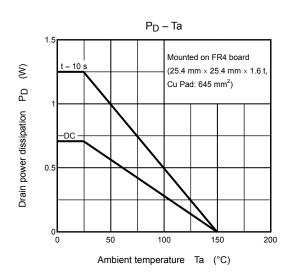


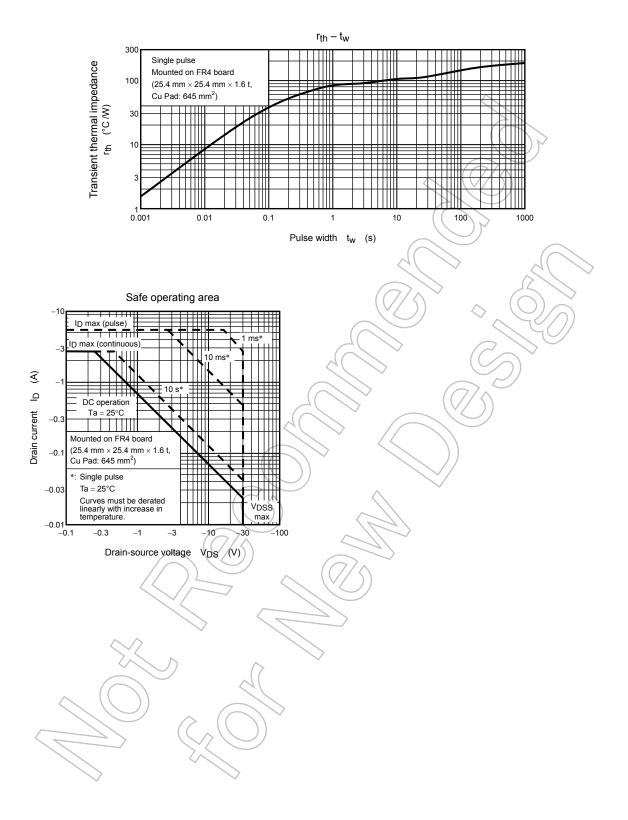


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Drain reverse current







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