



50V N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _A = +25°C
	2Ω @ V _{GS} = 5V	400mA
50V	2.5Ω @ V _G S = 2.5V	380mA
	4Ω @ V _{GS} = 1.8V	310mA

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- Load switches
- Level switches







Top View

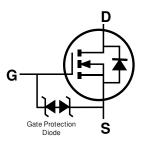
Features and Benefits

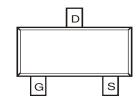
- Low On-Resistance
- Very Low Gate Threshold Voltage (1.0V max)
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **ESD Protected**
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

Mechanical Data

- Package: SOT23
- Package Material: UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish (Lead Free Plating). Solderable per MIL-STD-202, Method 208 @3
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)





Equivalent Circuit

Top View

Ordering Information (Note 4)

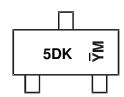
Part Number	Pookogo	Packing		
Part Number	Package	Qty.	Carrier	
DMN52D0U-7	SOT23	3,000	Reel	
DMN52D0U-13	SOT23	10,000	Reel	

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. Notes:

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



5DK = Product Type Marking Code $\overline{Y}M = Date Code Marking$ \overline{Y} = Year (ex: K = 2023) M = Month (ex: 9 = September)

Date Code Key

Date Code Ney												
Year	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	J	K	L	М	N	0	Р	R	S	Т	U	V
		l I		l I	I		l I					
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Drain-Source Voltage		V_{DSS}	50	V	
Gate-Source Voltage			Vgss	±12	V
Continuous Drain Current (Note 6) V _{GS} = 5V	I _D	400 320	mA		
Maximum Continuous Body Diode Forward Curr	Is	400	mA		
Pulsed Drain Current (10µs Pulse, Duty Cycle =	1%)		I _{DM}	1.2	Α

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.5	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Reja	257	°C/W
Total Power Dissipation (Note 6)		PD	0.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R _{0JA}	182	°C/W
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C

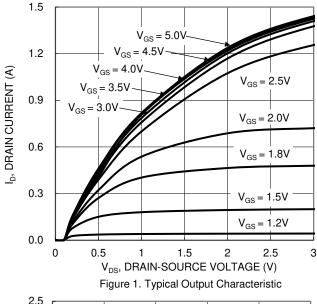
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)	, , , , , , , , , , , , , , , , , , ,						
Drain-Source Breakdown Voltage	BV _{DSS}	50	_	_	V	$V_{GS} = 0V, I_{D} = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 50V$, $V_{GS} = 0V$	
Gate-Source Leakage	lgss	_	_	±10	μΑ	$V_{GS} = \pm 12V$, $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	0.49	_	1.0	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
		_	1.6	4.0		$V_{GS} = 1.8V, I_D = 50mA$	
Static Drain-Source On-Resistance	R _{DS(ON)}		1.2	2.5	Ω	$V_{GS} = 2.5V, I_D = 50mA$	
		_	1.0	2.0		$V_{GS} = 5.0V, I_D = 50mA$	
Diode Forward Voltage	V _{SD}	_	0.6	1.2	V	V _{GS} = 0V, I _D = 50mA	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	39	_	pF	V 05V V 0V	
Output Capacitance	Coss	_	4.8	_	pF	V _{DS} = 25V, V _{GS} = 0V - f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	3.6	_	pF	1 - 1.000112	
Gate Resistance	Rg	_	47.8	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge (VGS = 4.5V)	Qg	_	0.8	_	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	_	1.5	_	nC	\/ O5\/ - 50mA	
Gate-Source Charge	Qgs	_	0.1	_	nC	$V_{DS} = 25V, I_{D} = 50mA$	
Gate-Drain Charge	Qgd	_	0.1	_	nC		
Turn-On Delay Time	tD(ON)	_	1.05	_	ns		
Turn-On Rise Time	tr	_	11.3	_	ns	V _{DS} = 25V, V _{GS} = 10V,	
Turn-Off Delay Time	t _{D(OFF)}	_	33	_	ns	$R_G = 50\Omega$, $I_D = 50mA$	
Turn-Off Fall Time	t _F	_	38.5	_	ns		

Notes:

- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.





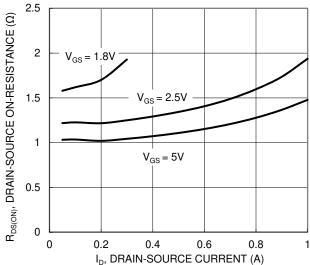


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

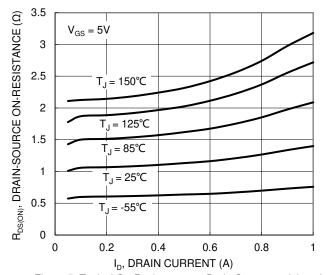
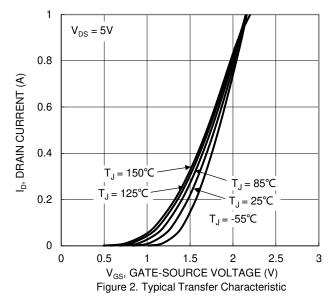
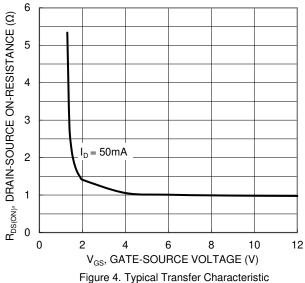


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature





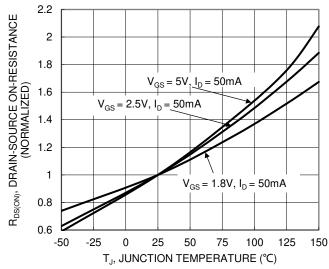


Figure 6. On-Resistance Variation with Junction Temperature



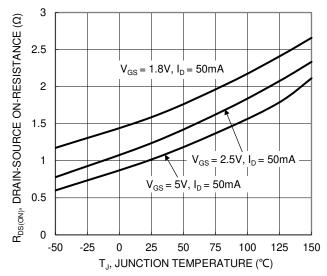


Figure 7. On-Resistance Variation with Junction Temperature

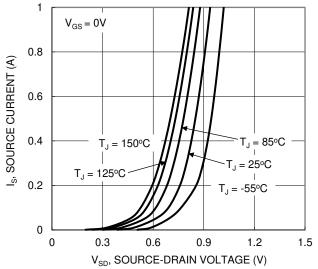
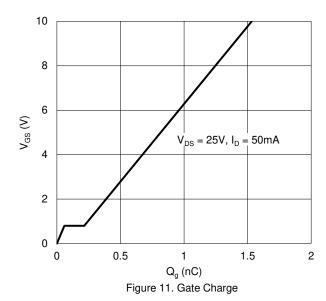


Figure 9. Diode Forward Voltage vs. Current



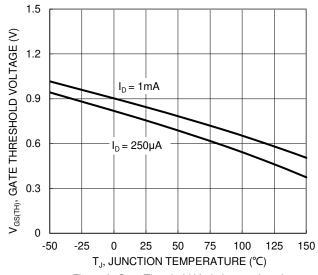
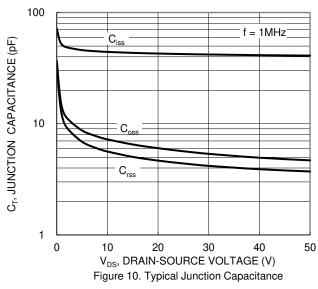
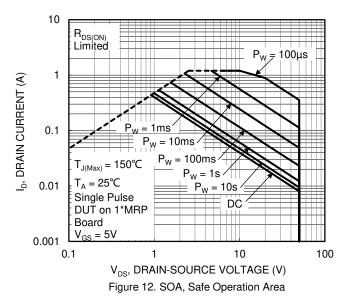


Figure 8. Gate Threshold Variation vs. Junction Temperature







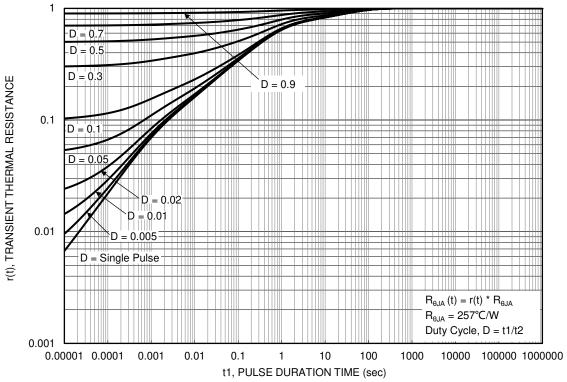


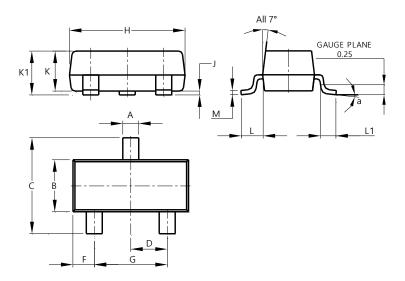
Figure 13. Transient Thermal Resistance



Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

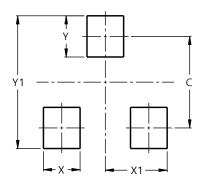


	SOT23							
Dim	Min	Max	Тур					
Α	0.37	0.51	0.40					
В	1.20	1.40	1.30					
С	2.30	2.50	2.40					
D	0.89	1.03	0.915					
F	0.45	0.60	0.535					
G	1.78 2.0	2.05	1.83					
Н	2.80	3.00	2.90					
J	0.013	0.05						
K	0.890	1.00	0.975					
K1	0.903	1.10	1.025					
L	0.45	0.61	0.55					
L1	0.25	0.55	0.40					
М	0.085	0.150	0.110					
а	0°	8°						
All	All Dimensions in mm							

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)				
С	2.0				
X	0.8				
X1	1.35				
Υ	0.9				
V1	2.0				

March 2023



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