

500 mA NPN general-purpose transistors

Rev. 3 — 22 July 2010

Product data sheet

1. Product profile

1.1 General description

NPN general-purpose transistors in a SOT323 (SC-70) very small Surface-Mounted Device (SMD) plastic package.

Table 1. Product overview

Type number	Package		PNP complement
	Nexperia	JEITA	
PMSTA05	SOT323	SC-70	PMSTA55
PMSTA06			PMSTA56

1.2 Features and benefits

- High current (max. 500 mA)
- Collector-emitter voltage:
 - ◆ 60 V (PMSTA05)
 - 80 V (PMSTA06)
- AEC-Q101 qualified
- Very small SMD plastic package

1.3 Applications

Primarily intended for telephony and professional communication equipment

1.4 Quick reference data

Table 2.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{CEO}	collector-emitter voltage	open base				
	PMSTA05		-	-	60	V
	PMSTA06		-	-	80	V
I _C	collector current		-	-	500	mA
h _{FE}	DC current gain	V _{CE} = 2 V; I _C = 10 mA	50	-	-	
		V _{CE} = 1 V; I _C = 100 mA	[1] 50	-	-	

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2. Pinning information

Table 3.	Pinning	
Pin	Description	Simplified outline Graphic symbol
1	base	
2	emitter	
3	collector	
		sym021

3. Ordering information

Table 4. Ordering information					
Type number Package					
	Name	Description	Version		
PMSTA05	SC-70	plastic surface-mounted package; 3 leads	SOT323		
PMSTA06					

4. Marking

Table 5. Marking codes	
Type number	Marking code ^[1]
PMSTA05	*1H
PMSTA06	*1G

[1] * = -: made in Hong Kong

* = p: made in Hong Kong

* = t: made in Malaysia

* = W: made in China

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5. Limiting values

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{CBO}	collector-base voltage	open emitter			
	PMSTA05		-	60	V
	PMSTA06		-	80	V
V _{CEO}	collector-emitter voltage	open base			
	PMSTA05		-	60	V
	PMSTA06		-	80	V
V _{EBO}	emitter-base voltage	open collector	-	4	V
l _C	collector current		-	500	mA
I _{CM}	peak collector current		-	500	mA
I _{BM}	peak base current		-	500	mA
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	<u>[1]</u> -	200	mW
Tj	junction temperature		-	150	°C
T _{amb}	ambient temperature		-65	+150	°C
T _{stg}	storage temperature		-65	+150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB).

6. Thermal characteristics

Table 7.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from junction to ambient	in free air	<u>[1]</u> -	-	625	K/W

[1] Device mounted on an FR4 PCB.

7. Characteristics

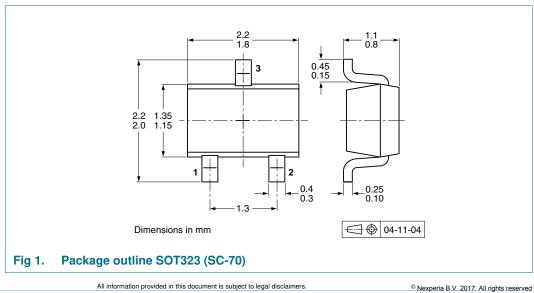
Table 8. T _{amb} = 25	Characteristics 5 °C unless otherwise spe	ecified.					
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current						
	PMSTA05	$V_{CB} = 60 \text{ V}; I_E = 0 \text{ A}$		-	-	100	nA
	PMSTA06	$V_{CB} = 80 \text{ V}; I_E = 0 \text{ A}$		-	-	100	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 3 \text{ V}; \text{ I}_{C} = 0 \text{ A}$		-	-	500	nA
h _{FE}	DC current gain	$V_{CE} = 2 \text{ V}; \text{ I}_{C} = 10 \text{ mA}$		50	-	-	
		$V_{CE} = 1 V; I_{C} = 100 mA$	[1]	50	-	-	
V _{CEsat}	collector-emitter saturation voltage	$I_{\rm C}$ = 100 mA; $I_{\rm B}$ = 10 mA	<u>[1]</u>	-		250	mV
V _{BEsat}	base-emitter saturation voltage	$I_{\rm C} = 100 \text{ mA};$ $I_{\rm B} = 10 \text{ mA}$	[1]	-	-	900	mV
V _{BE}	base-emitter voltage	$I_{C} = 100 \text{ mA}; V_{CE} = 1 \text{ V}$		-	-	1.2	V
f _T	transition frequency	V_{CE} = 2 V; I_{C} = 10 mA; f = 100 MHz		100	-	-	MHz

8. Test information

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



PMSTA05 06

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10. Packing information

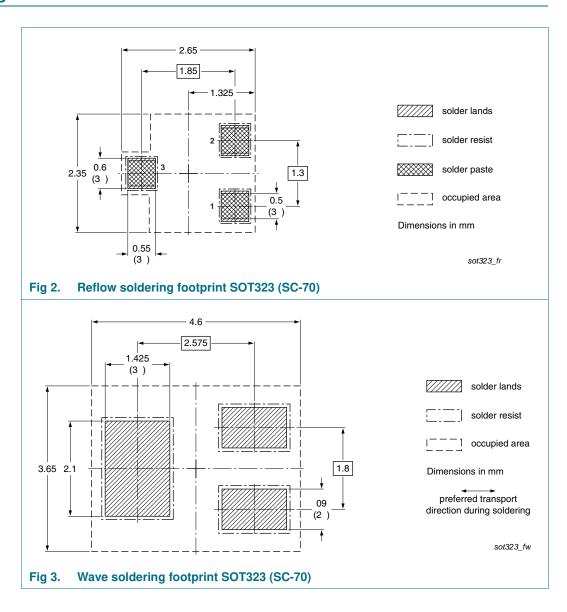
Table 9. Packing methods

The indicated -xxx are the last three digits of the 12NC ordering code.[1]

Type number	Package	Description	scription Packing quantity	
			3000	10000
PMSTA05	SOT323	4 mm pitch, 8 mm tape and reel	-115	-135
PMSTA06				

[1] For further information and the availability of packing methods, see Section 14.

11. Soldering



12. Revision history

Table 10. **Revision history Document ID Release date** Data sheet status Change notice Supersedes PMSTA05 06 v.3 20100722 Product data sheet PMSTA05 06 2 _ Modifications: · The format of this data sheet has been redesigned to comply with the new identity guidelines of NXP Semiconductors. Legal texts have been adapted to the new company name where appropriate. • Section 1 "Product profile": amended ٠ Section 3 "Ordering information": added ٠ • Section 4 "Marking": updated • Section 8 "Test information": added Figure 1: superseded by minimized package outline drawing ٠ Section 10 "Packing information": added ٠ ٠ Section 11 "Soldering": added Section 13 "Legal information": updated ٠ PMSTA05 06 2 19990429 Product specification PMSTA05 06 1 _ PMSTA05_06_1 19970616 Product specification --

PMSTA05_06 Product data sheet

13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL http://www.nexperia.com.

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