SF21G - SF28G

Taiwan Semiconductor

2A, 50V - 600V Super Fast Rectifier

FEATURES

- AEC-Q101 qualified available
- Glass passivated chip junction
- High efficiency, Low V_F
- High current capability
- High surge current capability
- Low power loss
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- Freewheeling application

MECHANICAL DATA

- Case: DO-204AC (DO-15)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Pure tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- · Polarity: Indicated by cathode band
- Weight: 0.400g (approximately)

KEY PARAMETERS				
PARAMETER	VALUE	UNIT		
I _F	2	А		
V _{RRM}	50 - 600	V		
I _{FSM}	50	А		
T _{J MAX}	150	°C		
Package	DO-204AC (DO-15)			
Configuration	Single die			









Cathode -Anode

ABSOLUTE MAXIMUM RATINGS (T _A = 25°C unless otherwise noted)										
PARAMETER	SYMBOL	SF 21G	SF 22G	SF 23F	SF 24G	SF 25G	SF 26G	SF 27G	SF 28G	υνιτ
Marking code on the device		SF21G	SF22G	SF23F	SF24G	SF25G	SF26G	SF27G	SF28G	
Repetitive peak reverse voltage	V _{RRM}	50	100	150	200	300	400	500	600	V
Reverse voltage, total rms value	$V_{R(RMS)}$	35	70	105	140	210	280	350	420	V
Forward current	١ _F				:	2				А
Surge peak forward current, 8.3ms single half sine wave superimposed on rated load	I _{FSM}	50				A				
Junction temperature	TJ	-55 to +150			°C					
Storage temperature	T _{STG}	-55 to +150			°C					





THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-ambient thermal resistance	R _{eja}	65	°C/W	
Junction-to-case thermal resistance	R _{eJC}	16	°C/W	

ELECTRICAL SPECIFICATIONS (T _A = 25°C unless otherwise noted)						
PARAMETER		CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Si Si Si Si				-	0.95	V
Forward voltage ⁽¹⁾	SF25G SF26G	- I _F = 2A, I _J = 25°C -	V _F	-	1.30	V
	SF27G SF28G			-	1.70	V
Reverse current @ rated V _R ⁽²⁾		$T_J = 25^{\circ}C$	1	-	5	μA
		T _J = 125°C	I _R	-	100	μA
lunction conscitance	SF21G SF22G SF23G SF24G		Сյ	40	-	pF
Junction capacitance	SF25G SF26G SF27G SF28G	1MHz, V _R = 4.0V	0J	20	-	pF
Reverse recovery time		$I_F = 0.5A, I_R = 1.0A,$ $I_{rr} = 0.25A$	t _{rr}	-	35	ns

Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION		
ORDERING CODE ⁽¹⁾⁽²⁾	PACKAGE	PACKING
SF2xG	DO-204AC (DO-15)	3,500 / Tape & Reel
SF2xG A0G	DO-204AC (DO-15)	1,500 / Ammo box
SF2xGH	DO-204AC (DO-15)	3,500 / Tape & Reel
SF2xGHA0G	DO-204AC (DO-15)	1,500 / Ammo box

Notes:

1. "x" defines voltage from 50V (SF21G) to 600V (SF28G)

2. "H" means AEC-Q101 qualified



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

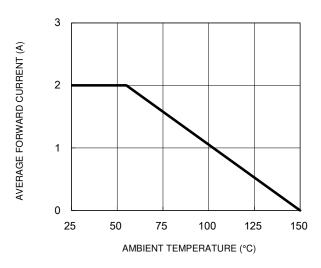
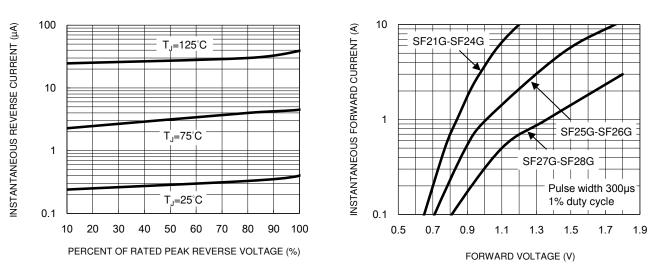


Fig.1 Forward Current Derating Curve

Fig.3 Typical Reverse Characteristics



100

10

1

1

CAPACITANCE (pF)

Fig.5 Maximum Non-Repetitive Forward Surge Current

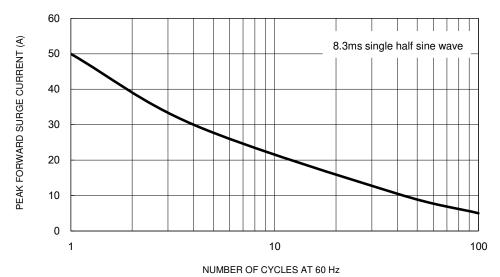


Fig.2 Typical Junction Capacitance

10

REVERSE VOLTAGE (V)

Fig.4 Typical Forward Characteristics

100

SF25G-SF28G

f=1.0MHz Vsig=50mVp-p SF21G-SF24G



CHARACTERISTICS CURVES

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$

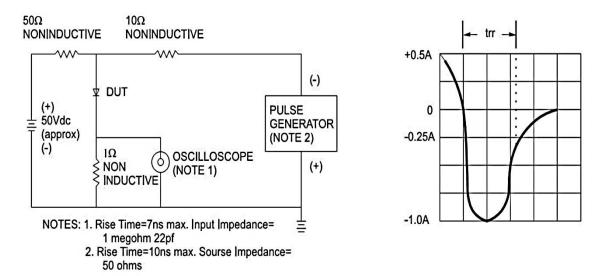
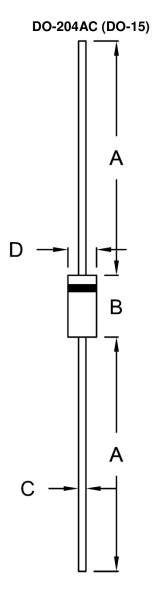


Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram



PACKAGE OUTLINE DIMENSIONS



DIM.	Unit	(mm)	Unit (inch)		
	Min.	Max.	Min.	Max.	
A	25.40	-	1.000	-	
В	5.80	7.60	0.228	0.299	
С	0.70	0.90	0.028	0.035	
D	2.60	3.60	0.102	0.142	

MARKING DIAGRAM



P/N	= Marking Code
G	= Green Compound
YWW	= Date Code
F	= Factory Code



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