



N-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
20	0.065 at V _{GS} = 4.5 V	3.9		
	0.075 at V _{GS} = 2.5 V	3.6		
	0.096 at V _{GS} = 1.8 V	3.2		

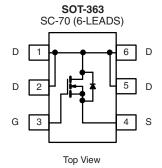
FEATURES

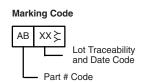
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFETs: 1.8 V Rated
- Thermally Enhanced SC-70 Package
- Compliant to RoHS Directive 2002/95/EC



APPLICATIONS

- · Load Switching
- PA Switch
- · Level Switch





Ordering Information: Si1406DH-T1-E3 (Lead (Pb)-free)

Si1406DH-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS	$T_A = 25 ^{\circ}C$, unles	ss otherwise r	noted		
Parameter		Symbol	5 s	Steady State	Unit
Drain-Source Voltage		V _{DS}	20		V
Gate-Source Voltage		V_{GS}	± 8		
Outime Dai Out (T. 150.00)	T _A = 25 °C	I _D	3.9	3.1	
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		2.8	2.2	
Pulsed Drain Current		I _{DM}	10		Α
Continuous Source Current (Diode Conduction) ^a		I _S	1.4	0.9	
Maximum Power Dissipation ^a	T _A = 25 °C	В	1.56	1.0	14/
	T _A = 85 °C	P_{D}	0.81	0.52	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Manipulation to Auditoria	t ≤ 5 s	- R _{thJA}	60	80	°C/W
Maximum Junction-to-Ambient ^a	Steady State		100	125	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	34	45	

Note:

a. Surface mounted on 1" x 1" FR4 board.

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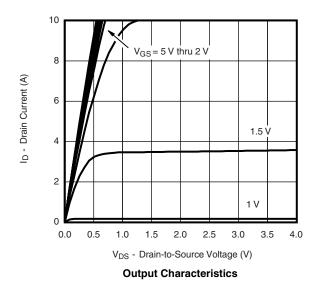
SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$	0.45		1.2	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
Zava Cata Valtaga Drain Current	I _{DSS}	$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current		$V_{DS} = 20 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			5	- μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 4.5 \text{ V}$	8			Α	
		$V_{GS} = 4.5 \text{ V}, I_D = 3.9 \text{ A}$		0.053	0.065		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = 2.5 V, I _D = 3.6 A		0.062	0.075	Ω	
		V _{GS} = 1.8 V, I _D = 2 A		0.079	0.096		
Forward Transconductance ^a	9 _{fs}	$V_{DS} = 10 \text{ V}, I_{D} = 3.9 \text{ A}$		11		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = 1.4 A, V_{GS} = 0 V$		0.75	1.1	V	
Dynamic ^b							
Total Gate Charge	Q_g			4.9	7.5		
Gate-Source Charge	Q_{gs} $V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 3.9 \text{ A}$		1.0		nC		
Gate-Drain Charge	Q_{gd}			0.95			
Turn-On Delay Time	t _{d(on)}			27	41		
Rise Time	t _r	V_{DD} = 10 V, R_L = 20 Ω		47	71		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong 0.5~A,~V_{GEN}=4.5~V,~R_g=6~\Omega$		54	81	ns	
Fall Time	t _f			29	44		
Source-Drain Reverse Recovery	t _{rr}	I _F = 1.4 A, dI/dt = 100 A/μs		35	60		

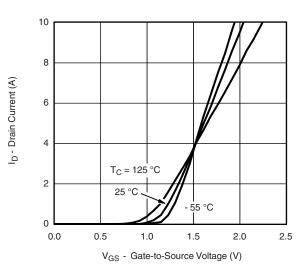
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



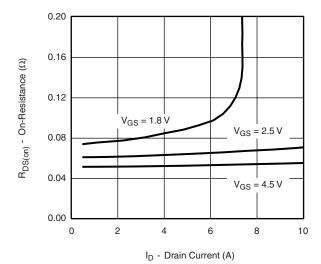




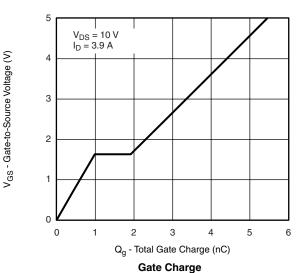


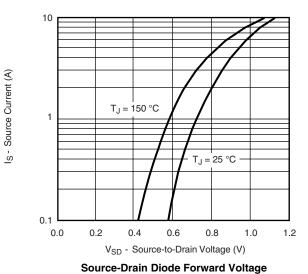


TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



On-Resistance vs. Drain Current



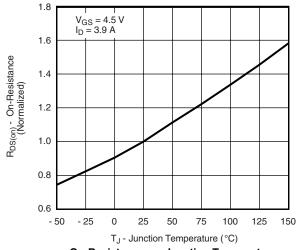


600 C_{iss}

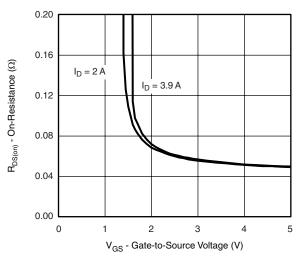
200 C_{rss}

0 4 8 12 16 20

V_{DS} - Drain-to-Source Voltage (V) **Capacitance**



On-Resistance vs. Junction Temperature

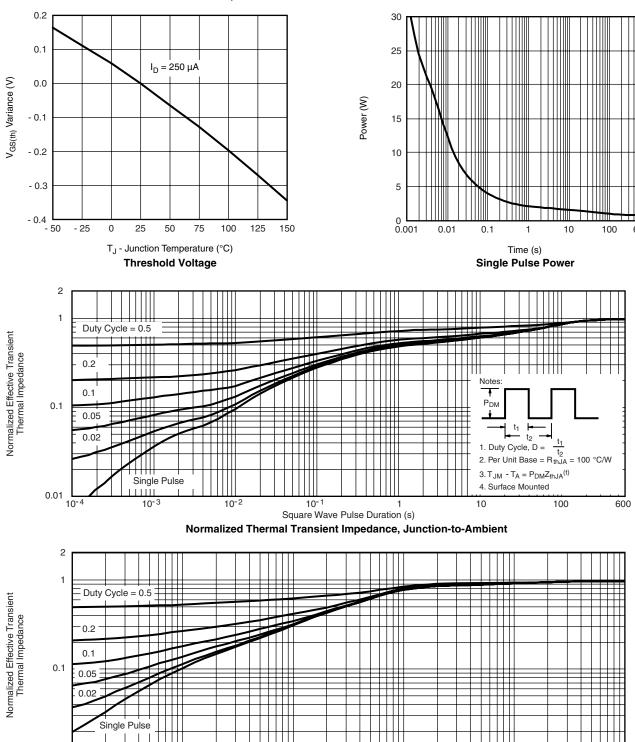


On-Resistance vs. Gate-to-Source Voltage

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Square Wave Pulse Duration (s)

Normalized Thermal Transient Impedance, Junction-to-Foot

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