



DMP2021UFDE

Product Summary

BVDSS	Rds(on) Max	I _D Max T _A = +25°C
-20V	16mΩ @ V _{GS} = -4.5V	-9.0A
-201	22mΩ @ V _{GS} = -2.5V	-7.7A

Description and Applications

This MOSFET is designed to minimize the on-state resistance (RDS(ON)) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Battery Management Application
- Power Management Functions
- DC-DC Converters

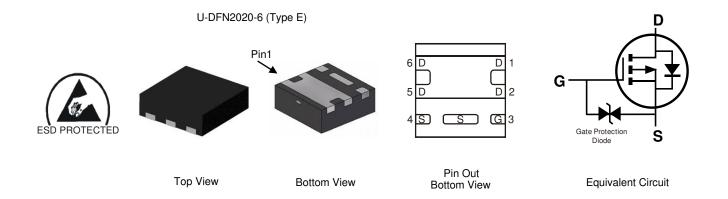
P-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

- 0.6mm Profile Ideal for Low Profile Applications
- Low Gate Threshold Voltage
- Low On-Resistance
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please <u>contact us</u> or your local Diodes representative. <u>https://www.diodes.com/quality/product-definitions/</u>

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 @4
- Weight: 0.007 grams (Approximate)



Ordering Information (Note 4)

Part Number	Case	Packaging
DMP2021UFDE-7	U-DFN2020-6 (Type E)	3,000/Tape & Reel
DMP2021UFDE-13	U-DFN2020-6 (Type E)	10,000/Tape & Reel

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



Site 1



FP = Product Type Marking Code YM = Date Code Marking Y = Year (ex: H = 2020) M = Month (ex: 9 = September)

Date	Code	Key

Date Code Key												
Year	2016		2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	D		Н	I	J	K	L	М	N	0	Р	R
	-			-		-		-	_	-		
Month	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec

Site 2



FP = Product Type Marking Code YWX = Date Code Marking Y = Year (ex: 0 = 2020) W = Week (ex: a = Week 27; z Represents Week 52 and 53) X = Internal Code (ex: U = Monday)

Date Code Key

Year	2016	 2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	6	 0	1	2	3	4	5	6	7	8	9

Week	1-26	27-52	53
Code	A-Z	a-z	Z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	Т	U	V	W	Х	Y	Z



Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage			VDSS	-20	V
Gate-Source Voltage			V _{GSS}	±10	V
	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lo	-9.0 -7.2	А
Continuous Drain Current (Note 6) $V_{GS} = -4.5V$	t<10s	T _A = +25°C T _A = +70°C	lo	-11.1 -8.9	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%))		I _{DM}	-60	А
Continuous Source-Drain Diode Current (Note 6)		TA = +25°C	ls	-2.4	А
Avalanche Current (Note 7) L = 0.1mH	las	-27	А		
Avalanche Energy (Note 7) L = 0.1mH			Eas	38	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Tetal Dower Discipation (Note 5)	T _A = +25°C	D	0.76	14/	
Fotal Power Dissipation (Note 5)	$T_A = +70^{\circ}C$	PD	0.48	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady state	D	165	°C/W	
	t<10s	R _{θJA}	116		
Tatal Dawar Diasiantian (Nata C)	TA = +25°C	D-	1.90	W	
Total Power Dissipation (Note 6)	T _A = +70°C	PD	1.20	vv	
Thermal Resistance, Junction to Ambient (Note 6)	Steady state	D	67		
memai resistance, sunction to Ambient (Note 6)	t<10s	Reja	47	°C/W	
Thermal Resistance, Junction to Case (Note 6)	Steady state	Rejc	18		
Operating and Storage Temperature Range		TJ, TSTG	-55 to +150	°C	

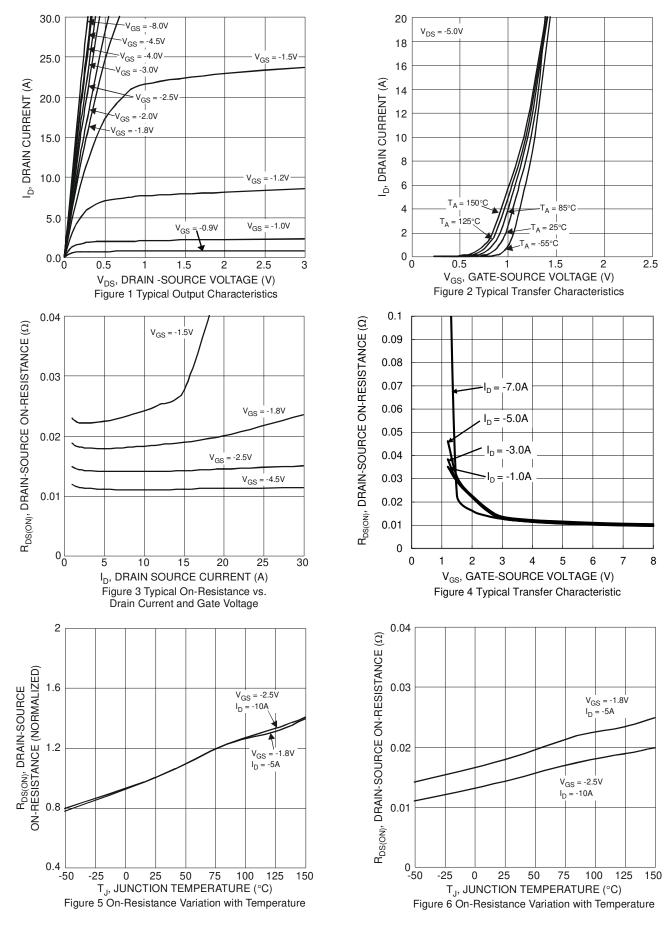
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						·
Drain-Source Breakdown Voltage	BVDSS	-20	-	_	V	V _{GS} = 0V, I _D = -250µA
Zero Gate Voltage Drain Current TJ = +25°C	IDSS	_	_	-1	μA	$V_{DS} = -20V, V_{GS} = 0V$
Gate-Source Leakage	lgss	_		±10	μA	$V_{GS} = \pm 8V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	VGS(TH)	-0.35	_	-1.0	V	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$
			12	16		$V_{GS} = -4.5V, I_{D} = -7.0A$
Static Drain-Source On-Resistance	Decient		15	22	mΩ	$V_{GS} = -2.5V, I_{D} = -5.0A$
Static Drain-Source On-Resistance	RDS(ON)	_	19	40	11152	$V_{GS} = -1.8V, I_{D} = -3.0A$
			21	80		$V_{GS} = -1.5V, I_D = -1.0A$
Diode Forward Voltage	Vsd		-0.8	-1.2	V	VGS = 0V, IS = -1.0A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	Ciss		2,760	—		
Output Capacitance	Coss	_	262	—	pF	$V_{DS} = -15V, V_{GS} = 0V,$ f = 1.0MHz
Reverse Transfer Capacitance	Crss		220	—		
Gate Resistance	Rg		16	—	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge (V _{GS} = -4.5V)	Qg	—	34	—		
Total Gate Charge (V _{GS} = -8V)	Qg	_	59	—	nC	
Gate-Source Charge	Qgs	—	3.5	—	no	$V_{DS} = -15V, I_{D} = -4.0A$
Gate-Drain Charge	Qgd	_	8.3	—		
Turn-On Delay Time	td(ON)	_	7.5	—		
Turn-On Rise Time	tR		25			V _{DS} = -15V, V _{GS} = -4.5V,
Turn-Off Delay Time	tD(OFF)		125		ns	$R_G = 1\Omega$, $I_D = -4.0A$
Turn-Off Fall Time	tF		96			
Reverse Recovery Time	t _{RR}	_	48		ns	I _F = -1.0A, di/dt = 100A/µs
Reverse Recovery Charge	QRR	_	33		nC	IF = -1.0A, di/dt = 100A/µs

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout. 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate. 7. I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep $T_J = +25^{\circ}$ C. 8. Short duration pulse test used to minimize self-heating effect. 9. Guaranteed by design. Not subject to product testing.

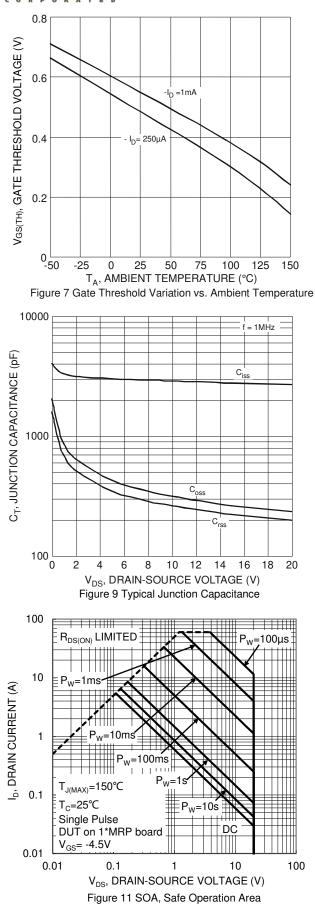


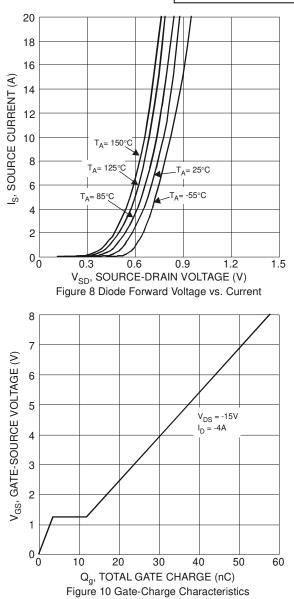
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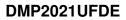




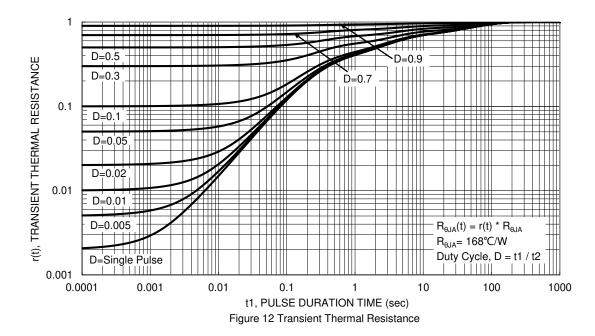










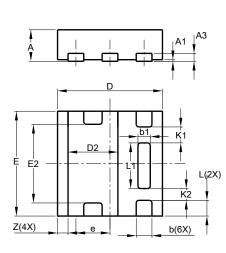




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type E)

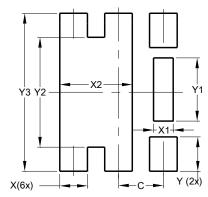


	U-DFN2020-6 Type E							
Dim	Min	Max	Тур					
Α	0.57	0.63	0.60					
A1	0	0.05	0.03					
A3	_	_	0.15					
b	0.25	0.35	0.30					
b1	0.185	0.285	0.235					
D	1.95	2.05	2.00					
D2	0.85	1.05	0.95					
Е	1.95	2.05	2.00					
E2	1.40	1.60	1.50					
e	-	-	0.65					
L	0.25	0.35	0.30					
L1	0.82	0.92	0.87					
K1	-	_	0.305					
K2	-	-	0.225					
Z	_	_	0.20					
All	Dimer	isions i	in mm					

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

U-DFN2020-6 (Type E)



Dimensions	Value
Dimensions	(in mm)
С	0.650
X	0.400
X1	0.285
X2	1.050
Y	0.500
Y1	0.920
Y2	1.600
Y3	2.300



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