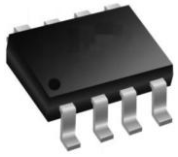
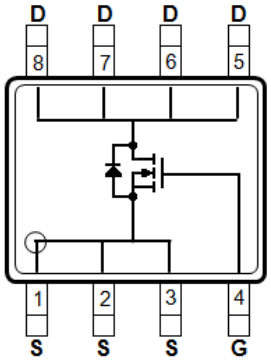
PJL9480

150V N-Channel Enhancement Mode MOSFET

Voltage	150 V	Current	4 A

SOP-8





Features

- RDS(ON), VGS@10V, ID@4A<65mΩ
- RDS(ON), VGS@6V, ID@2A<85mΩ
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS2.0 (2011/65/EU & 2015/865/EU directive)
- Green molding compound as per IEC61249 Std.. (Halogen Free)

Mechanical Data

- Case: SOP-8 package
- Terminals: Solderable per MIL-STD-750, Method 2026

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V_{DS}	150	V
Gate-Source Voltage		V_{GS}	±25	V
Continuous Drain Current	T_A=25°C	I_D	4.0	A
	T_A=70°C		3.2	
Pulsed Drain Current (Note 1)		I_{DM}	16	A
Power Dissipation	T_A=25°C	P_D	2.5	W
	T_A=70°C		1.6	
Operating Junction and Storage Temperature Range		T_J,T_{STG}	-55~150	°C
Typical Thermal Resistance - Junction to Ambient, t≦10s (Note 6)		$R_{\theta JA}$	50	°C/W

● Limited only By Maximum Junction Temperature

PJL9480

Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V,I_D=250uA	150	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250uA	2.0	2.7	4.0	V
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=10V,I_D=4A	-	52	65	mΩ
Drain-Source On-State Resistance	$R_{DS(on)}$	V_{GS}=6V,I_D=2A	-	60	85	mΩ
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=150V,V_{GS}=0V	-	-	1.0	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±25V,V_{DS}=0V	-	-	±100	nA
Dynamic (Note 7)						
Total Gate Charge	Q_g	V_{DS}=75V, I_D=4A, V_{GS}=10V (Note 1,2)	-	29.5	-	nC
Gate-Source Charge	Q_{gs}		-	9.2	-	
Gate-Drain Charge	Q_{gd}		-	8.0	-	
Input Capacitance	Ciss	V_{DS}=30V, V_{GS}=0V, f=1.0MHZ	-	1764	-	pF
Output Capacitance	Coss		-	148	-	
Reverse Transfer Capacitance	Crss		-	62	-	
Turn-On Delay Time	$td_{(on)}$	V_{DS}=30V,I_D=1A, V_{GS}=10V, R_G=6Ω (Note 1,2)	-	14	-	ns
Turn-On Rise Time	tr		-	21	-	
Turn-Off Delay Time	$td_{(off)}$		-	32	-	
Turn-Off Fall Time	tf		-	23	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I_S	---	-	-	4.0	A
Diode Forward Voltage	V_{SD}	I_S=1A, V_{GS}=0V	-	0.7	1.0	V

NOTES :

1. Pulse width≤300us, Duty cycle≤2%
2. Essentially independent of operating temperature typical characteristics.
3. The maximum current rating is package limited.
4. Repetitive rating, pulse width limited by junction temperature TJ(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial TJ =25°C.
5. $R_{\Theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 $inch^2$ with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

PJL9480

TYPICAL CHARACTERISTIC CURVES

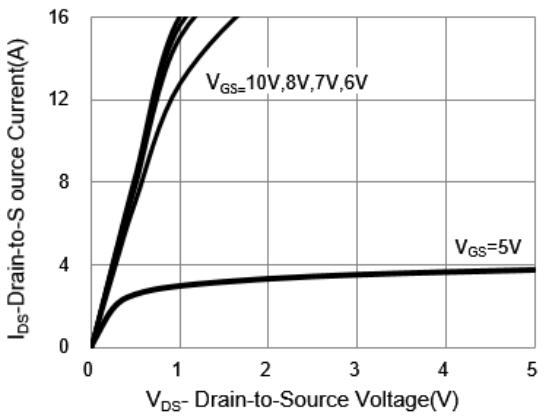


Fig.1 Output Characteristics

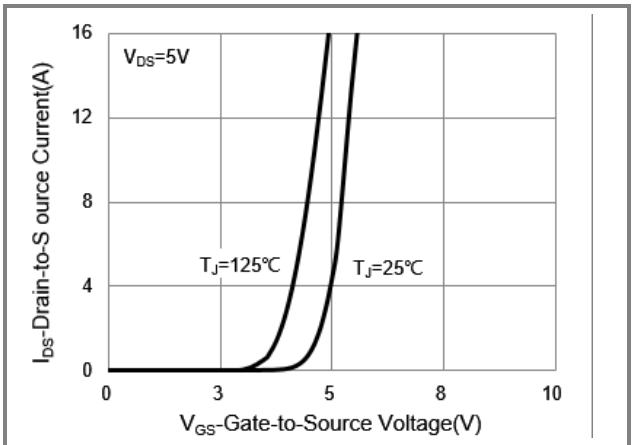


Fig.2 Transfer Characteristics

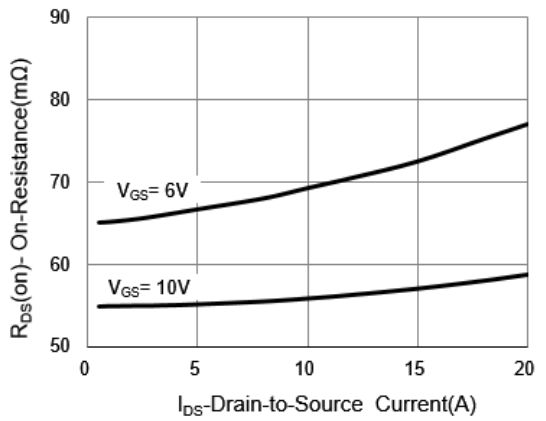


Fig.3 On-Resistance vs. Drain Current

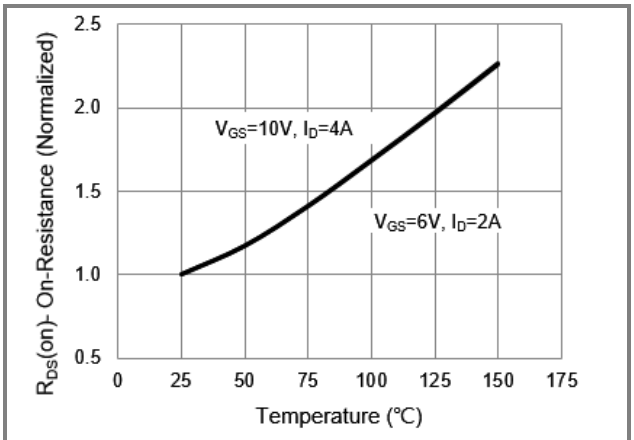


Fig.4 On-Resistance vs. Junction temperature

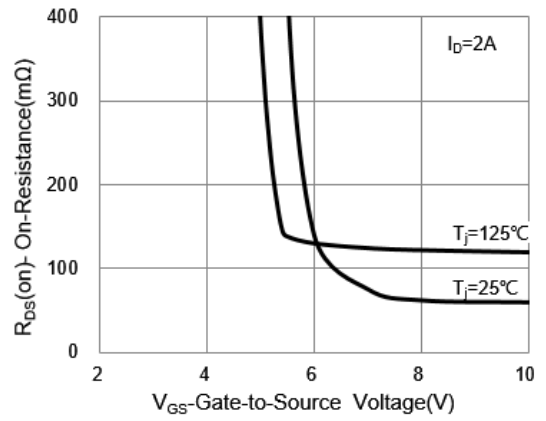


Fig.5 On-Resistance Variation with VGS.

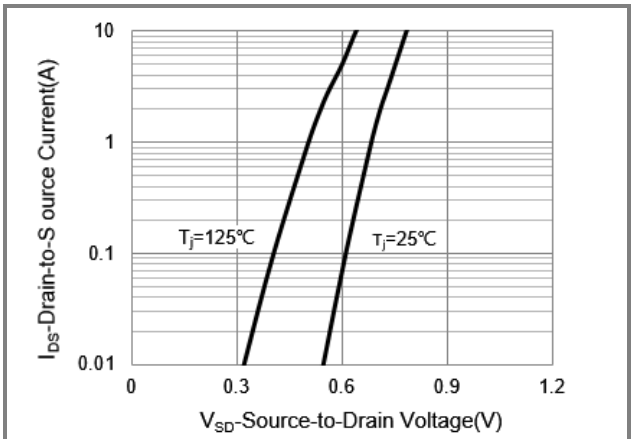


Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

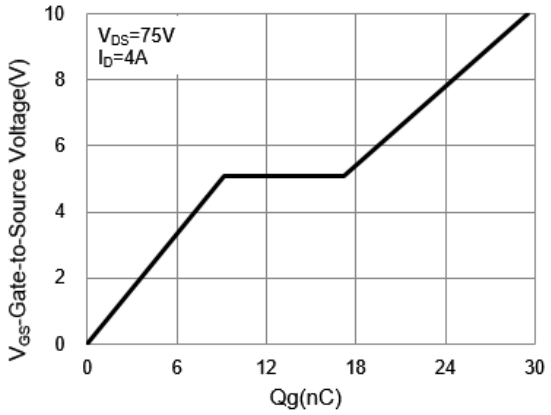


Fig.7 Gate-Charge Characteristics

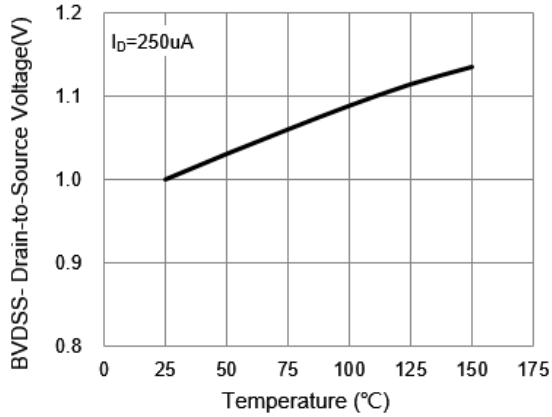


Fig.8 Breakdown Voltage Variation vs. Temperature

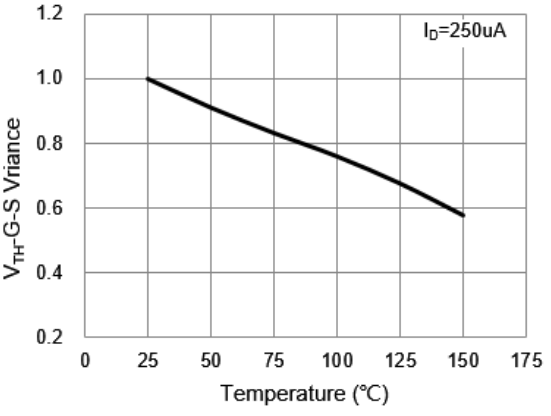


Fig.9 Threshold Voltage Variation with Temperature

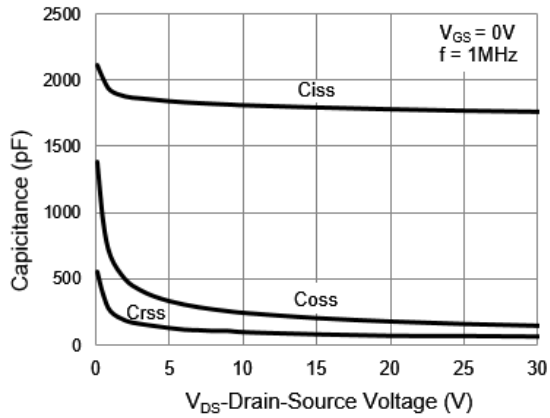


Fig.10 Capacitance vs. Drain-Source Voltage

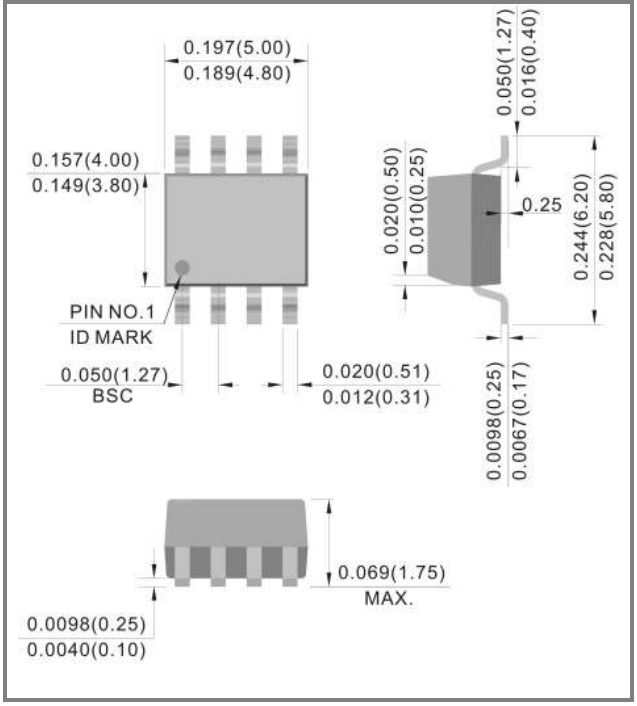
PJL9480

PART NO PACKING CODE VERSION

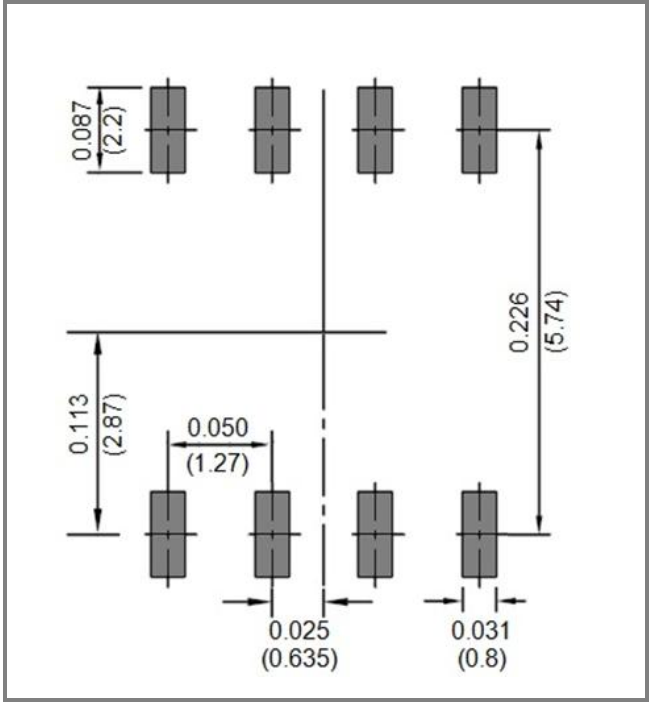
Part No Packing Code	Package Type	Packing Type	Marking	Version
PJL9480_R2_00001	SOP-8	2.5K pcs / 13” reel	L9480	Halogen free

Packaging Information & Mounting Pad Layout

SOP-8 Dimension — Unit: inch(mm)



SOP-8 PAD LATOUT — Unit: inch(mm)



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