



A Product Line of Diodes Incorporated

ZXTC2063E6

40V COMPLEMENTARY MEDIUM POWER TRANSISTOR IN SOT26

Features

- NPN + PNP Combination
- BV_{CEO} > 40 (-40)V
- BV_{ECO} > 6 (-3)V
- I_{CM} = 9 (-9)A Peak Pulse Current
- V_{CE(sat)} < 60 (-90)mV @ 1A
- R_{CE(sat)} = 38 (58)mΩ
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Description

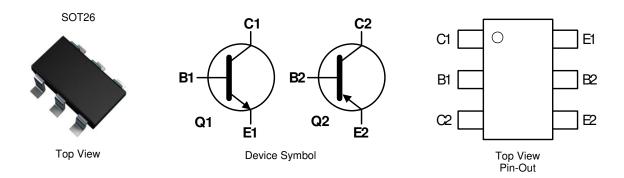
Advanced process capability is used to achieve this high performance device. Combining NPN and PNP transistors in the SOT26 package provides a compact solution for the intended applications.

Mechanical Data

- Case: SOT26
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.015 grams (Approximate)

Applications

- MOSFET and IGBT Gate Driving
- Motor Drive



Ordering Information (Note 4)

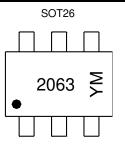
Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2063E6TA	2063	7	8	3,000

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.

See http://www.diodes.com/ for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com.

Marking Information



 $\begin{array}{l} 2063 = \mbox{Product Type Marking Code} \\ \mbox{YM} = \mbox{Date Code Marking} \\ \mbox{Y or } \overline{Y} = \mbox{Year (ex: C = 2015)} \\ \mbox{M or } \overline{M} = \mbox{Month (ex: 9 = September)} \end{array}$

Date Code Key

Notes:

D 410 0040													
Year	2015	20	016	2017	2018	2019	2020	202	1 20	22 2	2023	2024	2025
Code	С		D	E	F	G	Н			J	К	L	М
Mont	h	Jan	Feb	Mar	Apr	Мау	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	•	1	2	3	4	5	6	7	8	9	0	N	D



Absolute Maximum Ratings – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	130	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Collector Voltage (Reverse blocking)	V _{ECO}	6	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	Ι _C	3.5	A
Peak Pulsed Collector Current	I _{CM}	9	A
Base Current	IB	1	A

Absolute Maximum Ratings – Q2 (PNP Transistor) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-45	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Collector Voltage (Reverse blocking)	V _{ECO}	-3	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	Ic	-3	A
Peak Pulsed Collector Current	I _{CM}	-9	A
Base Current	Ι _Β	-1	A

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
	(Notes 5 & 9)		0.7 5.6		
	(Notes 6 & 9)		0.9 7.2		
Power Dissipation Linear Derating Factor	(Notes 6 & 10)	PD	1.1 8.8	W mW/°C	
	(Notes 7 & 9)		1.1 8.8		
	(Notes 8 & 9)		1.7 13.6		
	(Notes 5 & 9)		179		
	(Notes 6 & 9)		139		
Thermal Resistance, Junction to Ambient	(Notes 6 & 10)	$R_{ ext{ heta}JA}$	113	00111	
	(Notes 7 & 9)	0-	113	°C/W	
	(Notes 8 & 9)		73		
Thermal Resistance, Junction to Lead	(Note 11)	$R_{ ext{ heta}JL}$	87.58		
Operating and Storage Temperature Range	•	T _J , T _{STG}	-55 to +150	°C	

ESD Ratings (Note 12)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	ЗA
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes: 5. For a device surface mounted on 15mm x 15mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.

6. Same as Note (5), except the device is surface mounted on 25mm x 25mm 1oz copper.

7. Same as Note (5), except the device is surface mounted on 50mm x 50mm 2oz copper.

8. Same as Note (7), except the device is measured at t < 5 seconds.

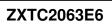
9. For device with one active die, both collectors attached to a common heatsink.

10. For device with two active dice running at equal power, split heatsink 50% to each collector.

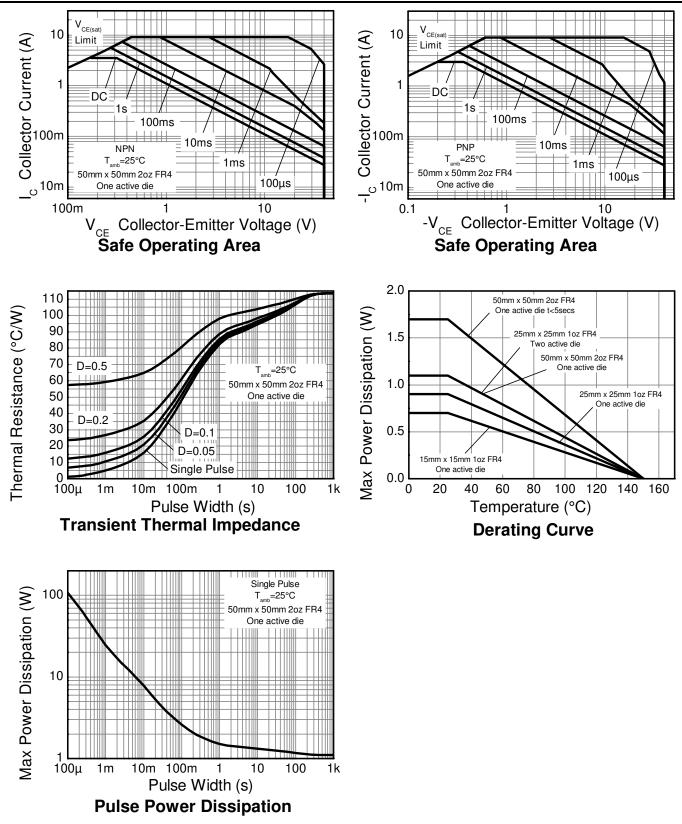
11. Thermal resistance from junction to solder-point (at the end of the collector lead).

12. Refer to JEDEC specification JESD22-A114 and JESD22-A115.





Thermal Characteristics and Derating Information





Electrical Characteristics – Q1 (NPN Transistor) (@T _A = +25°C, unless otherwise specified.)								
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition		
OFF CHARACTERISTICS								
Collector-Base Breakdown Voltage	BV _{CBO}	130	170	_	V	$I_{C} = 100 \mu A, I_{E} = 0$		
Collector-Emitter Breakdown Voltage (Note 13)	BV _{CEO}	40	63		V	$I_{\rm C} = 10 {\rm mA}, I_{\rm B} = 0$		
Emitter-Base Breakdown Voltage	BV _{EBO}	7	8.3	_	V	$I_E = 100 \mu A, I_C = 0$		
Emitter-Collector Breakdown Voltage (reverse blocking)	BV _{ECX}	6	7.4	_	V	$I_E = 100 \mu A$, $R_{BC} < 1 k \Omega$ or 0.25V > V _{BC} > -0.25V		
Emitter-Collector Breakdown Voltage (base open)	BV _{ECO}	6	7.4		V	I _E = 100μA		
Collector Cutoff Current	I _{CBO}	_	<1	50 20	nA μA	V _{CB} = 100V V _{CB} = 100V, T _A = +100°C		
Collector Cutoff Current	I _{EBO}		<1	50	nA	V _{EB} = 5.6V		
ON CHARACTERISTICS (Note 13)								
DC Current Gain	h _{FE}	300 280 40	450 400 60	900		$\begin{split} I_{C} &= 10 \text{mA}, V_{CE} = 2 \text{V} \\ I_{C} &= 1.0 \text{A}, V_{CE} = 2 \text{V} \\ I_{C} &= 3.5 \text{A}, V_{CE} = 2 \text{V} \end{split}$		
Collector-Emitter Saturation Voltage	V _{CE(sat)}	_	50 85 150 135	60 110 220 195	mV	$\begin{split} I_{C} &= 1.0A, I_{B} = 100 \text{mA} \\ I_{C} &= 1.0A, I_{B} = 20 \text{mA} \\ I_{C} &= 2.0A, I_{B} = 40 \text{mA} \\ I_{C} &= 3.5A, I_{B} = 350 \text{mA} \end{split}$		
Base-Emitter Saturation Voltage	V _{BE(sat)}	_	960	1,050	mV	I _C = 3.5A, I _B = 350mA		
Base-Emitter Turn-On Voltage	V _{BE(on)}	_	860	950	mV	$I_{C} = 3.5A, V_{CE} = 2V$		
SMALL SIGNAL CHARACTERISTICS								
Output Capacitance	C _{obo}	_	12	20	pF	$V_{CB} = 10V, f = 1.0MHz$		
Current Gain-Bandwidth Product	f _T		190	_	MHz	$V_{CE} = 10V, I_{C} = 50mA, f = 100MHz$		
Delay Time	td		64		ns			
Rise Time	tr		108	_	ns	$V_{CC} = 10V$, $I_C = 1A$, $I_{B1} = I_{B2} = 10mA$		
Storage Time	ts		428		ns	$v_{CC} = 10v, i_C = 1A, i_{B1} = i_{B2} = 1000$		
Fall Time	t _f	_	130	_	ns			

Electrical Characteristics – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)

Note: 13. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.



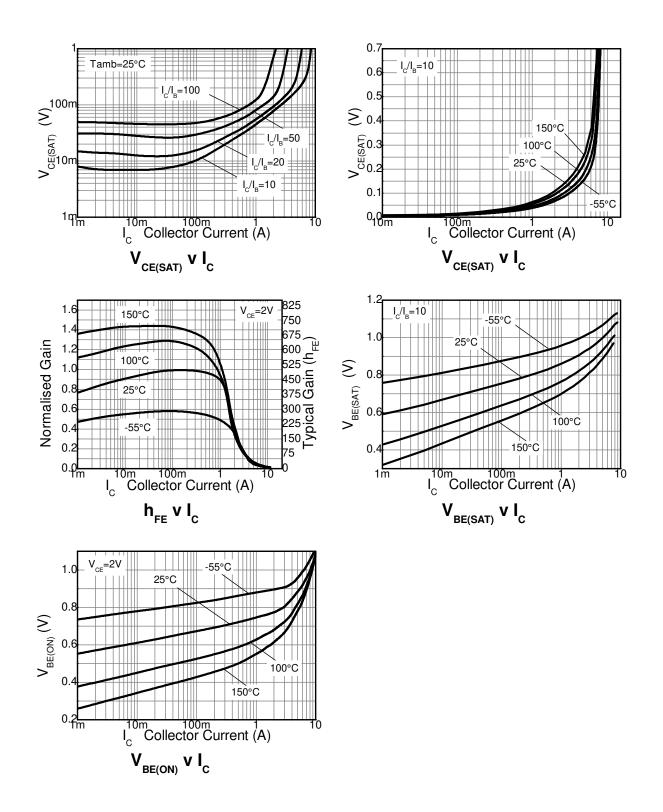
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS	•					
Collector-Base Breakdown Voltage	BV CBO	-45	-80	_	V	$I_{C} = -100 \mu A, I_{E} = 0$
Collector-Emitter Breakdown Voltage (Note 14)	BV _{CEO}	-40	-65	_	V	$I_{\rm C} = -10 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	-8.3		V	$I_{\rm E} = -100 \mu A, I_{\rm C} = 0$
Emitter-Collector Breakdown Voltage (reverse blocking)	BV_{ECX}	-6	-7.4		V	$-I_E = 100\mu A$, $R_{BC} < 1k\Omega$ or 0.25V < $V_{BC} < -0.25V$
Emitter-Collector Breakdown Voltage (base open)	BV _{ECO}	-3	-8.7	_	V	I _E = -100μA
Collector Cutoff Current	1	_	<1	-50	nA	$V_{CB} = -36V$
	I _{CBO}			-20	μA	$V_{CB} = -36V, T_A = +100^{\circ}C$
Collector Cutoff Current	I _{EBO}	_	<1	-50	nA	V _{EB} = -5.6V
ON CHARACTERISTICS (Note 14)						
DC Current Gain	h _{FE}	300 200 20	450 280 50	900 		$\begin{split} I_{C} &= -10 mA, \ V_{CE} = -2V \\ I_{C} &= -1.0A, \ V_{CE} = -2V \\ I_{C} &= -3.0A, \ V_{CE} = -2V \end{split}$
Collector-Emitter Saturation Voltage	V _{CE(sat)}		-70 -195 -175	-90 -290 -260	mV	$I_{C} = -1.0A, I_{B} = -100mA$ $I_{C} = -1.0A, I_{B} = -20mA$ $I_{C} = -3.0A, I_{B} = -300mA$
Base-Emitter Saturation Voltage	V _{BE(sat)}	_	-935	-1,000	mV	I _C = -3.0A, I _B = -300mA
Base-Emitter Turn-On Voltage	V _{BE(on)}	_	-855	-950	mV	I _C = -3.0A, V _{CE} = -2V
SMALL SIGNAL CHARACTERISTICS						·
Output Capacitance	Cobo		17	25	рF	$V_{CB} = -10V, f = 1.0MHz$
Current Gain-Bandwidth Product	fт	_	270		MHz	V _{CE} = -10V, I _C = -50mA, f = 100MHz
Delay Time	t _d		57	_	ns	
Rise Time	tr		69	_	ns	
Storage Time	ts	_	154	_	ns	$V_{CC} = -10V, I_C = -1A, I_{B1} = I_{B2} = -10m$
Fall Time	t _f	_	60		ns]

Electrical Characteristics – Q2 (PNP Transistor) (@TA = +25°C, unless otherwise specified.)

Note: 14. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.

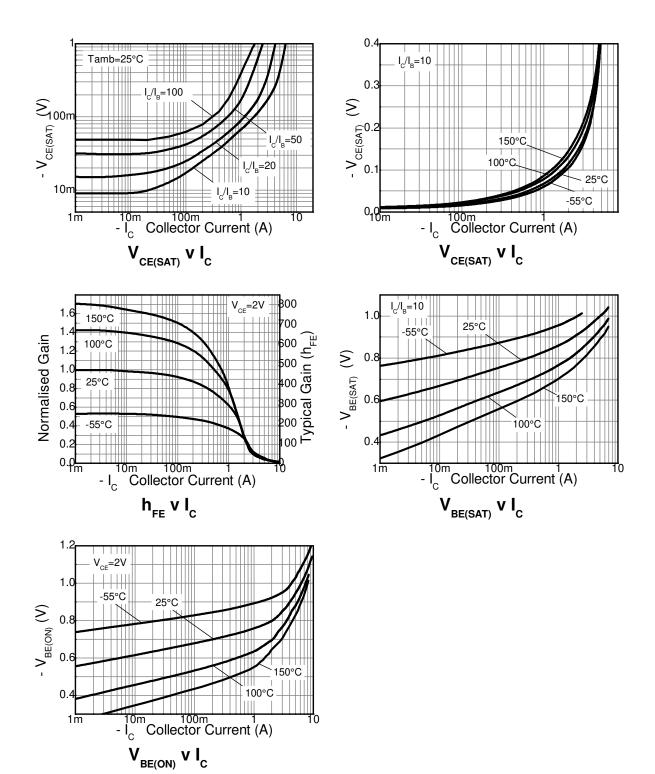


Typical Electrical Characteristics – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)





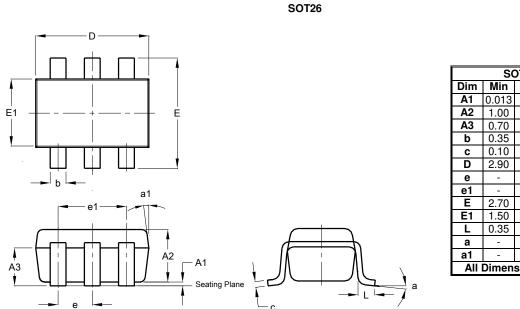
Typical Electrical Characteristics – Q2 (PNP Transistor) (@T_A = +25°C, unless otherwise specified.)





Package Outline Dimensions

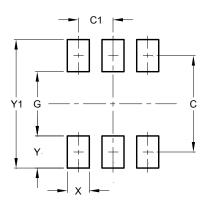
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



	SOT26								
Dim	Min	Max	Тур						
A 1	0.013	0.10	0.05						
A2	1.00	1.30	1.10						
A3	0.70	0.80	0.75						
b	0.35	0.50	0.38						
С	0.10	0.20	0.15						
D	2.90	3.10	3.00						
е	-	-	0.95						
e1	-	-	1.90						
Е	2.70	3.00	2.80						
E1	1.50	1.70	1.60						
L	0.35	0.55	0.40						
а	-	-	8°						
a1	-	-	7°						
All	Dimen	sions	in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
С	2.40
C1	0.95
G	1.60
Х	0.55
Y	0.80
Y1	3.20

SOT26



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