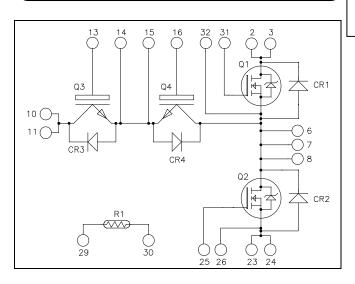
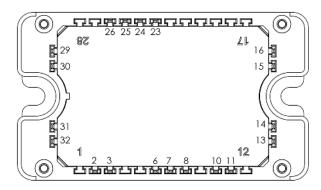


Phase Leg & Dual Common Emitter Power Module





All multiple inputs and outputs must be shorted together 10/11; 23/24; 2/3; ...

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SiC MOSFET (Q1, Q2):

 $V_{CES} = 1200V$; $R_{DSon} = 98m\Omega$ max @ Tj = 25°C

Trench & Field Stop IGBT3 (Q3, Q4): V_{CES} = 600V ; I_C = 20A @ Tc = 100°C

Application

- Solar converter
- Uninterruptible Power Supplies

Features

- Q1, Q2 SiC Power MOSFET
 - Low R_{DS(on)}
 - High temperature perf ormance
 - Q3, Q4 Trench + field Stop IGBT3
 - Low voltage drop
 - Low tail current
 - Switching frequency up to 20 kHz
- SiC Schottky Diode (CR1 to CR4)
 - Zero reverse recovery
 - Zero forward recovery
 - Temperature Independent switching behavior
 - Positive temperature coefficient on VF
- Kelvin emitter for easy drive
- Very low stray inductance
- AlN substrate for improved thermal performance
- Internal thermistor for temperature monitoring

Benefits

- Stable temperature behavior
- Very rugged
- Solderable terminals for easy PCB mounting
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Low profile
- RoHS Compliant

All ratings (a) $T_j = 25^{\circ}C$ unless otherwise specified

CAUTION: These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



1. SiC MOSFET characteristics (Per MOSFET)

Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
V _{DSS}	Drain - Source Voltage		1200	V
т	Continuous Drain Comment	$T_c = 25^{\circ}C$	26	
I _D	Continuous Drain Current	$T_c = 80^{\circ}C$	20	Α
I _{DM}	Pulsed Drain current		55	
V _{GS}	Gate - Source Voltage		-10/+25	V
R _{DSon}	Drain - Source ON Resistance		98	mΩ
PD	Power Dissipation	$T_c = 25^{\circ}C$	125	W

Electrical Characteristics

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit
I _{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V$, $V_{DS} = 1200V$				100	μA
р	Drain – Source on Resistance	$V_{GS} = 20V$	$T_j = 25^{\circ}C$		80	98	
R _{DS(on)}		$I_D = 20A$	$T_{j} = 150^{\circ}C$		153		mΩ
V _{GS(th)}	Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 5mA$		2.4	3		V
I _{GSS}	Gate – Source Leakage Current	$V_{GS} = 20 V, V_{DS} = 0V$	/			250	nA

Dynamic Characteristics

	Characteristic	Test Conditions		Min	Тур	Max	Unit
C _{iss}	Input Capacitance	$V_{GS} = 0V$			950		
C _{oss}	Output Capacitance	$V_{\rm DS} = 1000 V$			80		pF
C _{rss}	Reverse Transfer Capacitance	f = 1 MHz			7.6		
Qg	Total gate Charge	$V_{GE} = 20V$			62		
Q _{gs}	Gate – Source Charge	$V_{Bus} = 800V$			15		nC
Q _{gd}	Gate – Drain Charge	$I_D = 20A$			23		
T _{d(on)}	Turn-on Delay Time	$V_{} = 2/\pm 20 V_{}$			12		
Tr	Rise Time	$V_{\rm Bus} = 800 V$	$V_{GS} = -2/+20V$ $V_{Rus} = 800V$		14		
T _{d(off)}	Turn-off Delay Time	$I_D = 20A$			23		ns
$T_{\rm f}$	Fall Time	$R_{\rm L} = 40\Omega ; R_{\rm G} = 50\Omega$	2		18		
Eon	Turn on Energy	Inductive Switching $V_{GS} = -5/+20V$ $V_{Bus} = 600V$	$T_j = 150^{\circ}C$		0.45		mJ
E _{off}	Turn off Energy	$I_{\rm D} = 20A$ $R_{\rm G} = 50\Omega$	$T_j = 150^{\circ}C$		0.25		1113
R _{thJC}	Junction to Case Thermal Resistanc	e				1	°C/W

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SiC diode ratings and characteristics (CR1 & CR2) (per diode)

Symbol	Characteristic	Test Conditions		Min	Тур	Max	Unit	
V _{RRM}	Peak Repetitive Reverse Voltage					1200	V	
T	Payarsa Laakaga Currant	V = 1200 V	$T_j = 25^{\circ}C$		10	200	۸	
I _{RM}	Reverse Leakage Current	$v_{R} - 1200v$	$V_{R} = 1200V$ $T_{i} =$	$T_{j} = 175^{\circ}C$		500		μA
I _F	DC Forward Current		$Tc = 100^{\circ}C$		10		А	
V	Diada Formund Valtaga	$I_F = 10A \qquad \frac{T_i = 25^{\circ}C}{T_i = 175^{\circ}C}$	$T_i = 25^{\circ}C$		1.5	1.8	V	
V _F	Diode Forward Voltage		$T_i = 175^{\circ}C$		2.3		v	
Qc	Total Capacitive Charge	$I_F = 10A, V_R = 600V$ di/dt = 500A/ μ s			120		nC	
С		$f = 1 MHz, V_R = 200 V$	200V		115		πE	
C	Total Capacitance	$f = 1 MHz, V_R = 400 V$			85		pF	
R _{thJC}	Junction to Case Thermal Resistance					1.1	°C/W	

2. Trench & Field Stop IGBT3 (per IGBT)

Absolute maximum ratings

Symbol	Parameter		Max ratings	Unit
V _{CES}	Collector - Emitter Voltage		600	V
т	Continuous Collector Current	$T_C = 25^{\circ}C$	37	
I _C	Continuous Collector Current $T_{C} = 1$		20	А
I _{CM}	Pulsed Collector Current	$T_C = 25^{\circ}C$	40	
V _{GE}	Gate – Emitter Voltage		± 20	V
P _D	Power Dissipation	$T_C = 25^{\circ}C$	78	W
RBSOA	Reverse Bias Safe Operating Area	$T_{\rm J} = 150^{\circ}{\rm C}$	40A @ 550V	

Electrical Characteristics

Syml	ool Characteristic	Test Conditions	Min	Тур	Max	Unit	
ICES	Zero Gate Voltage Collector Current	$V_{GE} = 0V, V_{CE} =$			250	μA	
V	at) Collector Emitter Saturation Voltage	$V_{GE} = 15V$	$T_j = 25^{\circ}C$		1.5	1.9	V
V _{CE(sat)}	(collector Ellitter Saturation voltage	$I_C = 20A$	$T_{j} = 150^{\circ}C$		1.7		v
V _{GE(}	h) Gate Threshold Voltage	$V_{GE} = V_{CE}, I_C = 300 \mu A$		5.0	5.8	6.5	V
I _{GES}	Gate – Emitter Leakage Current	$V_{GE} = 20V, V_{CE}$	= 0V			300	nA



Dynamic Characteristics

Symbol	Characteristic	Test Conditions	1	Min	Тур	Max	Unit
Cies	Input Capacitance	$V_{GE} = 0V$			1100		
Coes	Output Capacitance	$V_{CE} = 25V$			70		pF
C _{res}	Reverse Transfer Capacitance	f = 1 MHz			35		
Q _G	Gate charge	$V_{GE} = \pm 15V, I_C = V_{CE} = 300V$	= 20A		200		nC
T _{d(on)}	Turn-on Delay Time	Inductive Switch	hing (25°C)		110		
Tr	Rise Time	$V_{GE} = \pm 15V$			45		
T _{d(off)}	Turn-off Delay Time	$V_{Bus} = 300V$ $I_C = 20A$			200		ns
T _f	Fall Time	$R_{G} = 12\Omega$			40		
T _{d(on)}	Turn-on Delay Time	Inductive Switching (150°C)			120		
Tr	Rise Time	$V_{GE} = \pm 15V$			50		
T _{d(off)}	Turn-off Delay Time	$V_{Bus} = 300V$ $I_C = 20A$			250		ns
T _f	Fall Time	$R_G = 12\Omega$			60		
Б	Turn-on Switching Energy	$V_{GE} = \pm 15V$	$T_j = 25^{\circ}C$		0.11		mJ
Eon	Tuni-on Switching Energy	$V_{Bus} = 300V$	$T_{j} = 150^{\circ}C$		0.2		IIIJ
E _{off}	Turn-off Switching Energy	$I_C = 20A$	$T_j = 25^{\circ}C$		0.5		mJ
Loff	Tuni-on Switching Energy	$R_G = 12\Omega$	$T_{j} = 150^{\circ}C$		0.7		1115
I _{sc}	Short Circuit data	$V_{GE} \le 15V$; $V_{Bus} = 360V$ $t_p \le 10\mu s$; $T_1 = 150^{\circ}C$			100		А
R _{thJC}	Junction to Case Thermal Resistance					1.92	°C/W

3. SiC diode ratings and characteristics (CR3 & CR4) (per diode)

Symbol	Characteristic	Test Conditions			Тур	Max	Unit
V _{RRM}	Peak Repetitive Reverse Voltage					600	V
I _{RM}	$\mathbf{V} = (00\mathbf{V})$	$V_{R} = 600 V$	$T_j = 25^{\circ}C$		10	60	μA
I _{RM}	Reverse Leakage Current	$v_R = 600 v$	$T_{j} = 175^{\circ}C$		20	300	μA
I _F	DC Forward Current		$Tc = 100^{\circ}C$		10		А
$V_{\rm F}$	Diode Forward Voltage	$I_{r} = 10A$	$T_i = 25^{\circ}C$		1.6	1.8	V
• F	Didde Forward Voltage		$T_1 = 175^{\circ}C$		2	2.4	v
Q _C	Total Capacitive Charge	$I_F = 10A, V_R = 600V$ di/dt = 500A/ μ s			28		nC
С	Tatal Conscitance	$f = 1 MHz, V_R = 200 V$			65		тE
C	Total Capacitance $f = 1 MHz, V_R = 400$		400V		50		pF
R _{thJC}	Junction to Case Thermal Resistance					2.2	°C/W



4. Temperature sensor NTC

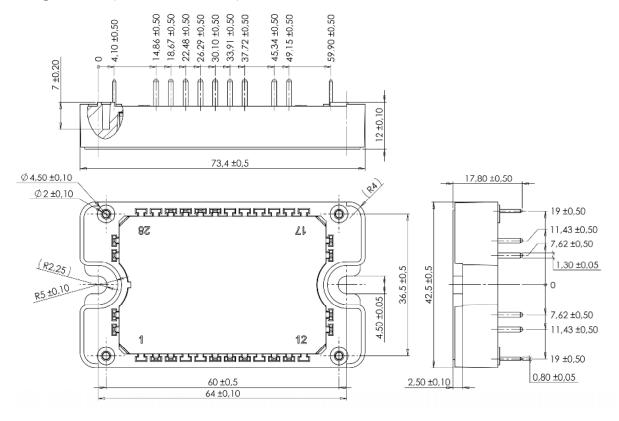
Symbol	Characteristic	Min	Тур	Max	Unit
R ₂₅	Resistance @ 25°C		22		kΩ
$\Delta R_{25}/R_{25}$	Resistance tolerance			5	%
$\Delta B/B$	Beta tolerance			3	70
B 25/100	$T_{25} = 298.16 \text{ K}$		3980		K

 $R_{T} = \frac{R_{25}}{\exp\left[B_{25/100}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$ T: Thermistor temperature R_T: Thermistor value at T

5. Thermal and package characteristics

Symbol	Characteristic			Min	Max	Unit
V _{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000		V
т	Operating junction temperature range	SiC MOS	FET	-40	150	
T_{J}	SiC		es + IGBT	-40	175	
T _{JOP}	Recommended junction temperature under switching conditions			-40	T _J max -25	°C
T _{STG}	Storage Temperature Range			-40	125	
T _C	Operating Case Temperature				125	
Torque	Mounting torque	To heatsink	M4	2	3	N.m
Wt	Package Weight				110	g

Package outline (dimensions in mm)

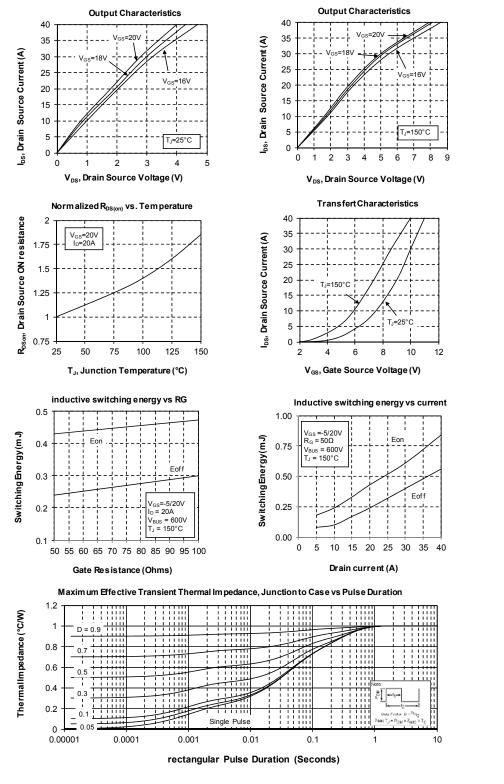


See application note 1906 - Mounting Instructions for SP3F Power Modules on www.microsemi.com



6. Typical performance curve

Q1, Q2 SiC MOSFET



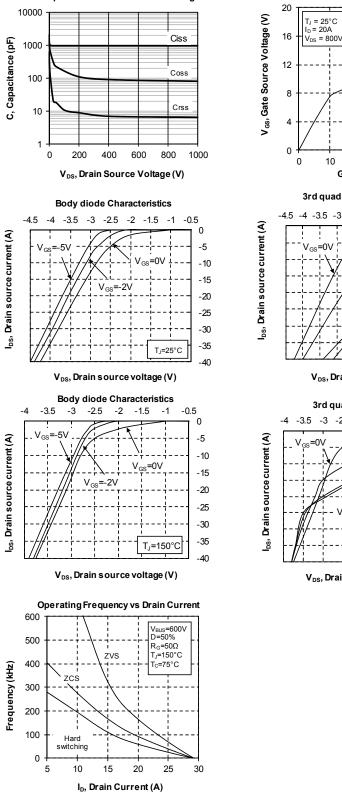
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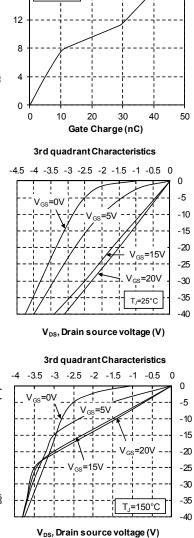


Capacitance vs Drain Source Voltage

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Gate Charge vs Gate Source Voltage

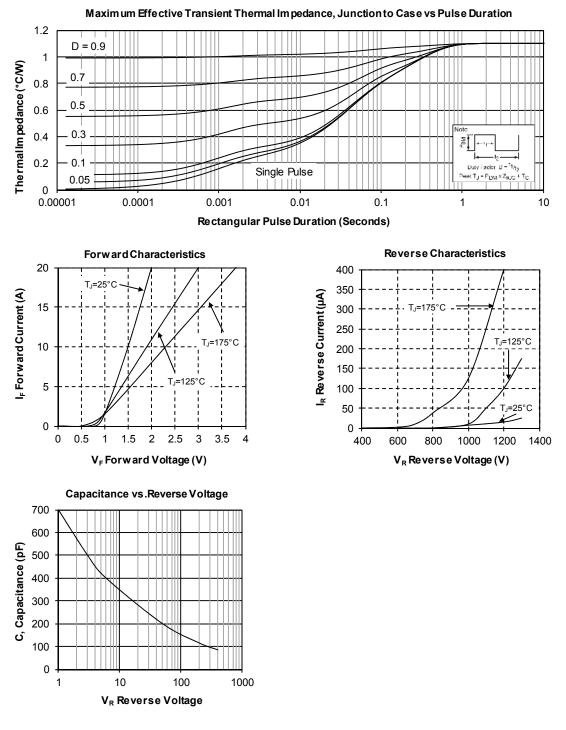




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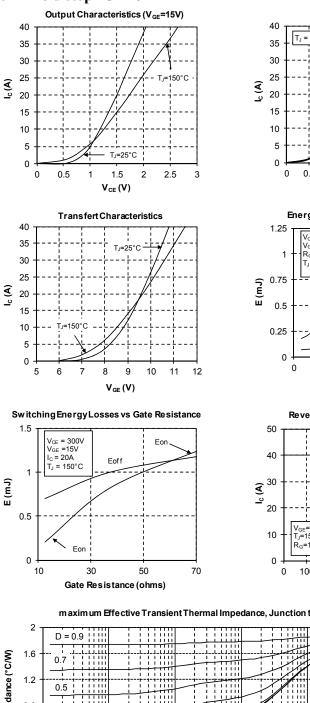
CR1 & CR2 SiC diode characteristics

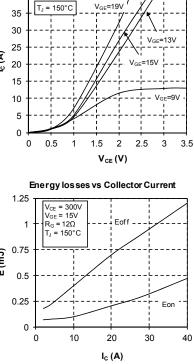




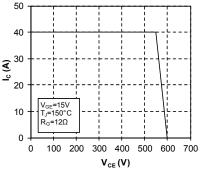
Output Characteristics

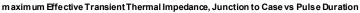
Q3, Q4 Trench + field stop IGBT3

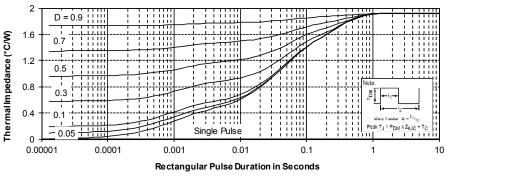




Reverse Bias Safe Operating Area

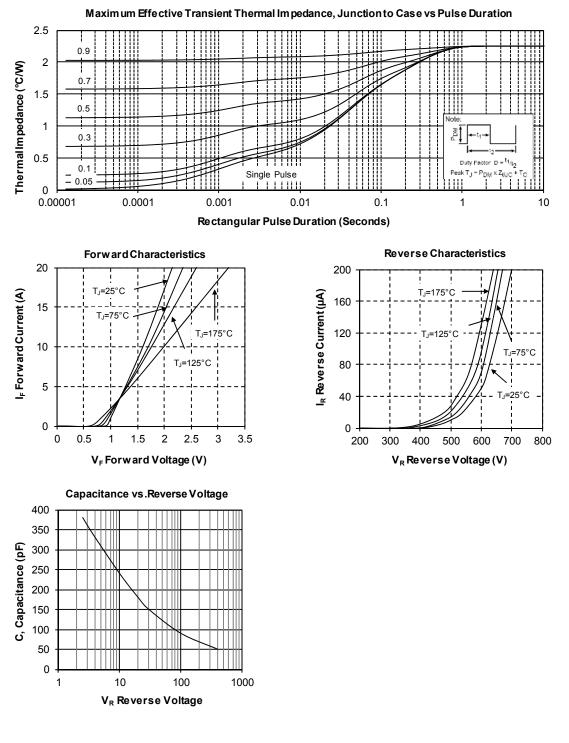








CR3 & CR4 SiC diode characteristics



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