TAOGLAS

Datasheet

DXP.01.A

Description:

SMD L1/L2 SAW Diplexer for GPS/GALILEO L1, GLONASS L2 & BeiDou B2

Features:

SAW Diplexer SMT Direct Mount

L2 1227.6 / L1 1575.42MHz

Low Insertion Loss In band

High Isolation Port to Port

Compact Size: 5 * 5 * 1.7 mm

RoHS & Reach Compliant

1. Introduction

The Taoglas DXP.01.A is a compact SAW diplexer for use in any navigation system application using the GPS/GALILEO L1, GLONASS L2 and BeiDou B2 bands.

The diplexer is designed to function as both a bandpass filter for each band and to either split one path into three or to combine the bands back into one RF feed. For example, a customer who wanted to use passive antenna elements would need to implement a diplexer in some cases to split the bands out into separate paths. It is also designed to isolate and reject any unwanted GPS/GALILEO signals from getting to the application port.

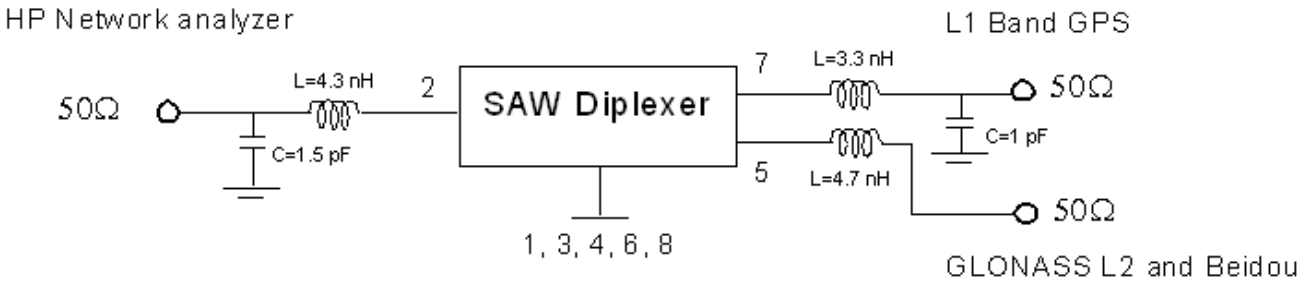
It is housed in a compact 5*5*1.7mm over-molded laminate package and is easy to integrate using SMD process mounting directly onto the target PCB.

For further optimization to customer-specific device environments and for support to integrate and test this antennas performance in your device, contact your regional Taoglas Customer Services Team.

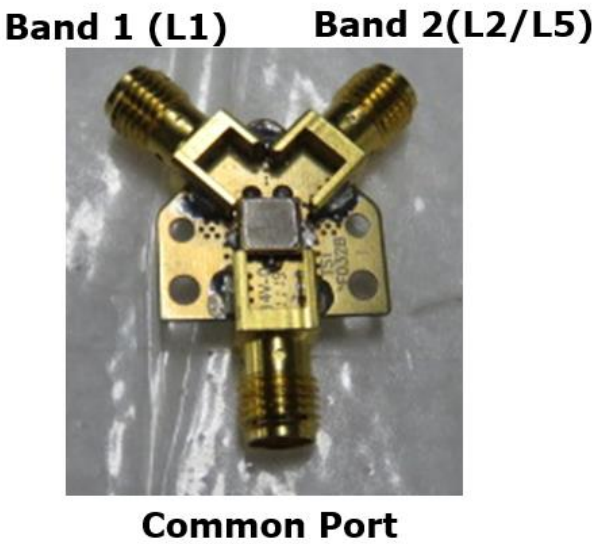
2. Specifications

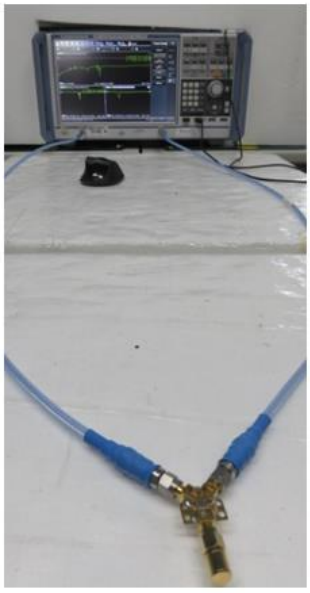
L1 Band GPS/GALILEO			
	Min.	Typ.	Max.
Center Frequency (MHz)	-	1575.42	-
Insertion Loss (dB)	-	3.3	3.8
Amplitude Ripple (dB)	-	0.1	1.0
Return Loss (dB)	-	-12	-8.5
Attenuation (Reference level from 0dB)			
824 ~ 960 (MHz)	25	47	-
1500 ~ 1525.42 (MHz)	8	19	-
1625.42 ~ 1650 (MHz)	8	16	-
1710 ~ 2170 (MHz)	25	34	-
L2 Band GLONASS and B2 Band BeiDou			
	Min.	Typ.	Max.
Center frequency (MHz)	-	1227.625	-
Insertion Loss (dB)	-	4.1	4.8
Amplitude Ripple (dB)	-	0.9	1.8
Return Loss (dB)	-	-12	8.5
Attenuation (Reference level from 0dB)			
464 ~ 600 (MHz)	25	32	-
1110 ~ 1130 (MHz)	16	23	-
1330 ~ 1450 (MHz)	28	37	-
1500 ~ 1820 (MHz)	25	30	-
L1 Band GPS/GALILEO, L2 Band GLONASS and B2 Band BeiDou			
	Min.	Typ.	Max.
Isolation (1196.9~1248.625MHz)	22	36	-
Isolation (1574.22~1576.62 dB)	22	33	-
Environmental			
Operating Temperature	-40°C to 85°C		
Storage Temperature	-40°C to 85°C		
Input power Level	10 dBm		
DC Voltage	3 V		
Moisture Sensitivity Level (MSL)	1		

3. Measurement Circuit

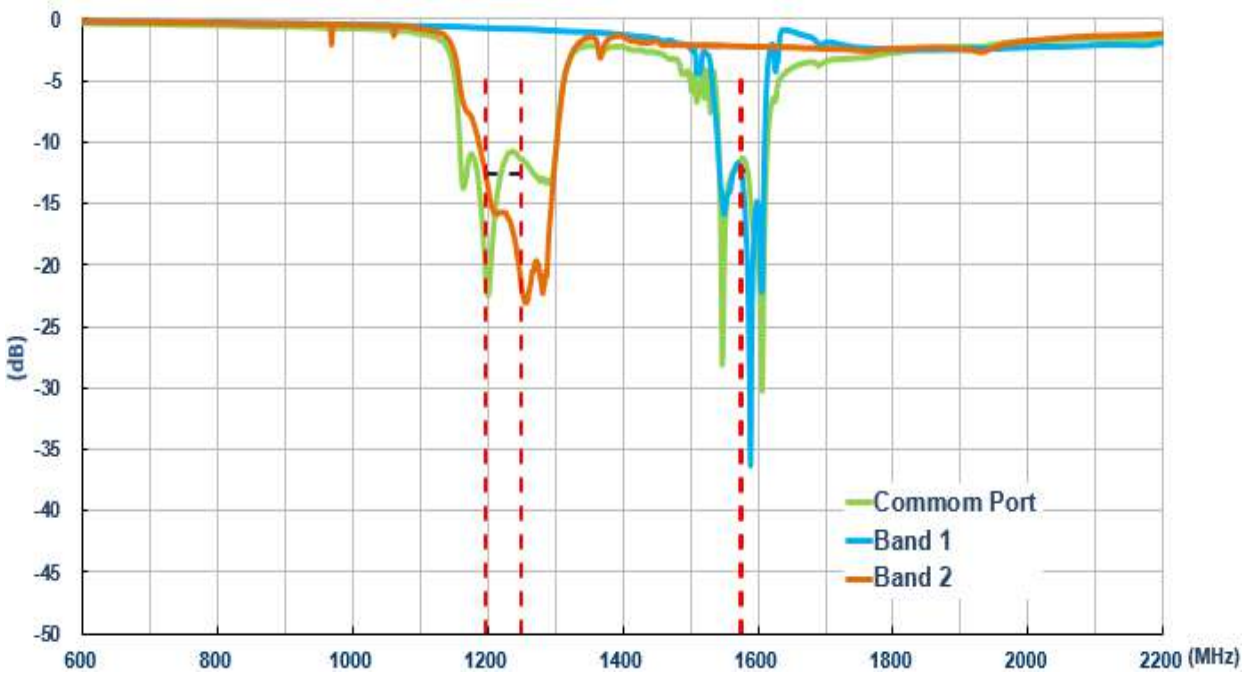


3.1 Test Setup

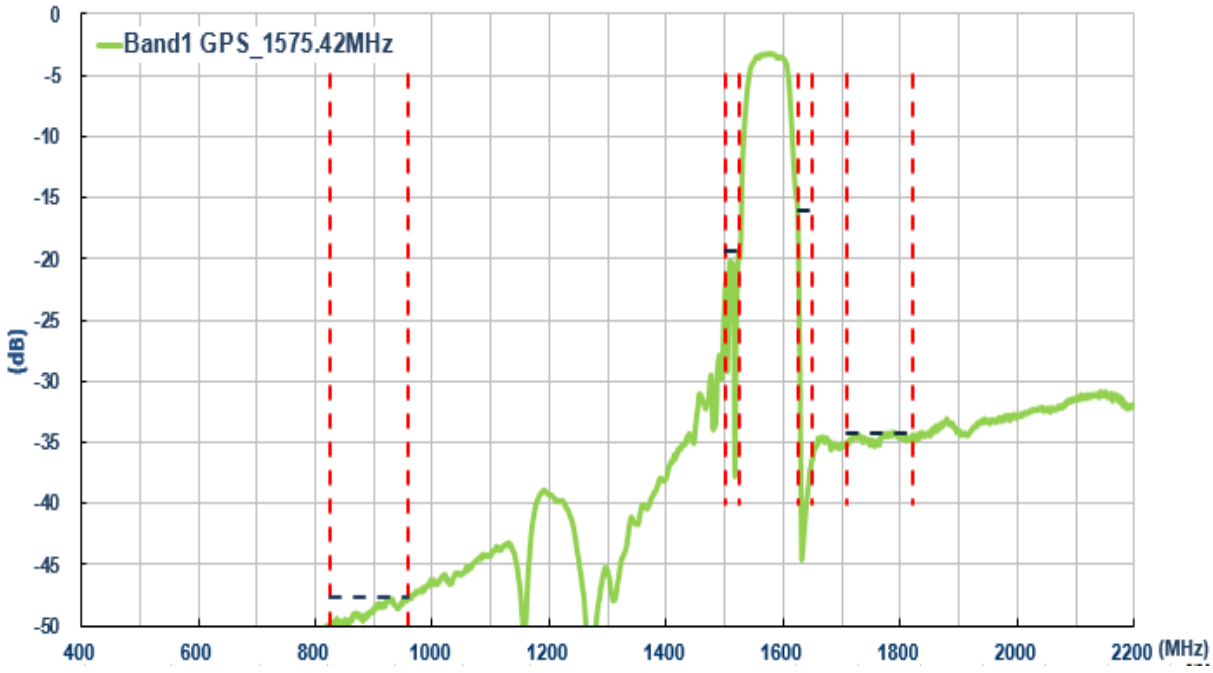




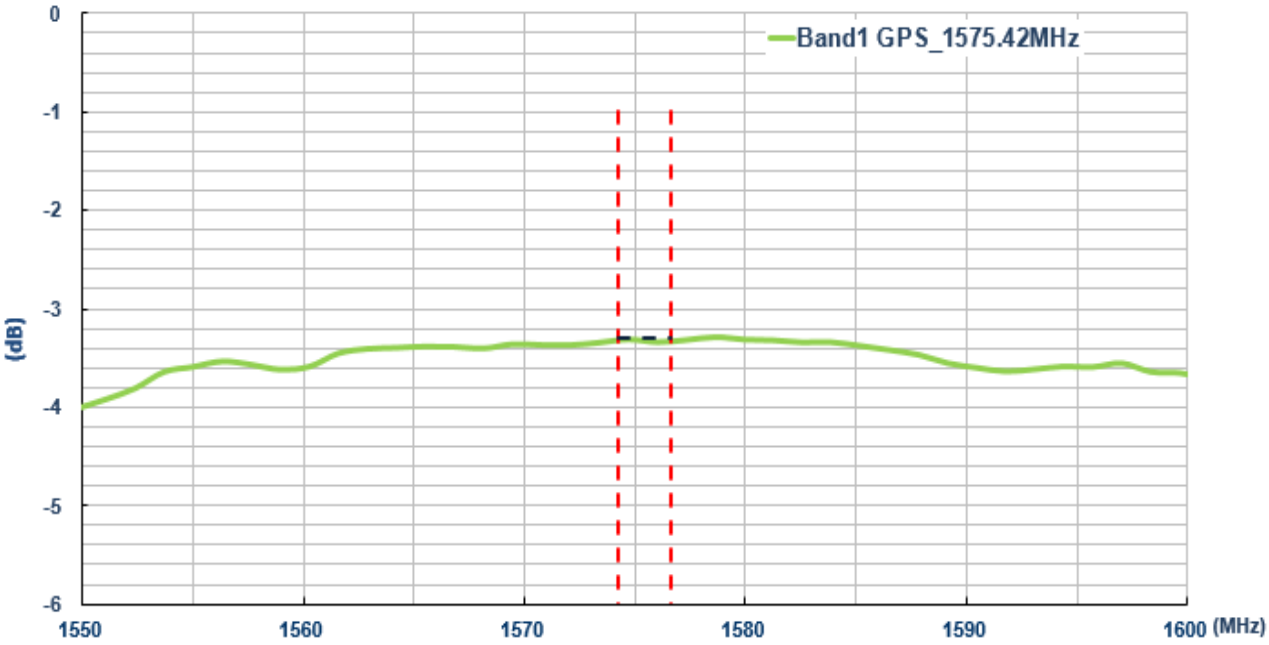
3.2 Return Loss



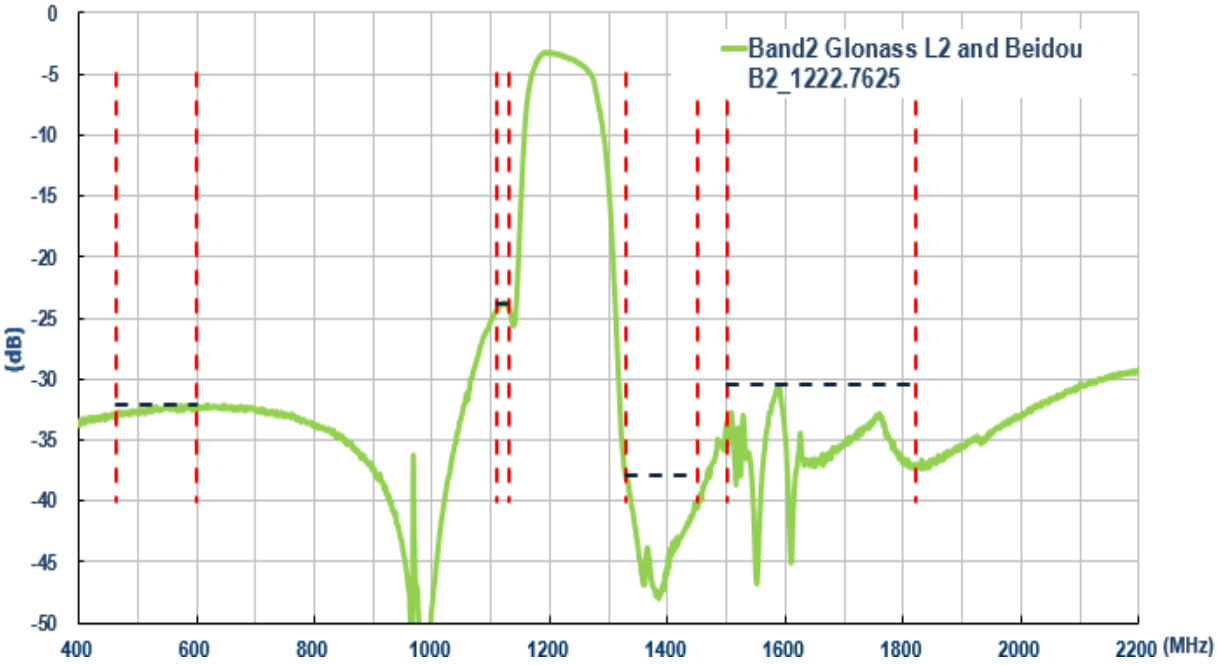
3.3 Common Port to Band 1 Port _ 1575.42MHz Attenuation



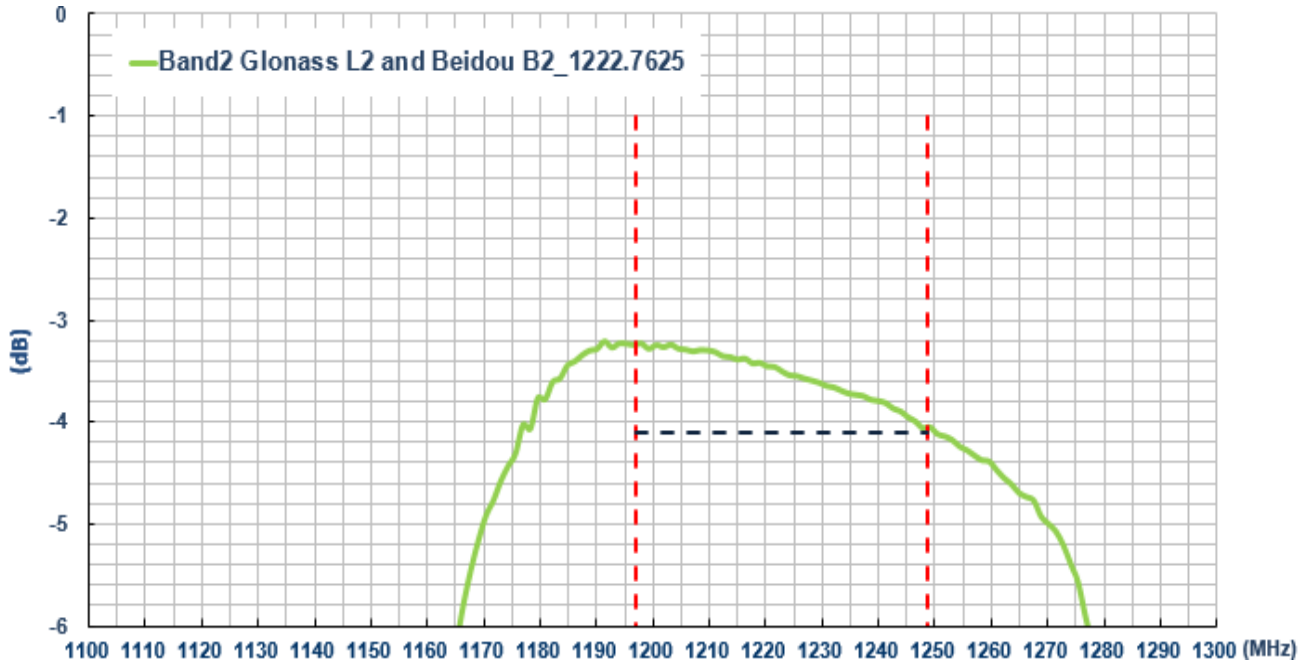
3.4 Common Port to Band 1 Port _ 1575.42MHz Insertion Loss



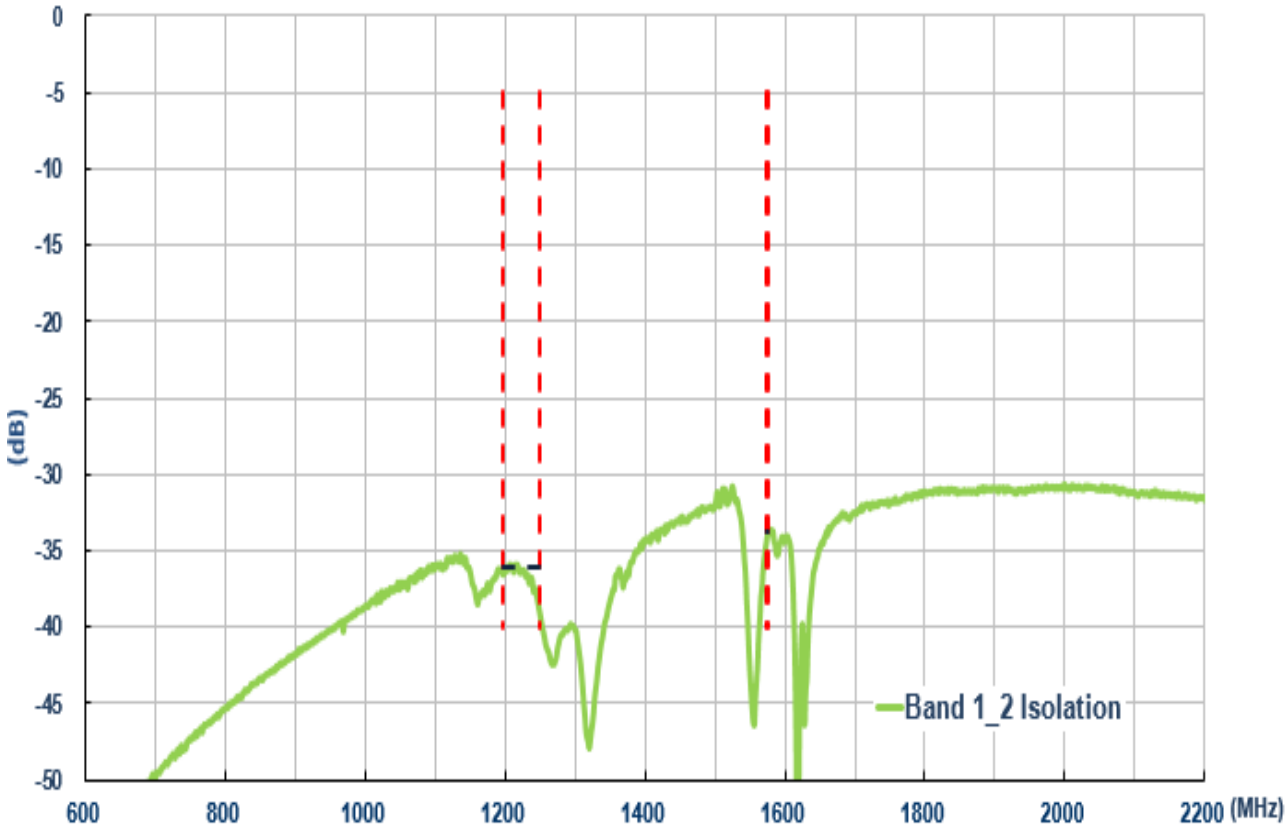
3.4 Common Port to Band 1 Port _1227.6MHz Attenuation



3.5 Common Port to Band 2 Port _ 1227.6MHz Insertion Loss



3.6 Band 1 Port – Band 2 Port Isolation

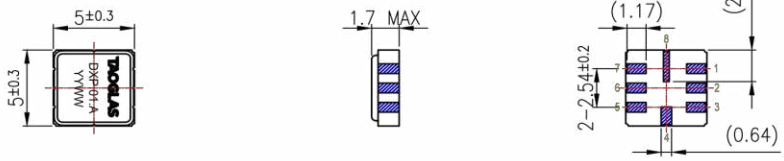


4. Mechanical Drawing (Units: mm)

ISO NO: EDW-18-8-0738 <Release>

REV	ZONE	DESCRIPTION	ENG	APPROVED	ISSUED DATE
A	ALL	Initial Design	Eva	Paul	2018/08/30
B	ALL	Replace the new LOGO <ECR-18-8-259>	Ruby	Aaron	2021/10/05

Front View B Side View Back View

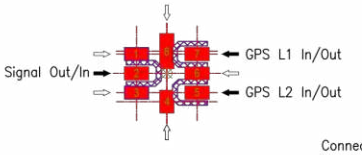


SCALE: 1/1

Foot Print

Top Copper

Pad 1, 3, 4, 6 and 8 should be connected to Ground.
Pad 2, 5 and 7 should be connected to a 50 ohm transmission line.

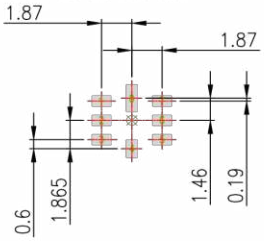


⇨ : Connected to GND

➡ : Connected to 50 ohm transmission line.

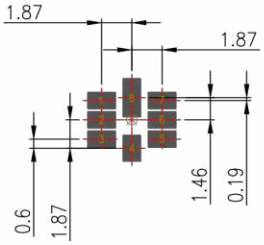
Top Solder Paste

Pads 1, 2, 3, 4, 5, 6 and 7 are the same size.

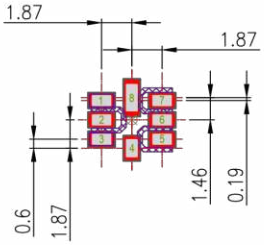


Top Solder Mask

Pads 1, 2, 3, 4, 5, 6 and 7 are the same size.
This drawing is a negative of solder mask. Black regions are anti-mask.



Composite Diagram

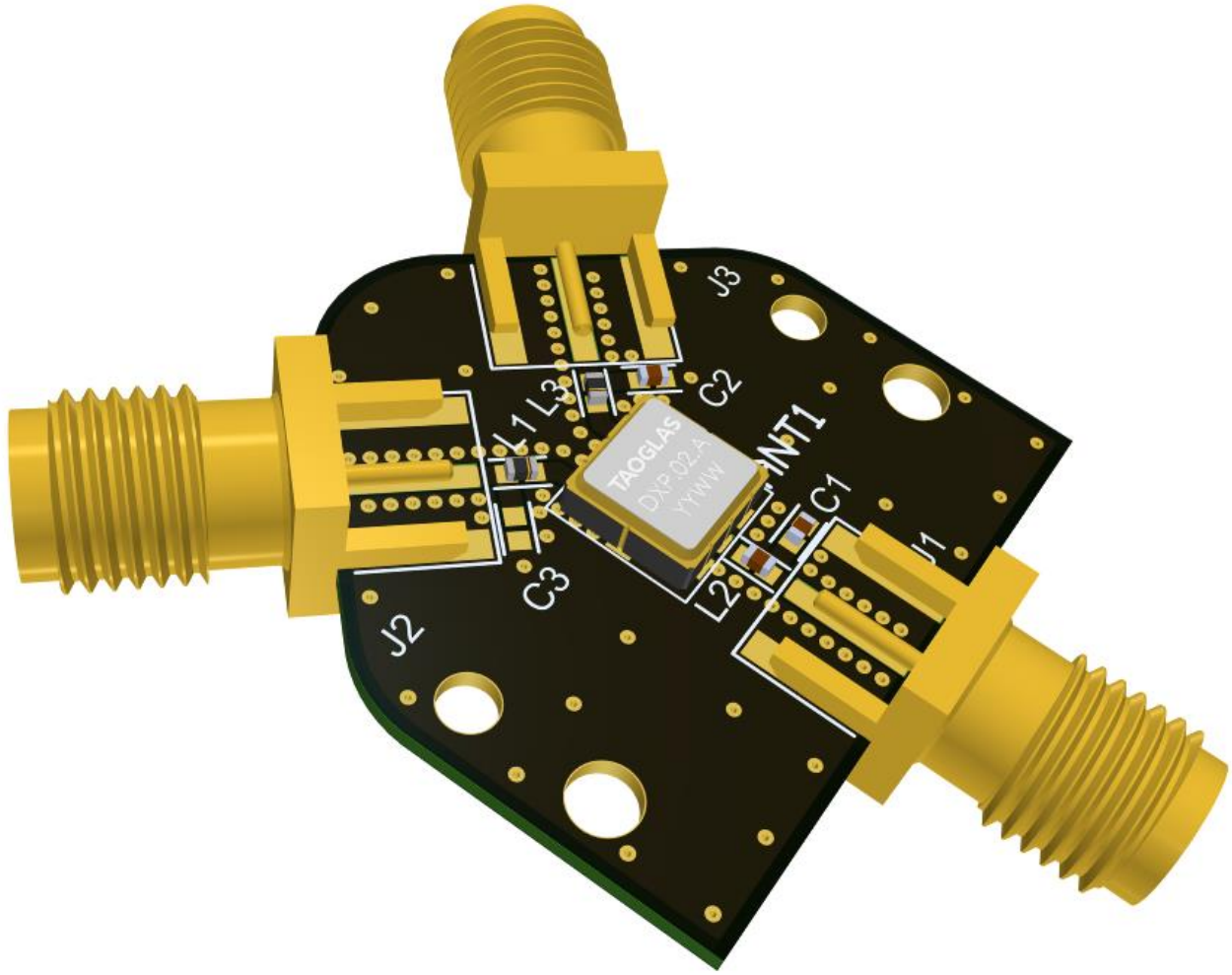


NOTE:
1. Solder Mask area
2. Copper area
3. Paste area
4. Copper Keepout Area

6. Any vias in pads should be either filled or tented to prevent solder from wicking away from the pad during reflow.
7. The dimension tolerances should follow standard PCB manufacturing guidelines
8. Copper keepout area should extend to inner PCB layers to keep transmission line impedance in 50 ohms.
This area is variant due to keep transmission line 50 ohms for diffferent substrate and board thickness. The proposed area in the drawing is reserved for 0.2mm thickness FR4 application.

UNLESS OTHERWISE SPECFIED TOLERANCES ON: .X± 0.2 XX.± 0.5 .XX± 0.1 X.± 0.3 .XXX± 0.05	DATE: 2016/05/20	MAT'L:	TAOGLAS. TW Design Centre. This drawing and its inherent design concepts are property of Taoglas. Not to be copied or given to third parties without the written consent of Taoglas. REV B
	UNIT: mm	FINISH:	
	THIRD ANGLE PROJECTION	SCALE: 2.5/1	TITLE. : 1222.7625 / 1575.42 MHz SAW Diplexer SMD 5.0x5.0 mm
APPROVED BY: Wayne	CHECKED BY: Jack/Paul	DRAWN BY: Haley	CUSTOMERS SIGNATURE / DATE — PART NO. : DXP.01.A

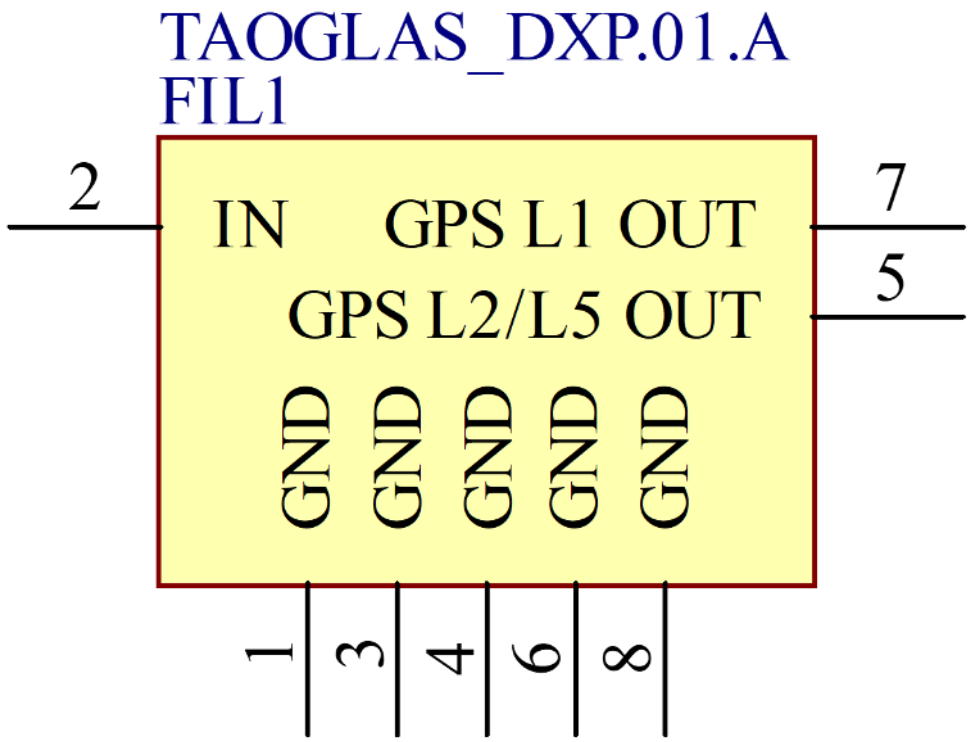
5. Integration Guide



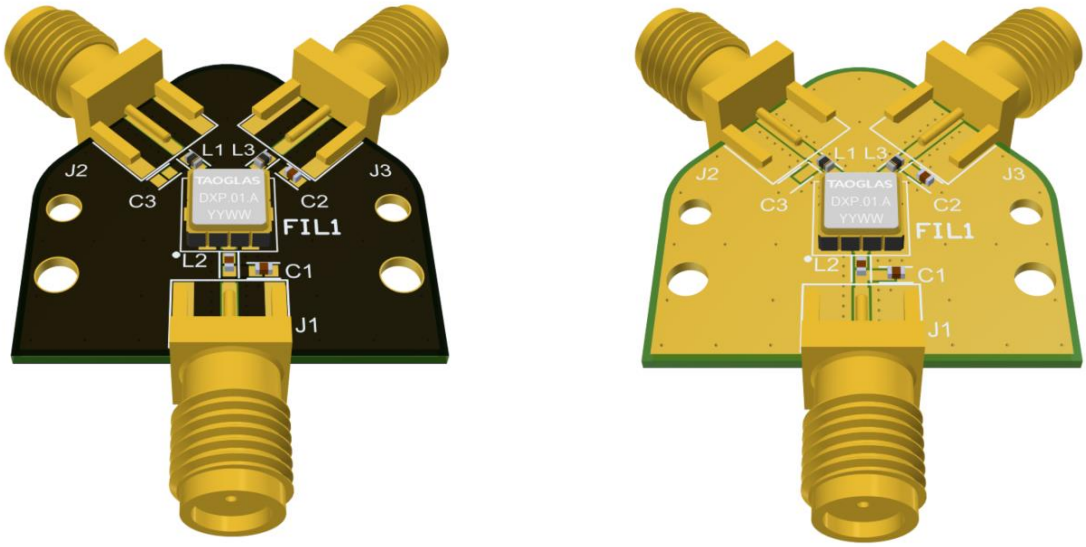
5.1 Schematic Symbol and Pin Definition

The circuit symbol for the SAW Diplexer is shown below. The SAW Diplexer has 8 pins as indicated below. The L1 pin represents the higher GNSS frequency bands at 1559 - 1610MHz and the L2 pin represents the lower GNSS frequency bands at 1164 - 1300MHz, including L5, E5a and E5b bands.

Pin	Description
2	Signal Input
5	GPS L1 Output
7	GPS L1/L2 Output
1, 3, 4, 6, 8	Ground



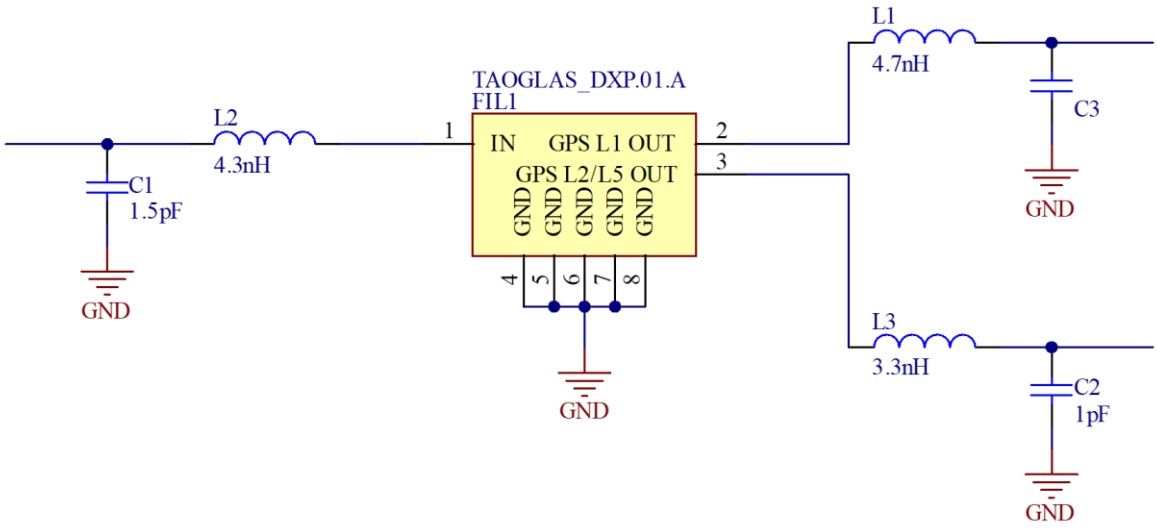
5.2 SAW Diplexer Integration Guide



5.3 PCB Layout

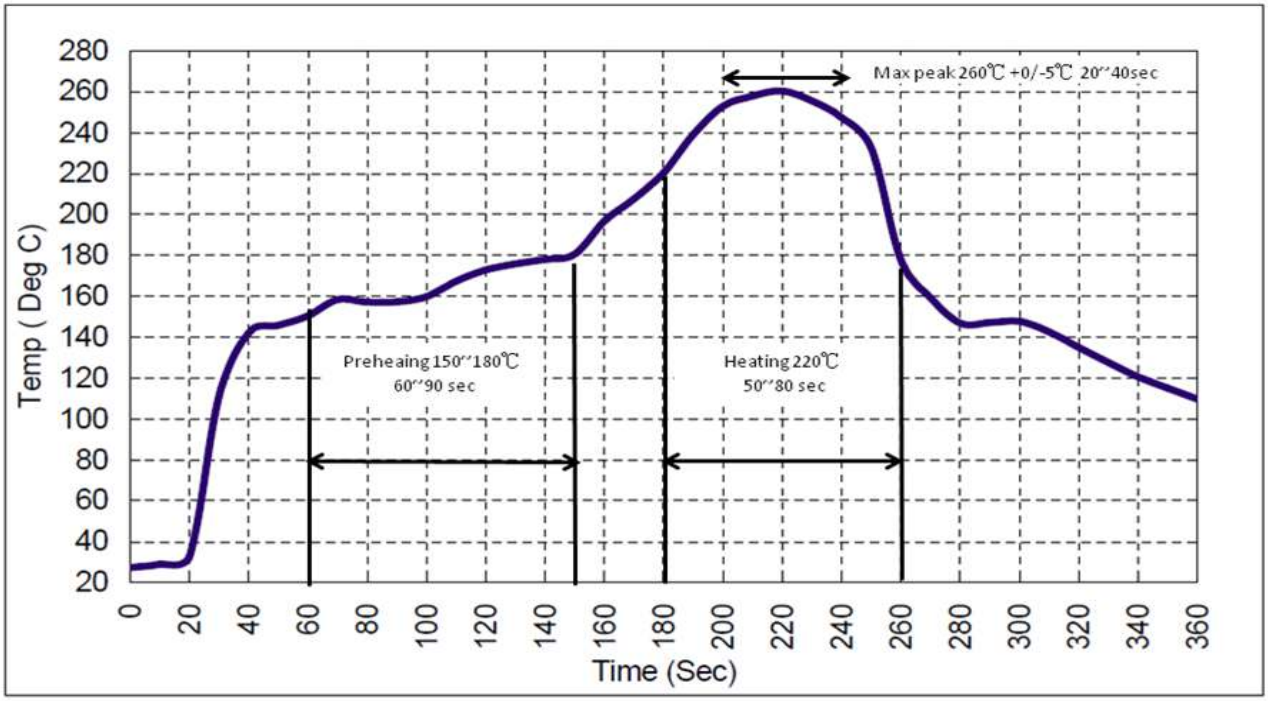
5.4 Evaluation Board Matching Circuit

Each patch element uses two orthogonal feeds that need to be combined in a Saw Diplexer to ensure optimal axial ratio. Taoglas recommends our DXP.01, a high-performance Saw Diplexer specifically engineered for use with our multi feed patches.



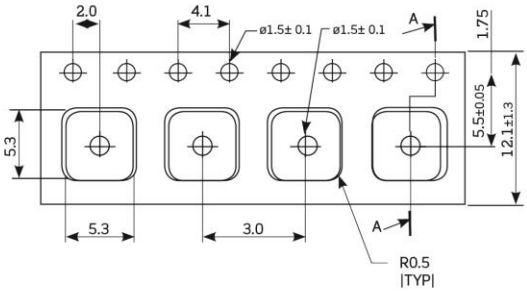
Designator	Type	Value	Manufacturer
L1	Inductor	4.7nH	TDK
L2	Inductor	4.3nH	TDK
L3	Inductor	3.3nH	TDK
C1	Capacitor	1.5pF	Murata
C2	Capacitor	1pF	Murata
C2	Capacitor	Not Fitted	-

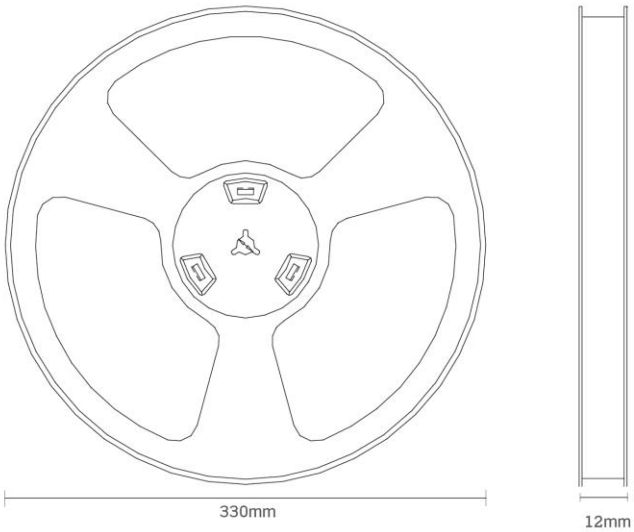
6. Recommended Reflow Profile



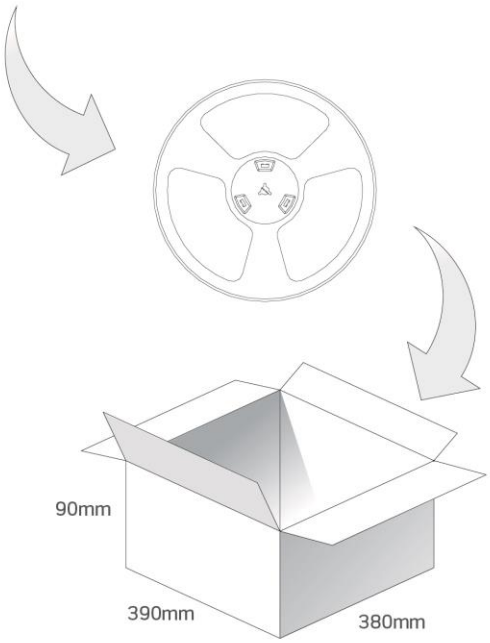
1. Preheating shall be fixed at 150~180°C for 60~90 seconds.
2. Ascending time to preheating temperature 150°C shall be 30 seconds minimum.
3. Heating shall be fixed at 220°C for 50~80 seconds and 260°C as the peak for 20-40 seconds.
4. Time: 2 times.

7. Packaging





1000 pcs DXP.01 reel
Dimensions - 330*12mm
Weight -0.2g



4 reels /4000 pcs
in one carton
Carton Dimensions - 390*380*90mm
Weight - 1.3Kg

Changelog for the datasheet

SPE-17-8-006 – DXP.01.A

Revision: D (Current Version)	
Date:	2023-01-23
Changes:	Updated MSL and drawing
Changes Made by:	Cesar Sousa

Previous Revisions

Revision: D	
Date:	2022-08-23
Changes:	Updated MSL and drawing
Changes Made by:	Cesar Sousa

Revision: C	
Date:	2022-08-02
Changes:	Added EVB drawing
Changes Made by:	Cesar Sousa

Revision: B	
Date:	2021-10-05
Changes:	Updated MSL and drawing
Changes Made by:	Jack Conroy

Revision: A (Original First Release)	
Date:	2017-01-25
Notes:	Initial Specification Release
Author:	Jack Conroy

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