

**APD140** 

### SCHOTTKY BARRIER RECTIFIERS

### **Product Summary**

V <sub>RRM</sub> (V)	I <sub>O</sub> (A)	V <sub>F (MAX)</sub> (V) @ +25°C	I <sub>R (MAX)</sub> (mA) @ +25°C
40	1	0.5	0.5

## Description

The APD140 is a low voltage dual Schottky rectifier suited for switch mode power supplies and other power converters. This device is intended for use in medium voltage operation, and particularly, in high frequency circuits where low switching losses and low noise are required.

The APD140 is available in standard DO-214AC and DO-41 packages.

DO-41

# **Applications**

- Low Voltage High Frequency Inverters
- DC-DC Converters
- Free Wheeling
- Polarity Protection

#### Features

- Low Forward Voltage: 0.5V @ +25°C
- High Surge Current Capacity
- +125°C Operating Junction Temperature
- 1A Total
- Guard-Ring for Stress Protection
- DO-41
  - Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Available in "Green" Packages: DO-214AC, DO-41
  - Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
  - Halogen and Antimony Free. "Green" Device (Note 3)

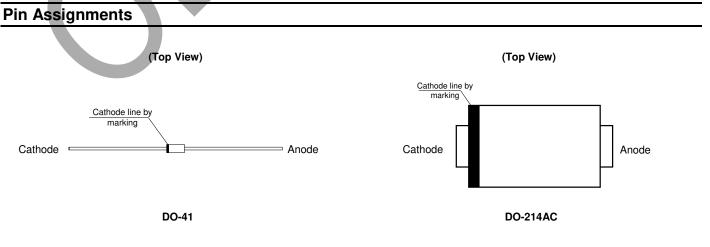
## **Mechanical Data**

- Case: DO-214AC, DO-41
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (3)
- Weight
  - DO-41 0.33Grams (Approximately)
    - DO-214AC 0.062Grams (Approximately)



#### DO-214AC

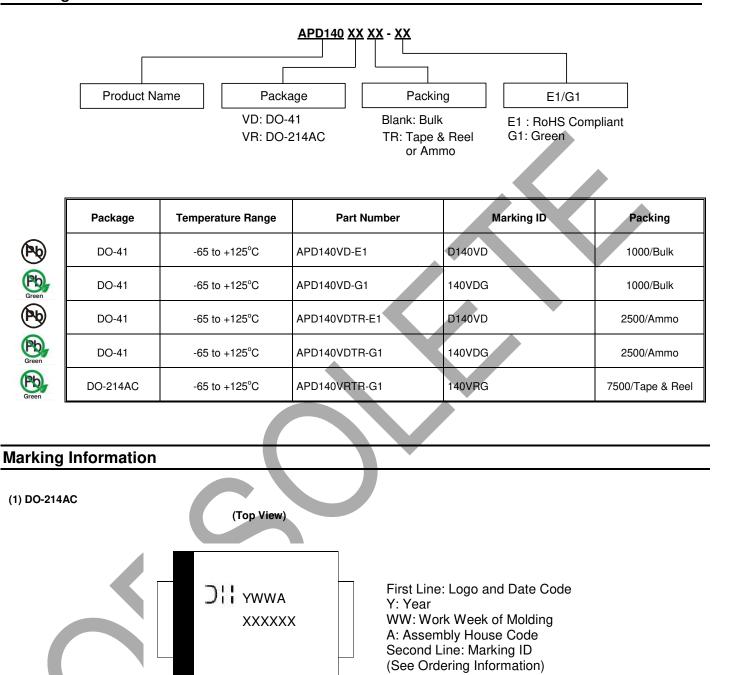
- Notes: 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
  - 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  - 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





### **Ordering Information**

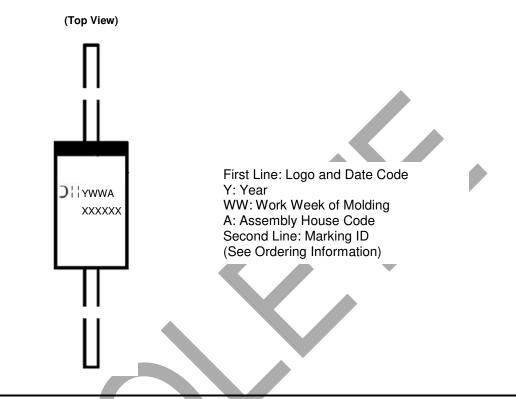
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## Marking Information (Cont.)





### **Maximum Ratings** ( $T_A = +25^{\circ}C$ , unless otherwise noted.) (Note 4)

Characteristic	Symbol	Rating	Unit	
Maximum Repetitive Reverse Voltage	V <sub>RRM</sub>			
Maximum DC Blocking Voltage	V <sub>DC</sub>	40	V	
Maximum RMS Voltage	V <sub>RMS</sub>	28	V	
Average Rectified Forward Current 0.375 " (9.5mm) Lead Length (See Figure 1)	I <sub>F(AV)</sub>	1.0	A	
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-wave on Rated Load	I <sub>FSM</sub>	35	A	
Voltage Rate of Change (Rated V <sub>R</sub> )	dv/dt	10000	V/µS	
Operating Junction Temperature Range (Note 5)	TJ	-65 to +125	°C	
Storage Temperature Range	T <sub>ST</sub>	-65 to +150	°C	

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

5. The heat generated must be less than the thermal conductivity from Junction to Ambient:  $dP_D/dT_J < 1/\theta_{JA}$ .



# Thermal Characteristics (T<sub>A</sub> = +25°C, unless otherwise noted.)

Characteristic	Symbol	Rating		Unit
Typical Thermal Resistance (Note 6)	Rθ <sub>JA</sub>	DO-41	65	°C/W
		DO-214AC	100	

Note 6: Device mounted on heat sink, with minimum recommended pad layout per http://www.diodes.com/package-outlines.html.

## Electrical Characteristics (T<sub>A</sub> = +25°C, unless otherwise noted.)

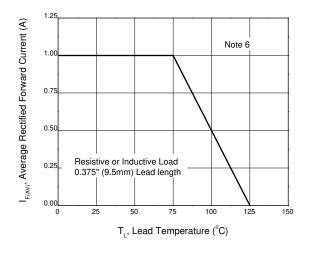
Characteristic	Symbol	Rating	Unit	Test Condition
Forward Voltage @ $I_F = 1.0A$	VF	0.5	V	-
Reverse Current @ Rated V <sub>R</sub> (Note 7)		0.5 mA		T <sub>A</sub> = +25°C
	IR			$T_A = +100^{\circ}C$

Note 7: Short duration pulse test used to minimize self-heating effect, Pulse Test: 300µs pulse width, 1.0% duty cycle.

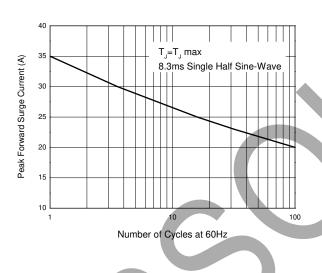


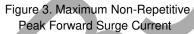
# PART OBSOLETE - NO ALTERNATE PART

**APD140** 



### Figure 1. Forward Current Derating Curve





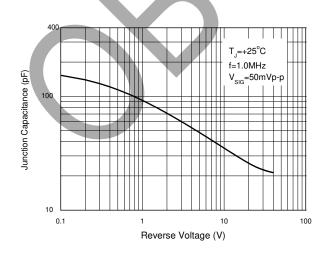
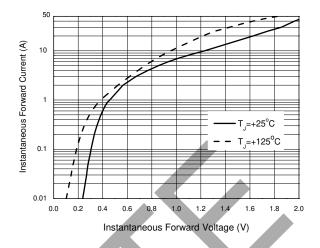
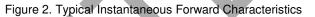
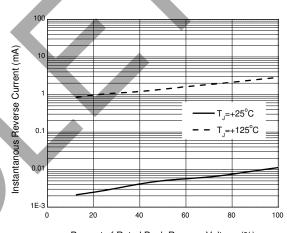


Figure 5. Typical Junction Capacitance







Percent of Rated Peak Reverse Voltage (%)

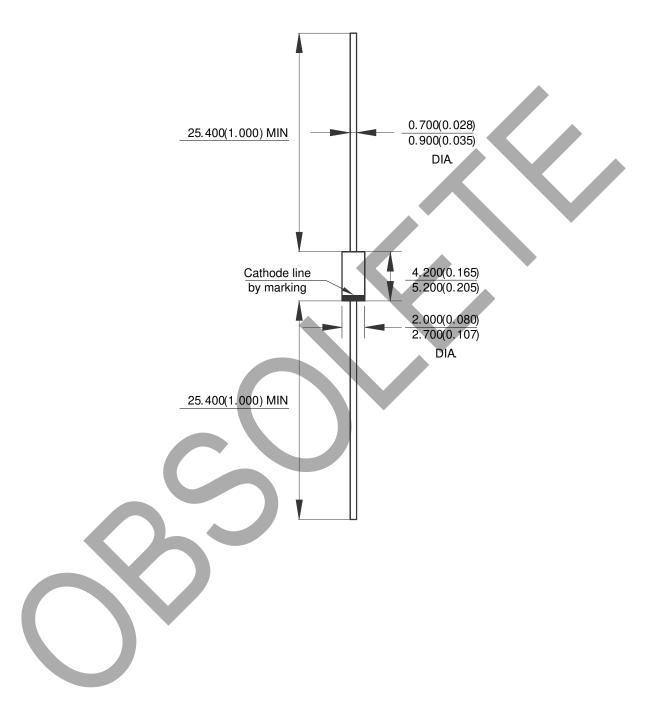
Figure 4. Typical Reverse Characteristics



**APD140** 

## Package Outline Dimensions (All dimensions in mm(inch).)

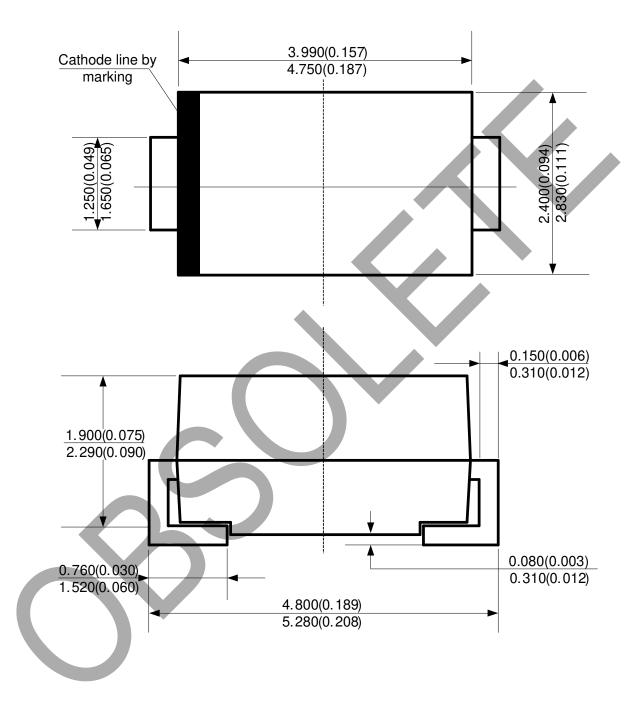
#### (1) Package Type: DO-41





## Package Outline Dimensions (Cont. All dimensions in mm(inch).)

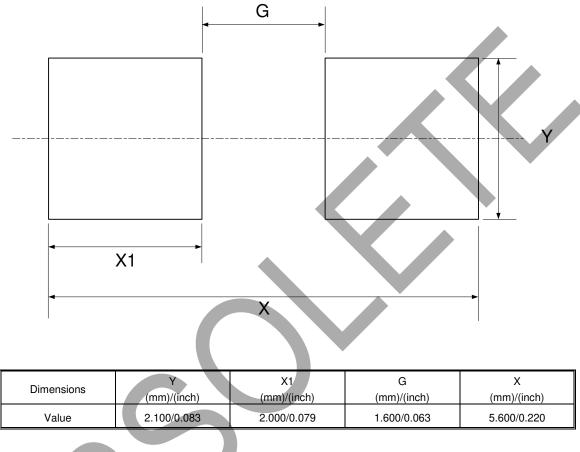
#### (2) Package Type: DO-214AC





# **Suggested Pad Layout**

#### (1) Package Type: DO-214AC







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