

# N-Channel Super Trench Power MOSFET

#### Description

The RM180N60T2 uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

#### **General Features**

- V<sub>DS</sub> =60V,I<sub>D</sub> =180A
  D = 100 m Q = 0
- $R_{DS(ON)} < 2.9 m\Omega @ V_{GS}=10V$  (Typ:2.5m $\Omega$ )
- Excellent gate charge x R<sub>DS(on)</sub> product
- Very low on-resistance R<sub>DS(on)</sub>
- 150 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

#### Application

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification
- Halogen-free

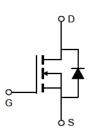
#### 100% UIS TESTED! 100% ΔVds TESTED!

#### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
180N60	RM180N60T2	TO-220-3L	-	-	-

### Absolute Maximum Ratings (Tc=25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	Vds	60	V	
Gate-Source Voltage	Vgs	±20	V	
Drain Current-Continuous (Silicon Limited)	I <sub>D</sub>	180	А	
Drain Current-Continuous(T <sub>C</sub> =100°C)	I <sub>D</sub> (100℃)	126	А	
Pulsed Drain Current	I <sub>DM</sub>	720	А	
Maximum Power Dissipation	PD	220	W	
Derating factor		1.47	W/℃	
Single pulse avalanche energy (Note 5)	E <sub>AS</sub>	1036	mJ	
Operating Junction and Storage Temperature Range	T <sub>J</sub> ,T <sub>STG</sub>	-55 To 175	°C	
Thermal Characteristic				
Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	R <sub>θJC</sub>	0.68	°C/W	



Schematic diagram



TO-220-3L top view

#### Electrical Characteristics (T<sub>c</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics			-			
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	$V_{GS}$ =0V I <sub>D</sub> =250µA	60		-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =60V,V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V,V <sub>DS</sub> =0V	-	-	±100	nA
On Characteristics (Note 3)	····		•			•
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_D=250\mu A$	2.0	2.8	4.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	2.5	2.9	mΩ
Forward Transconductance	<b>g</b> fs	V <sub>DS</sub> =5V,I <sub>D</sub> =20A	50	-	-	S
Dynamic Characteristics (Note4)						-
Input Capacitance	C <sub>lss</sub>	<u> </u>	-	4500	-	PF
Output Capacitance	C <sub>oss</sub>	$V_{DS}$ =30V, $V_{GS}$ =0V,	-	965	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>	F=1.0MHz	-	24	-	PF
Switching Characteristics (Note 4)	····		•			
Turn-on Delay Time	t <sub>d(on)</sub>		-	6	-	nS
Turn-on Rise Time	tr	V <sub>DD</sub> =30V,I <sub>D</sub> =20A	-	11	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>	$V_{GS}$ =10V, $R_{G}$ =4.7 $\Omega$	-	23	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	3	-	nS
Total Gate Charge	Qg	<u>)/ -20)/1 -200</u>	-	70	-	nC
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ =30V,I <sub>D</sub> =20A,	-	18.6	-	nC
Gate-Drain Charge	Q <sub>gd</sub>	V <sub>GS</sub> =10V	-	15.3	-	nC
Drain-Source Diode Characteristics	<b>I</b>					
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V,I <sub>S</sub> =150A	-		1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	180	А
Reverse Recovery Time	trr	$T_J = 25^{\circ}C, I_F = I_S$	-	50		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs <sup>(Note3)</sup>	-	66		nC

#### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.

2. Surface Mounted on FR4 Board, t  $\leq$  10 sec.

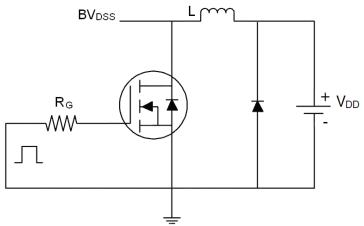
3. Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2%.

4. Guaranteed by design, not subject to production

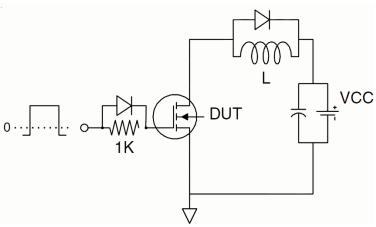
5. EAS condition : Tj=25  $^\circ \!\! \mathbb{C}$  ,V\_{DD}=30V,V\_G=10V,L=0.5mH,Rg=25\Omega

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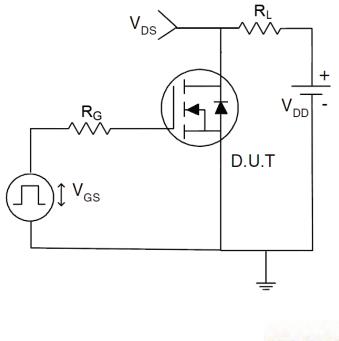
# Test Circuit 1) E<sub>AS</sub> test Circuit



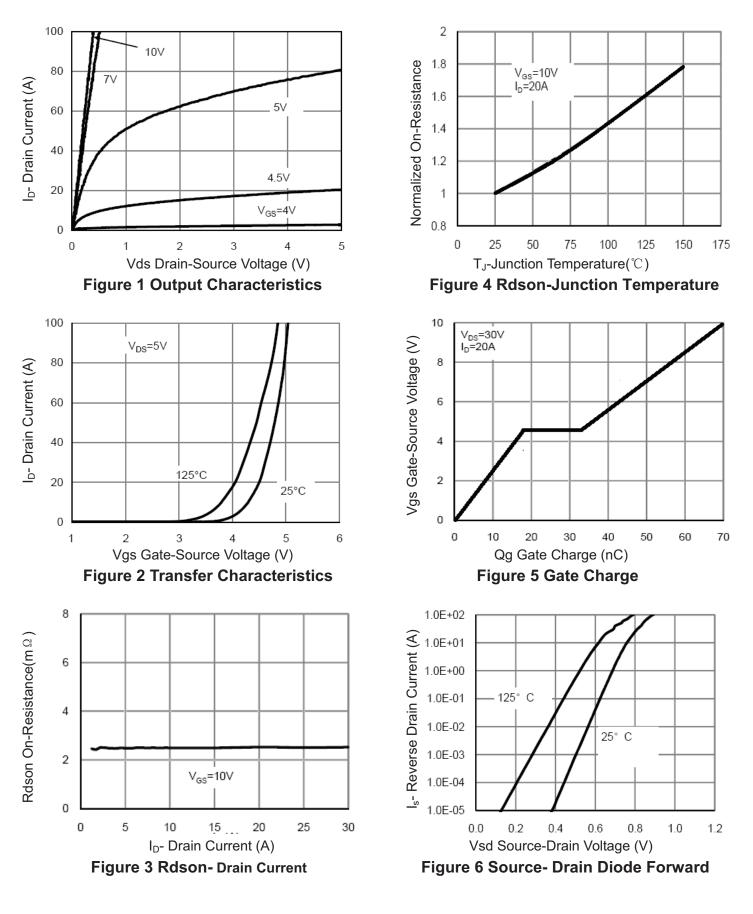
#### 2) Gate charge test Circuit



3) Switch Time Test Circuit

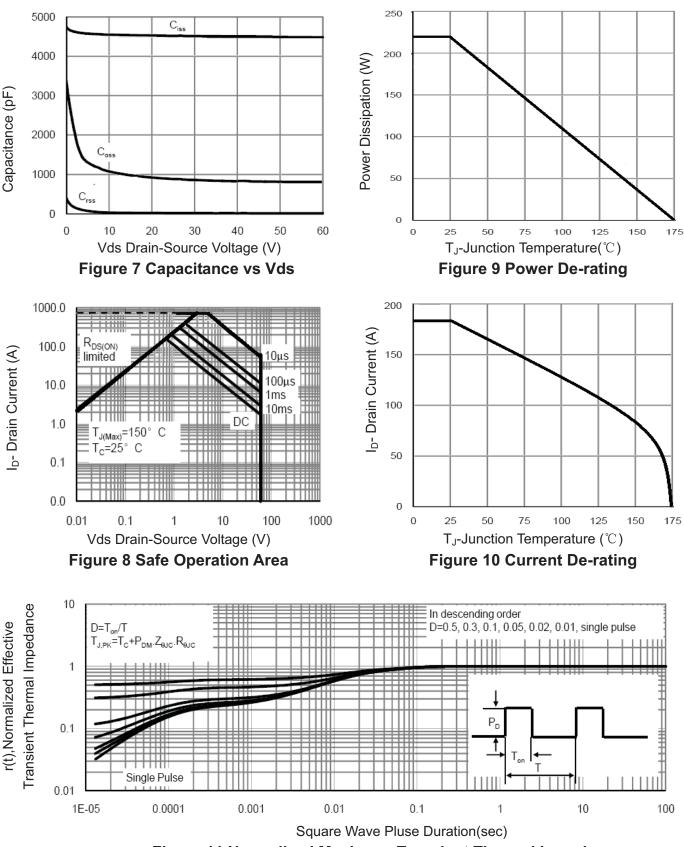






## **RATING AND CHARACTERISTICS CURVES (RM180N60T2)**

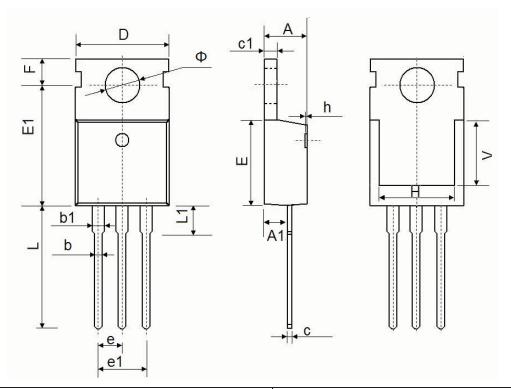




# **RATING AND CHARACTERISTICS CURVES (RM180N60T2)**

Figure 11 Normalized Maximum Transient Thermal Impedance

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Symbol	Dimensions	In Millimeters	Dimensions In Inches		
	Min.	Max.	Min.	Max.	
A	4.400	4.600	0.173	0.181	
A1	2.250	2.550	0.089	0.100	
b	0.710	0.910	0.028	0.036	
b1	1.170	1.370	0.046	0.054	
с	0.330	0.650	0.013	0.026	
c1	1.200	1.400	0.047	0.055	
D	9.910	10.250	0.390	0.404	
E	8.9500	9.750	0.352	0.384	
E1	12.650	12.950	0.498	0.510	
е	2.540 TYP.		0.100 TYP.		
e1	4.980	5.180	0.196	0.204	
F	2.650	2.950	0.104	0.116	
Н	7.900	8.100	0.311	0.319	
h	0.000	0.300	0.000	0.012	
L	12.900	13.400	0.508	0.528	
L1	2.850	3.250	0.112	0.128	
V	7.500 REF.		0.295 REF.		
Φ	3.400	3.800	0.134	0.150	



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