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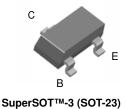
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September 2018

FSB660A PNP Low Saturation Transistor

Description

These devices are designed with high-current gain and low saturation voltage with collector currents up to 2 A continuous.



Ordering Information

Part Number	Marking	Package	Packing Method
FSB660A	660A	SSOT 3L	Tape and Reel

Absolute Maximum Ratings(1),(2)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at Values are at $T_A = 25^{\circ}$ C unless otherwise noted.

Symbol	Parameter	Value	Unit
V _{CEO}	Collector-Emitter Voltage	-60	V
V _{CBO}	Collector-Base Voltage	-60	V
V _{EBO}	Emitter-Base Voltage	-5	V
I _C	Collector Current - Continuous	-2	Α
T _{J,} T _{STG}	Operating and Storage Junction Temperature Range	-55 to +150	°C

Notes:

- 1. These ratings are based on a maximum junction temperature of 150°C.
- 2. These are steady-state limits. ON Semiconductor should be consulted on applications involving pulsed or lowduty cycle operations.

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Thermal Characteristics(3)

Values are at $T_A = 25^{\circ}C$ unless otherwise noted.

Symbol	Parameter	Max.	Unit
P_{D}	Total Device Dissipation	500	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	250	°C/W

Note:

3. PCB size: FR-4 76 x 114 x 1.57 mm^3 (3.0 inch x 4.5 inch x 0.062 inch) with minimum land pattern size.

Electrical Characteristics(4)

Values are at $T_A = 25$ °C unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
BV _{CEO}	Collector-Emitter Breakdown Voltage	I _C = -10 mA	-60		V
BV _{CBO}	Collector-Base Breakdown Voltage	I _C = -100 μA	-60		V
BV _{EBO}	Emitter-Base Breakdown Voltage	I _E = -100 μA	-5		V
	Collector Cut-Off Current	V _{CB} = -30 V		-100	nA
ICBO		V _{CB} = -30 V, T _A = 100°C		-10	μΑ
I _{EBO}	Emitter Cut-Off Current	V _{EB} = -4 V		-100	nA
	DC Current Gain	$I_C = -100 \text{ mA}, V_{CE} = -2 \text{ V}$	70		
h _{FE}		$I_C = -500 \text{ mA}, V_{CE} = -2 \text{ V}$	250	550	
		I _C = -1 A, V _{CE} = -2 V	80		
		I _C = -2 A, V _{CE} = -2 V	40		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = -1 A, I _B = -100 mA		-300	mV
		I _C = -2 A, I _B = -200 mA		-300	111 V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = -1 A, I _B = -100 mV		-1.25	V
V _{BE} (on)	Base-Emitter On Voltage	I _C = -1 A, V _{CE} = -2 V		-1	V
C _{ob}	Output Capacitance	V _{CB} = -10 V, I _E = 0, f = 1 MHz		30	pF
f _T	Transition Frequency	$I_C = -100 \text{ mA}, V_{CE} = -5 \text{ V},$ f = 100 MHz	75		MHz

Note:

4. Pulse test: pulse width \leq 300 μ s, duty cycle \leq 2.0%.

Typical Performance Characteristics

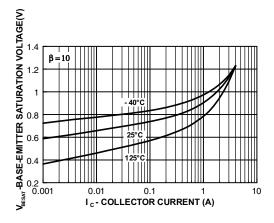


Figure 1. Base-Emitter Saturation Voltage vs.
Collector Current

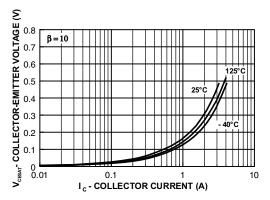


Figure 3. Collector-Emitter Saturation Voltage vs.
Collector Current

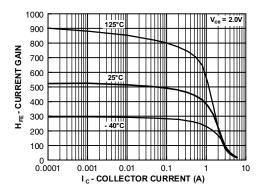


Figure 5. Current Gain vs. Collector Current

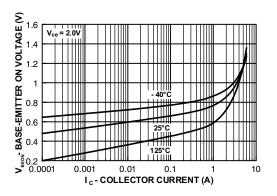


Figure 2. Base-Emitter On Voltage vs. Collector Current

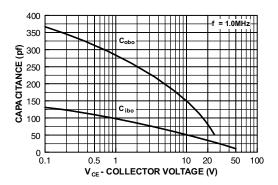
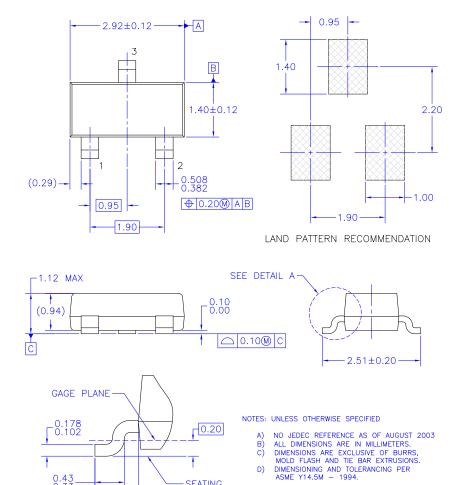


Figure 4. Input/Output Capacitance vs. Reverse Bias Voltage

Physical Dimensions

SSOT 3L



MA03BREVB

0.43

Figure 6. MOLDED PACKAGE, SUPERSOT, 3-LEAD (ACTIVE)

SEATING **PLANE**

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Definition of Terms

Definition of Terms			
Datasheet Identification	Product Status	Definition	
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