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# 4-Channel RGB Video Filter with Asynchronous CVBS Input

### **General Description**

The MAX7448, 4-channel, buffered video reconstruction filter is ideal for anti-aliasing and digital-to-analog converter (DAC)-smoothing video applications or wherever analog video is reconstructed from a digital data stream (such as cable/satellite/terrestrial set-top boxes, DVD players, hard-disk recorders (HDRs), and personal video recorders (PVRs)). This device operates from a single +5V supply and has a flat passband out to 5MHz with a stopband attenuation of 43dB at 27MHz. This makes it ideal for use with NTSC, PAL, and standard-definition digital TV (SDTV) video systems. Each output is capable of driving two standard 150 $\Omega$  video loads.

The MAX7448 processes RGB and asynchronous CVBS video signals. The output video buffers have a fixed gain of +6dB. The channel used for CVBS video has high-frequency boost circuitry, which provides picture sharpness with +1.2dB of gain boost without degradation in the stopband. The output video drivers can be disabled with an external pin.

The MAX7448 is available in a 14-pin TSSOP package with an exposed pad, and is specified over the -40°C to +85°C extended temperature range.

Set-Top Boxes/HDRs Game Consoles Desktop Video Editors \_**Applications**DVD Players
Digital VCRs

### \_Features

- 4-Channel Filter and Buffer for RGB and CVBS Video Signals
- Filter Response Ideal for NTSC, PAL, and Interlaced SDTV Video Signals
- ♦ 43dB (typ) Stopband Attenuation at 27MHz
- ♦ ±0.75dB (max) Passband Ripple Out to 5MHz
- Blanking Level Voltage on Cable <1V</p>
- ♦ Each Channel Drives Two 150 $\Omega$  Video Loads
- ♦ +5V Single-Supply Voltage
- Small 14-Pin TSSOP Package

## **Ordering Information**

**Functional Diagram** 

MAX7448EUD -40°C to +85°C	14 TSSOP-F	EP* U14E-3

\*EP = Exposed pad.

Pin Configuration appears at end of data sheet.

#### $V_{CC}$ ENCODER 750 750 0.1µF OUTA INA SYNC LOWPASS G (WITH SYNC) 750 DETECTOR FILTER 75Ω 0.1µF OUTB INB LOWPASS R D/A 75O FILTER 750 0.1µF OUTC INC LOWPASS B D/A 750 FILTER 750 0.1µF OUTD IND CVBS LOWPASS SYNC D/A (WITH SYNC) FII TFR DETECTOR *//*//XI/// \*OPTIONAL CAPACITORS MAX7448 GND DISABLE -

## 

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

## **4-Channel RGB Video Filter with Asynchronous CVBS Input**

### **ABSOLUTE MAXIMUM RATINGS**

V <sub>CC</sub> to GND	+6V
All Other Pins to GND	
Maximum Current into Any Pin Except	
Continuous Power Dissipation (T <sub>A</sub> = +	
TSSOP-EP (derate 20.8mW/°C abov	/e +70°C)1667mW

Operating Temperature Range	40°C to +85°C
Storage Temperature Range	65°C to +150°C
Junction Temperature	+150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## **ELECTRICAL CHARACTERISTICS**

 $(V_{CC} = +5V \pm 5\%, C_L = 0 \text{ to } 20\text{pF}, R_L = 75\Omega \text{ to GND} \text{ for DC-coupled load}, R_L = 75\Omega \text{ to } V_{CC} / 2 \text{ for AC-coupled load}, C_{IN_} = 0.1\mu\text{F}, \text{GAIN} = +6\text{dB}, T_A = T_{MIN} \text{ to } T_{MAX}, \text{ unless otherwise noted}. Typical values are at V_{CC} = +5V, T_A = +25^{\circ}\text{C}.)$ 

PARAMETER	SYMBOL	CONDITIONS		MIN	ТҮР	MAX	UNITS
Passband Response		f = 100 kHz to 5MHz,	Channels A, B, C	+0.75	+0.15	+0.75	dB
		relative to 100kHz	Channel D	+0.9	+1.2	+1.5	uБ
Stopband Attenuation	A <sub>SB</sub>	f≥27MHz		39	43		dB
Differential Gain	dG	5-step modulated staircase			0.15	0.50	%
Differential Phase	dθ	5-step modulated staircase			0.15	0.50	Degrees
Signal-to-Noise Ratio	SNR	Peak signal ( $2V_{P-P}$ ) to RMS noise, f = 100Hz to 50MHz			80		dB
		Deviation from 100kHz	Channels A, B, C		11	20	ns
Group Delay Deviation	Δtg	to 4.1MHz	Channel D		17	30	
Line-Time Distortion	H <sub>DIST</sub>	18µs, 100 IRE bar				0.3	%
Field-Time Distortion	V <sub>DIST</sub>	130 lines, 18µs, 100 IRE bar				0.5	%
Clamp Settling Time	<b>t</b> CLAMP	To ±1%			430		Lines
Output DC Clamp Level		Channels A, D Channels B, C		0.6	0.9	1.1	V
Output DC Clamp Level				1.1	1.5	1.8	
Low-Frequency Gain Accuracy	Av	f = 100kHz		-3		+3	%
Low-Frequency Gain Matching	AV(MATCH)	Low-frequency channel-to-channel matching, f = 100kHz				4	%
Group Delay Matching	tg(MATCH)	Low-frequency channel-to-channel matching, f = 100kHz			2		ns
Channel-to-Channel Crosstalk	XTALK	f = 100kHz to 3.58MHz			60		dB
Output Short-Circuit Current	ISC	OUT_ shorted to GND or V <sub>CC</sub>			70		mA
Input Leakage Current	lin					10	μΑ
		Channels A, D Channels B, C				1.2	
Input Dynamic Swing						0.9	Vp-p
SUPPLY							
Supply Voltage Range	V <sub>CC</sub>			4.75		5.25	V
Supply Current	ICC	No load			100	140	mA
Power-Supply Rejection Ratio	PSRR	$V_{IN} = 100mV_{P-P}, f = 0 \text{ to } 3.5MHz$			40		dB

## 4-Channel RGB Video Filter with Asynchronous CVBS Input

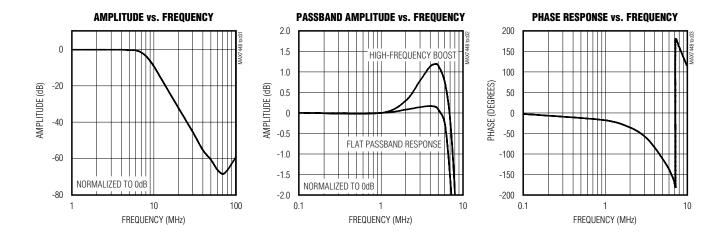
### **ELECTRICAL CHARACTERISTICS (continued)**

 $(V_{CC} = +5V \pm 5\%, C_L = 0 \text{ to } 20\text{pF}, R_L = 75\Omega \text{ to GND} \text{ for DC-coupled load}, R_L = 75\Omega \text{ to } V_{CC} / 2 \text{ for AC-coupled load}, C_{IN_} = 0.1\mu\text{F}, GAIN = +6dB, T_A = T_{MIN} \text{ to } T_{MAX}$ , unless otherwise noted. Typical values are at  $V_{CC} = +5V$ ,  $T_A = +25^{\circ}C$ .)

PARAMETER	SYMBOL	CONDITIONS	MIN	ТҮР	МАХ	UNITS
DISABLE						
Output Impedance during Disable	ZDISABLE	At 5MHz		2		kΩ
Disable Logic-Input High Voltage	VIH		2.0			V
Disable Logic-Input Low Voltage	VIL				0.8	V
Disable Logic-Input Current	IDISABLE	$V_{IL} = 0V \text{ (sink)}, V_{IH} = V_{CC} \text{ (source)}$			±10	μA

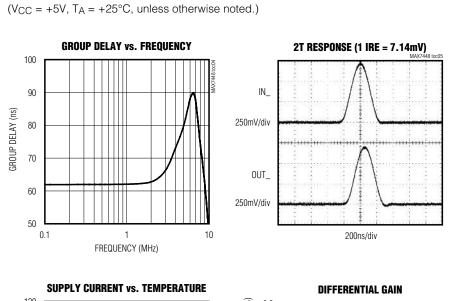
(V<sub>CC</sub> = +5V,  $T_A$  = +25°C, unless otherwise noted.)



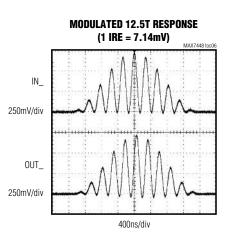


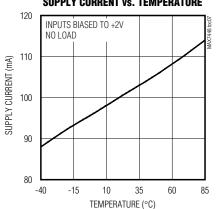
## **4-Channel RGB Video Filter with Asynchronous CVBS Input**

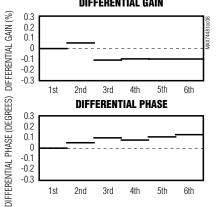


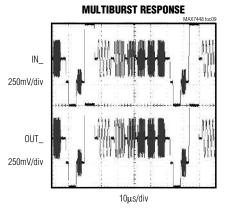


## **Typical Operating Characteristics (continued)**









## 4-Channel RGB Video Filter with Asynchronous CVBS Input

## Pin Description

PIN	NAME	FUNCTION	
1	INA	Channel A Video Input. Use channel A for the green (G with sync) video signal. AC-couple INA with a series 0.1µF capacitor.	
2	INB	Channel B Video Input. Use channel B for the red (R) video signal. AC-couple INB with a series 0.1µF capacitor.	
3	INC	Channel C Video Input. Use channel C for the blue (B) video signal. AC-couple INC with a series $0.1 \mu$ F capacitor.	
4	IND	Channel D Video Input. Use channel D for a CVBS (with sync) signal. AC-couple IND with a series 0.1µF capacitor.	
5	DISABLE	Disable Logic Input. A logic-low on DISABLE enables the output buffers. A logic-high on DISABLE disal all buffer outputs and puts them in a high-impedance state.	
6–9	GND	Ground	
10	V <sub>CC</sub>	+5V Supply Input	
11	OUTD	Channel D Buffer Output. This output can be either AC- or DC-coupled.	
12	OUTC	Channel C Buffer Output. This output can be either AC- or DC-coupled.	

## **Detailed Description**

The MAX7448 filters and buffers video-encoder DAC outputs in applications such as set-top boxes, HDRs, DVD players, and digital VCRs. The MAX7448 reconstructs and cleans up analog video signals from the output of DAC video encoders. Each channel consists of a lowpass filter and an output video buffer that can drive two standard  $150\Omega$  video loads.

The MAX7448 is designed to process R, G (with sync), B, and CVBSASYNC video signals. The video signal processed by channel A (G video signal) requires a sync pulse. This sync pulse provides the required timing for channels A, B, and C. Channel D allows an asynchronous video signal to be processed with its own local sync separator.

This device operates from a single +5V supply and has a nominal cutoff frequency of 5MHz optimized for NTSC, PAL, and SDTV.

#### **Filter**

#### Filter Response

The reconstruction filter consists of two 2nd-order Sallen-Key stages. The Butterworth-type response features a maximally flat passband for NTSC and PAL bandwidths. The stopband offers at least 43dB (typ) of attenuation at a video-encoder DAC sampling frequency of 27MHz (see the *Typical Operating Characteristics*).

#### High-Frequency Boost

The +1.2dB high-frequency boost on channel D (CVBS video signal) increases image sharpness by compensating for signal degradation and rolloff in the video encoder. Channels A, B, and C do not boost high-frequency signals and have a flat response over the video bandwidth.

#### **Output Buffers**

Each output buffer has a fixed gain of +6dB and can drive two 150 $\Omega$  video loads with a 2V<sub>P-P</sub> signal. The MAX7448 can drive an AC load or drive the video load directly without using a large output capacitor. The output buffers drive DC loads with an output blanking level of less than 1V.

## **4-Channel RGB Video Filter with Asynchronous CVBS Input**

#### **Output Clamp Level**

When a sync pulse is detected on channel A, the DC restore loop is activated for channel A, B, and C. Channel D's DC restore loop is activated by the sync pulse on channel D. The function of the loop is to set the DC level of the video signal to the specified level of the signal type (R, G, B, CVBS). See Table 1 for clamp levels and sync sources.

## Table 1. Output Clamp Level and SyncSource

CHANNEL	CLAMP LEVEL (V)	SYNC SOURCE
А	0.9	Channel A
В	1.5	Channel A
С	1.5	Channel A
D	0.9	Channel D

### **Applications Information**

#### Input Considerations

Use 0.1µF ceramic capacitors to AC-couple the inputs. These input capacitors store a DC level so the outputs are clamped to an appropriate DC voltage level.

#### **Output Considerations**

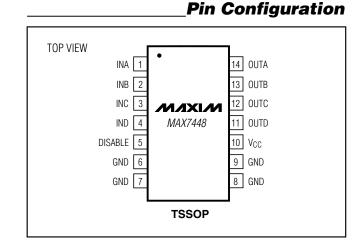
The outputs are typically connected to a 75 $\Omega$  series back-match resistor followed by the video cable. Because of the inherent divide-by-two of this configuration, the voltage on the video cable is always less than 1V, complying with industry-standard video requirements such as the European SCART standard (which allows up to 2V of DC on the video cable). The video buffer can also drive an AC-coupled video load. Good video performance is achieved with an output capacitor as low as 220µF.

#### **Power-Supply Bypassing and Layout**

The MAX7448 operates from a single +5V supply. Bypass V<sub>CC</sub> to GND with a 0.1µF capacitor. Place all external components as close to the device as possible.

#### **Exposed Pads**

The TSSOP-EP package has an exposed pad on the bottom of the package. This pad is electrically connected to GND and should be connected to the ground plane for improved thermal conductivity. Do not route signals under this package.



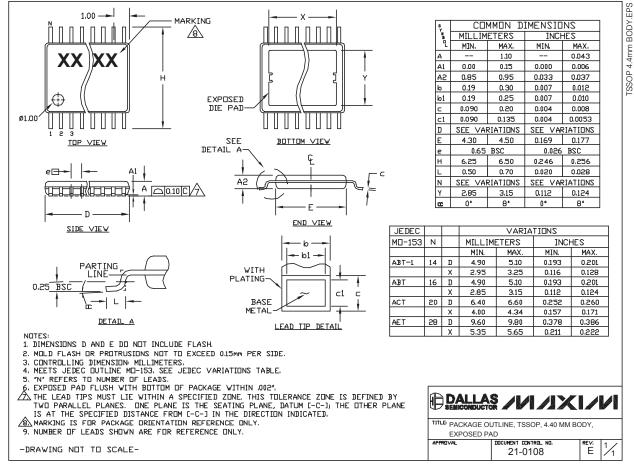
### Chip Information

TRANSISTOR COUNT: 6300 PROCESS: BICMOS

## 4-Channel RGB Video Filter with Asynchronous CVBS Input

### **Package Information**

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to **www.maxim-ic.com/packages**.)



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