Taiwan Semiconductor

# 1A, 200V - 1000V Standard Surface Mount Rectifier

## FEATURES

- Glass passivated chip junction
- Ideal for automated placement
- Low forward voltage drop
- High surge current capability
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free according to IEC 61249-2-21

# APPLICATIONS

- DC to DC converter
- Switching mode converters and inverters
- General purpose

## **MECHANICAL DATA**

- Case: DO-214AC (SMA)
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 1A whisker test
- Polarity: Indicated by cathode band
- Weight: 0.060g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
I <sub>F</sub>	1	А	
V <sub>RRM</sub>	200 - 1000	V	
I <sub>FSM</sub>	30, 40	А	
T <sub>J MAX</sub>	150	°C	
Package	DO-214AC (SMA)		
Configuration	Single die		





DO-214AC (SMA)



ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25°C unless otherwise noted)							
PARAMETER	SYMBOL	S1D-T	S1G-T	S1J-T	S1K-T	S1M-T	UNIT
Marking code on the device		S1D	S1G	S1J	S1K	S1M	
Repetitive peak reverse voltage	V <sub>RRM</sub>	200	400	600	800	1000	V
Reverse voltage, total rms value	V <sub>R(RMS)</sub>	140	280	420	560	700	V
Forward current	I <sub>F</sub>			1			Α
Peak forward surge current, 8.3ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	40 30		A			
Junction temperature	TJ	- 55 to +150		°C			
Storage temperature	T <sub>STG</sub>	- 55 to +150		°C			



THERMAL PERFORMANCE				
PARAMETER	SYMBOL	ТҮР	UNIT	
Junction-to-lead thermal resistance	R <sub>eJL</sub>	30	°C/W	
Junction-to-ambient thermal resistance	R <sub>eJA</sub>	85	°C/W	

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	ТҮР	MAX	UNIT
Forward voltage <sup>(1)</sup>	I <sub>F</sub> = 1A, T <sub>J</sub> = 25°C	V <sub>F</sub>	-	1.1	V
Reverse current @ rated V <sub>R</sub> <sup>(2)</sup>	$T_J = 25^{\circ}C$	I <sub>R</sub>	-	1	μA
	T <sub>J</sub> = 125°C		-	50	μA
Junction capacitance	1MHz, V <sub>R</sub> = 4.0V	CJ	12	-	pF
Reverse recovery time	$I_F = 0.5A, I_R = 1.0A, I_{rr} = 0.25A$	t <sub>rr</sub>	1500	-	ns

#### Notes:

1. Pulse test with PW = 0.3ms

2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE <sup>(1)</sup>	PACKAGE	PACKING	
S1x-T	DO-214AC (SMA)	7,500 / Tape & Reel	

Note:

1. "x" defines voltage from 200V (S1D-T) to 1000V (S1M-T)



INSTANTANEOUS REVERSE CURRENT (µA)

10

1

0.1

0.01

0.001

10 20 30

## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

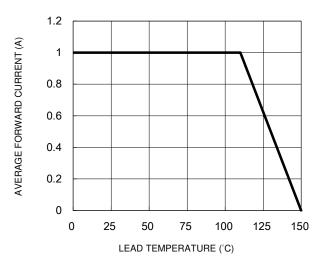
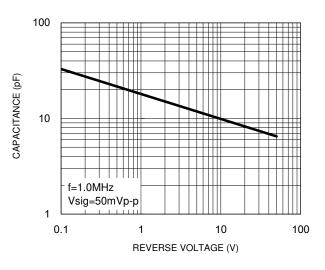


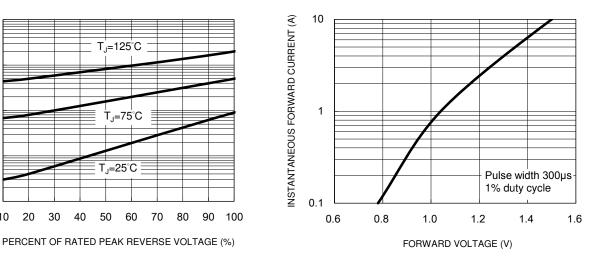
Fig.1 Forward Current Derating Curve

#### **Fig.3 Typical Reverse Characteristics**

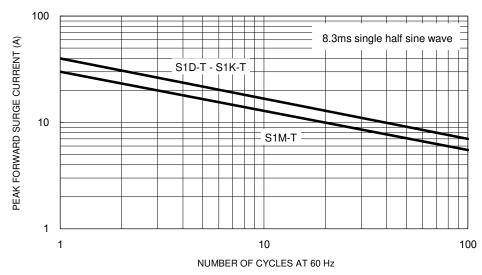


#### **Fig.2 Typical Junction Capacitance**

**Fig.4 Typical Forward Characteristics** 



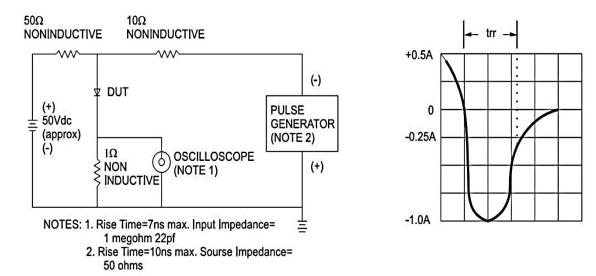
### Fig.5 Maximum Non-Repetitive Forward Surge Current





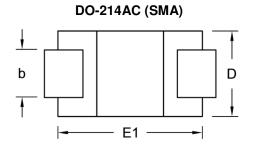
## **CHARACTERISTICS CURVES**

 $(T_A = 25^{\circ}C \text{ unless otherwise noted})$ 

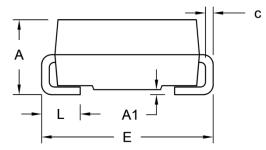


#### Fig.6 Reverse Recovery Time Characteristic and Test Circuit Diagram

# PACKAGE OUTLINE DIMENSIONS

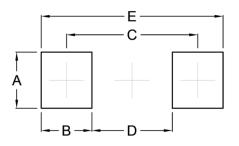


**5** TAIWAN SEMICONDUCTOR



DIM. Unit (mm		(mm)	Unit (inch)		
	Min.	Max.	Min.	Max.	
A	1.99	2.50	0.078	0.098	
A1	0.05	0.20	0.002	0.008	
b	1.27	1.58	0.050	0.062	
с	0.15	0.31	0.006	0.012	
D	2.29	2.83	0.090	0.111	
E	4.95	5.33	0.195	0.210	
E1	4.06	4.60	0.160	0.181	
L	0.90	1.41	0.035	0.056	

## SUGGESTED PAD LAYOUT



Symbol	Unit (mm)	Unit (inch)
A	1.68	0.066
В	1.52	0.060
С	3.93	0.155
D	2.41	0.095
E	5.45	0.215

## **MARKING DIAGRAM**



P/N	= Marking Code
-	

= Green Compound G

= Date Code YW

F = Factory Code



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