

HRC0201A

Silicon Schottky Barrier Diode for Rectifying

REJ03G0618-0200 Rev.2.00 Jan 09, 2009

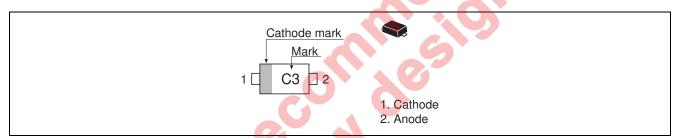
Features

- Low forward voltage drop and suitable for high efficiency rectifying.
- Ultra small Flat Lead Package (UFP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No. Laser Mark		Package Name	Package Code	Taping Abbreviation (Quantity)	
HRC0201ATRF	C3	UFP	PWSF0002ZA-A	TRF (4,000 pcs / reel)	

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Repetitive peak reverse voltage	V _{RRM} * ¹	15	V
Reverse voltage	V_R	15	V
Average rectified current	I ₀ * ¹	200	mA
Peak forward current	I _{FM}	300	mA
Non-Repetitive peak forward surge current	I _{FSM} *1	1	Α
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Notes: 1. See from Fig.4 to Fig.6, with polyimide board.

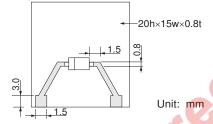
2. 10 ms sine wave 1 pulse.

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

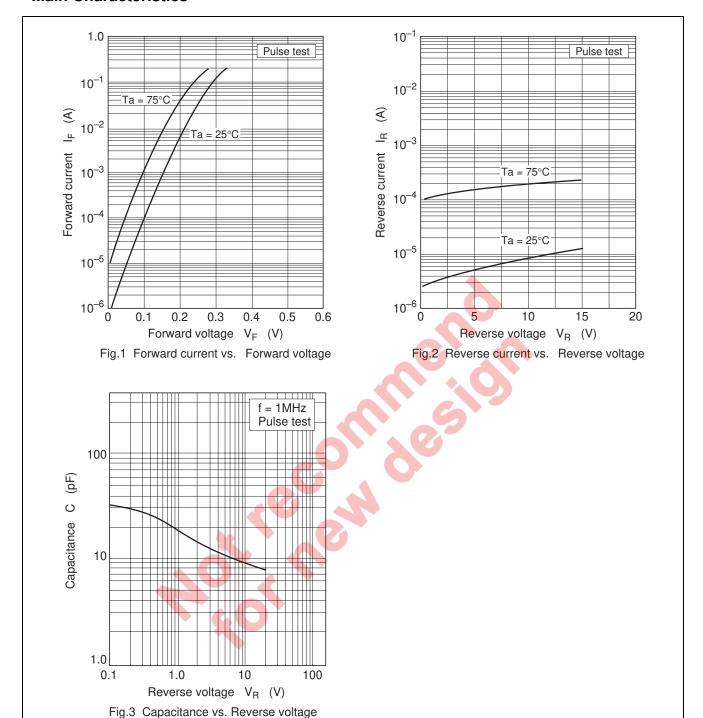
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _F	_	_	0.39	V	I _F = 200 mA
Reverse current	I _R	_	_	50	μΑ	V _R =6 V
Capacitance	С	_	18	- 6	pF	$V_R = 1 \text{ V, f} = 1 \text{ MHz}$
Thermal resistance	Rth(j-a)		600	-1	°C/W	Polyimide board *1

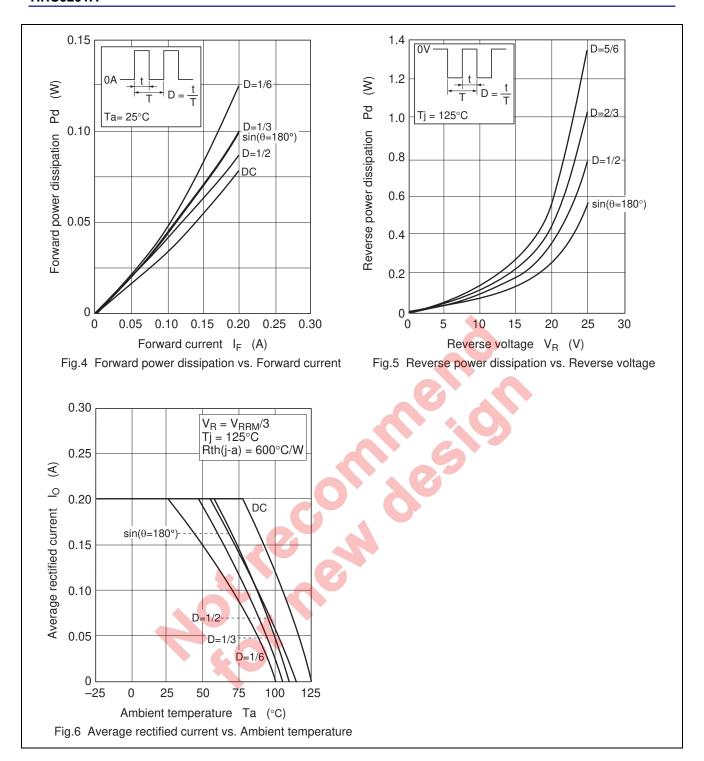
Note: 1. Polyimide board



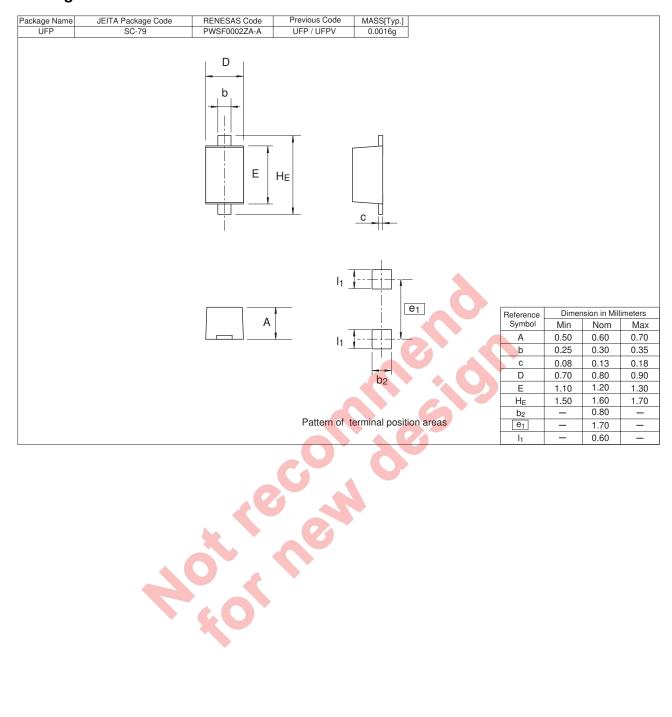
Note: In the UFP package, some lead is exposed because the tip of the lead is used as the cutting plane. Therefore, the solderability of the lead tip has been ignored. Please test and confirm before use.

Main Characteristics





Package Dimensions



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April 1st, 2010 Renesas Electronics Corporation

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