

Insulated Gate Bipolar Transistor (Trench IGBT), 175 A


SOT-227

PRODUCT SUMMARY	
V_{CES}	1200 V
$I_{C(DC)}$	175 A at 90 °C ⁽¹⁾
$V_{CE(on)}$ typical at 100 A, 25 °C	1.73 V
$I_{F(DC)}$	32 A at 90 °C
Speed	8 kHz to 30 kHz
Package	SOT-227
Circuit	Single switch diode

Note

⁽¹⁾ Maximum collector current admitted is 100 A, to not exceed the maximum temperature of terminals

FEATURES

- Trench IGBT technology with positive temperature coefficient
- Square RBSOA
- 10 μ s short circuit capability
- HEXFRED® antiparallel diodes with ultrasoft reverse recovery
- T_J maximum = 150 °C
- Fully isolated package
- Very low internal inductance (≤ 5 nH typical)
- Industry standard outline
- UL approved file E78996
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912


**RoHS
COMPLIANT**
BENEFITS

- Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- Direct mounting to heatsink
- Plug-in compatible with other SOT-227 packages
- Very low $V_{CE(on)}$
- Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V_{CES}		1200	V
Continuous collector current	I_C ⁽¹⁾	$T_C = 25$ °C	288	A
		$T_C = 90$ °C	175	
Pulsed collector current	I_{CM}		450	
Clamped inductive load current	I_{LM}		450	
Diode continuous forward current	I_F	$T_C = 25$ °C	54	
		$T_C = 90$ °C	32	
Gate to emitter voltage	V_{GE}		± 20	V
Power dissipation, IGBT	P_D	$T_C = 25$ °C	1087	W
		$T_C = 90$ °C	522	
Power dissipation, diode	P_D	$T_C = 25$ °C	219	
		$T_C = 90$ °C	105	
Isolation voltage	V_{ISOL}	Any terminal to case, $t = 1$ min	2500	V

Note

⁽¹⁾ Maximum collector current admitted is 100 A, to do not exceed the maximum temperature of terminals



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	V _{GE} = 0 V, I _C = 250 μA	1200	-	-	V	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 100 A	-	1.73	2.1		
		V _{GE} = 15 V, I _C = 100 A, T _J = 125 °C	-	1.98	2.2		
		V _{GE} = 15 V, I _C = 100 A, T _J = 150 °C	-	2.05	-		
Gate threshold voltage	V _{GE(th)}	V _{CE} = V _{GE} , I _C = 250 μA	-	5	-		
		V _{CE} = V _{GE} , I _C = 7.5 mA	4.9	5.9	7.9		
		V _{CE} = V _{GE} , I _C = 250 μA, T _J = 125 °C	-	2.9	-		
Temperature coefficient of threshold voltage	ΔV _{GE(th)} /ΔT _J	V _{CE} = V _{GE} , I _C = 1 mA (25 °C to 125 °C)	-	-17.6	-		mV/°C
Collector to emitter leakage current	I _{CES}	V _{GE} = 0 V, V _{CE} = 1200 V	-	0.9	100		μA
		V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 125 °C	-	0.85	10	mA	
		V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 150 °C	-	4	20		
Forward voltage drop, diode	V _{FM}	I _F = 40 A, V _{GE} = 0 V	-	3.12	3.44	V	
		I _F = 40 A, V _{GE} = 0 V, T _J = 125 °C	-	3.15	3.47		
		I _F = 40 A, V _{GE} = 0 V, T _J = 150 °C	-	3.25	-		
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA	

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Total gate charge (turn-on)	Q _g	I _C = 150 A (t _p < 400 μs, D < 2 %), V _{CC} = 600 V, V _{GE} = 15 V	-	830	-	nC		
Gate to emitter charge (turn-on)	Q _{ge}		-	180	-			
Gate to collector charge (turn-on)	Q _{gc}		-	380	-			
Turn-on switching loss	E _{on}	I _C = 100 A, V _{CC} = 720 V, V _{GE} = 15 V, R _g = 2.2 Ω, L = 500 μH, T _J = 25 °C	-	4.8	-	mJ		
Turn-off switching loss	E _{off}		-	7.0	-			
Total switching loss	E _{tot}		-	11.8	-			
Turn-on delay time	t _{d(on)}		I _C = 100 A, V _{CC} = 720 V, V _{GE} = 15 V, R _g = 2.2 Ω, L = 500 μH, T _J = 125 °C	-	274	-	ns	
Rise time	t _r			-	67	-		
Turn-off delay time	t _{d(off)}			-	271	-		
Fall time	t _f			I _C = 100 A, V _{CC} = 720 V, V _{GE} = 15 V, R _g = 2.2 Ω, L = 500 μH, T _J = 125 °C	-	177	-	mJ
Turn-on switching loss	E _{on}				-	6.0	-	
Turn-off switching loss	E _{off}				-	10.4	-	
Total switching loss	E _{tot}	I _C = 100 A, V _{CC} = 720 V, V _{GE} = 15 V, R _g = 2.2 Ω, L = 500 μH, T _J = 125 °C			-	16.4	-	ns
Turn-on delay time	t _{d(on)}				-	285	-	
Rise time	t _r				-	75	-	
Turn-off delay time	t _{d(off)}		I _C = 100 A, V _{CC} = 720 V, V _{GE} = 15 V, R _g = 2.2 Ω, L = 500 μH, T _J = 125 °C		-	306	-	mJ
Fall time	t _f				-	244	-	
Reverse bias safe operating area	RBSOA				T _J = 150 °C, I _C = 450 A, R _g = 4.7 Ω, V _{GE} = 15 V to 0 V, V _{CC} = 600 V, V _P = 1200 V, L = 500 μH	Fullsquare		
Diode reverse recovery time	t _{rr}			I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 400 V	-	164	-	ns
Diode peak reverse current	I _{rr}				-	12	-	A
Diode recovery charge	Q _{rr}				-	994	-	nC
Diode reverse recovery time	t _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 400 V, T _J = 125 °C		-	230	-	ns	
Diode peak reverse current	I _{rr}			-	16.5	-	A	
Diode recovery charge	Q _{rr}			-	1864	-	nC	
Short circuit safe operating area	SCSOA	T _J = 150 °C, R _g = 22 Ω, V _{GE} = 15 V to 0 V, V _{CC} = 900 V, V _P = 1200 V	10			μs		



THERMAL AND MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range	T_J, T_{Stg}		-40	-	150	°C
Junction to case	IGBT		-	-	0.115	°C/W
	Diode		-	-	0.57	
Case to heatsink	R_{thCS}	Flat, greased surface	-	0.05	-	
Weight			-	30	-	g
Mounting torque		Torque to terminal	-	-	1.1 (9.7)	Nm (lbf.in)
		Torque to heatsink	-	-	1.3 (11.5)	Nm (lbf.in)
Case style		SOT-227				

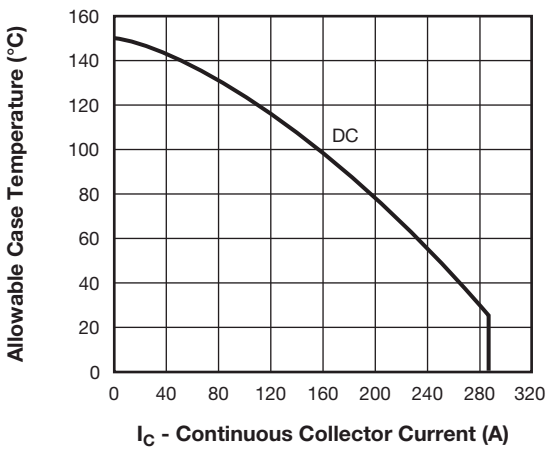


Fig. 1 - Maximum DC IGBT Collector Current vs. Case Temperature

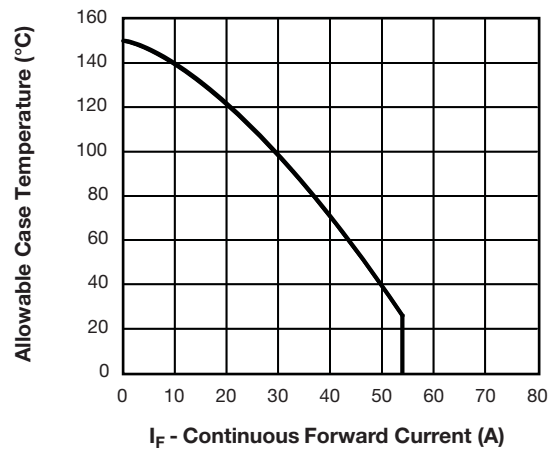


Fig. 3 - Maximum Allowable Forward Current vs. Case Temperature Diode Leg

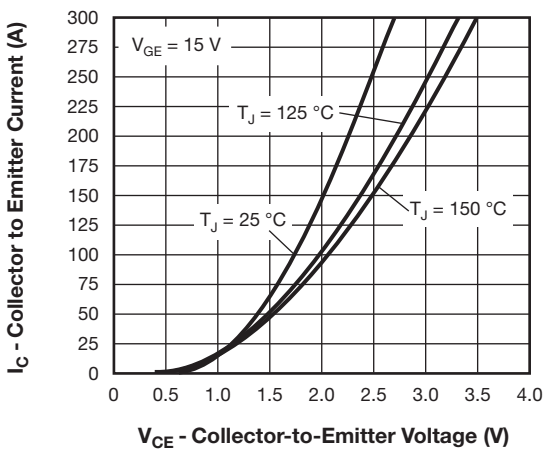


Fig. 2 - Typical Collector to Emitter Current Output Characteristics of IGBT

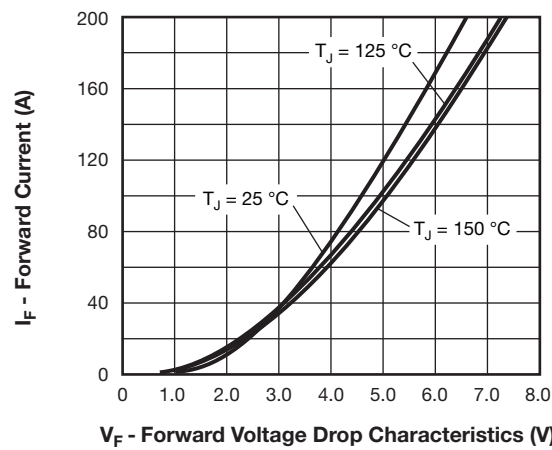


Fig. 4 - Typical Diode Forward Voltage Drop Characteristics

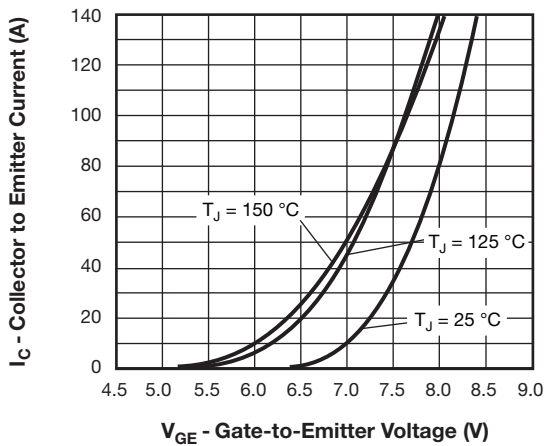


Fig. 5 - Typical IGBT Transfer Characteristics

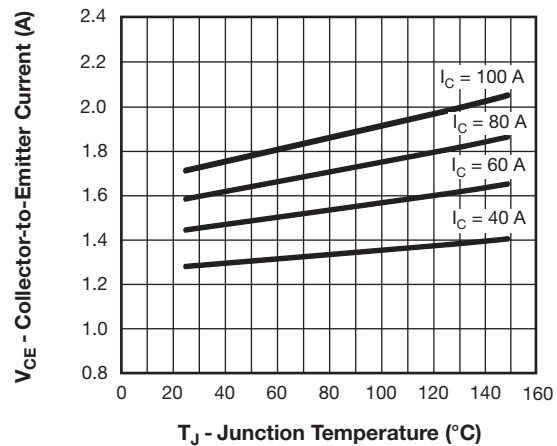


Fig. 8 - Typical IGBT Collector to Emitter Voltage vs. Junction Temperature, $V_{GE} = 15\text{ V}$

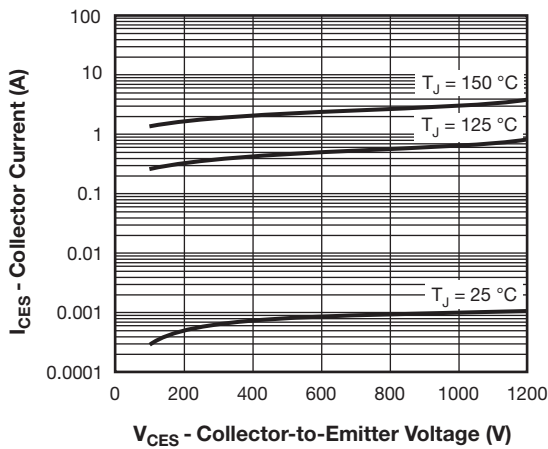


Fig. 6 - Typical IGBT Zero Gate Voltage Collector Current

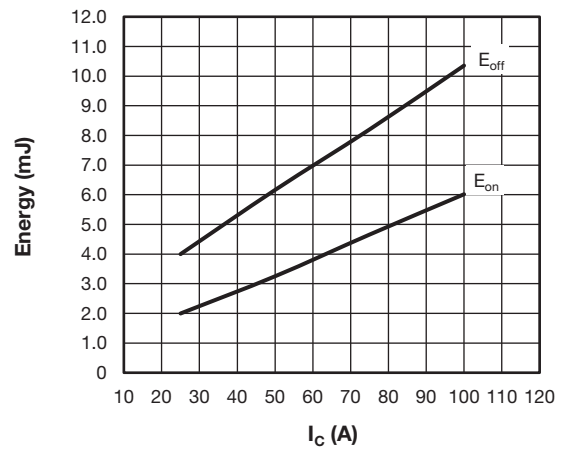


Fig. 9 - Typical IGBT Energy Losses vs. I_C
 $T_J = 125\text{ }^\circ\text{C}$, $L = 500\text{ }\mu\text{H}$, $V_{CC} = 720\text{ V}$, $R_g = 2.2\text{ }\Omega$, $V_{GE} = 15\text{ V}$
 Diode used: HFA16PB120

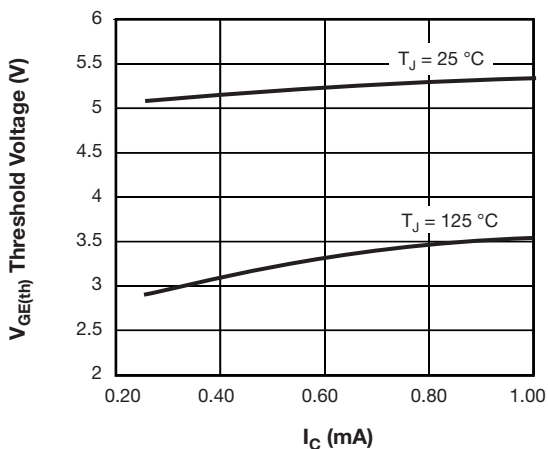


Fig. 7 - Typical IGBT Threshold Voltage

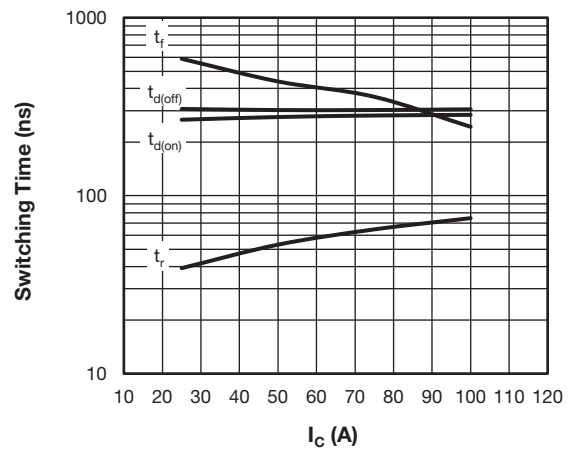


Fig. 10 - Typical IGBT Switching Time vs. I_C
 $T_J = 125\text{ }^\circ\text{C}$, $L = 500\text{ }\mu\text{H}$, $V_{CC} = 720\text{ V}$, $R_g = 2.2\text{ }\Omega$, $V_{GE} = 15\text{ V}$
 Diode used: HFA16PB120

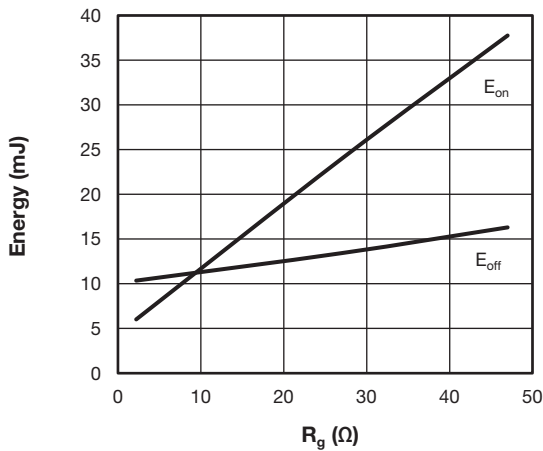


Fig. 11 - Typical IGBT Energy Losses vs. R_g
 $T_J = 125\text{ }^\circ\text{C}$, $I_C = 100\text{ A}$, $L = 500\text{ }\mu\text{H}$, $V_{CC} = 720\text{ V}$, $V_{GE} = 15\text{ V}$
 Diode used: HFA16PB120

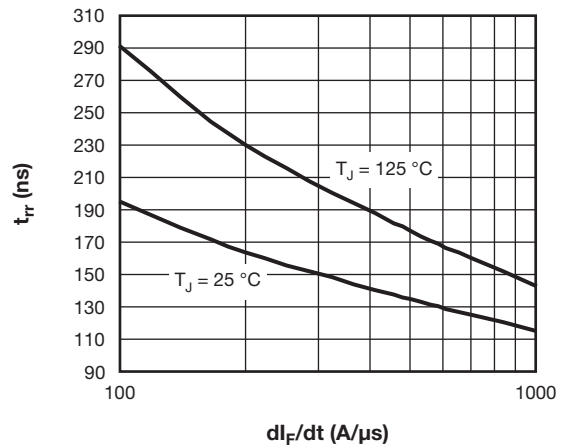


Fig. 13 - Typical Reverse Recovery Time vs. dI_F/dt , of Diode,
 at $I_F = 50\text{ A}$, $V_R = 400\text{ V}$

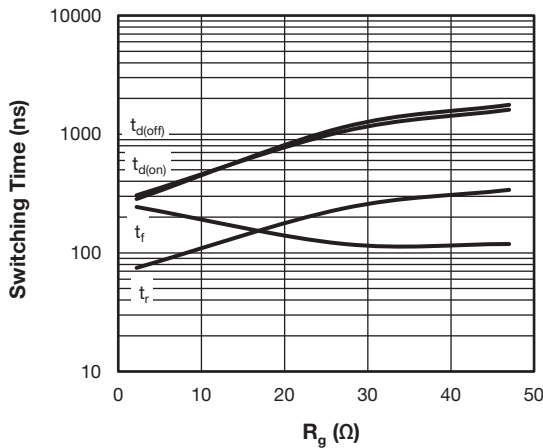


Fig. 12 - Typical IGBT Switching Time vs. R_g
 $T_J = 125\text{ }^\circ\text{C}$, $L = 500\text{ }\mu\text{H}$, $V_{CC} = 720\text{ V}$, $I_C = 100\text{ A}$, $V_{GE} = 15\text{ V}$
 Diode used: HFA16PB120

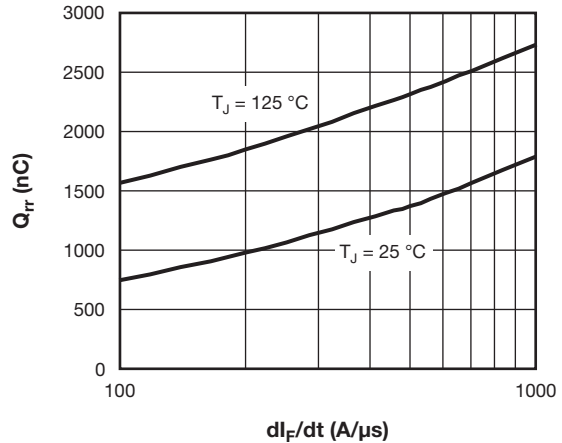


Fig. 14 - Typical Stored Charge vs. dI_F/dt of Diode,
 at $I_F = 50\text{ A}$, $V_R = 400\text{ V}$

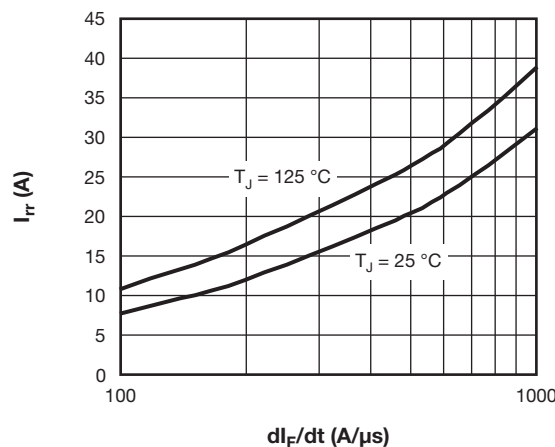


Fig. 15 - Typical Reverse Recovery Current vs. dI_F/dt , of Diode,
 at $I_F = 50\text{ A}$, $V_R = 400\text{ V}$

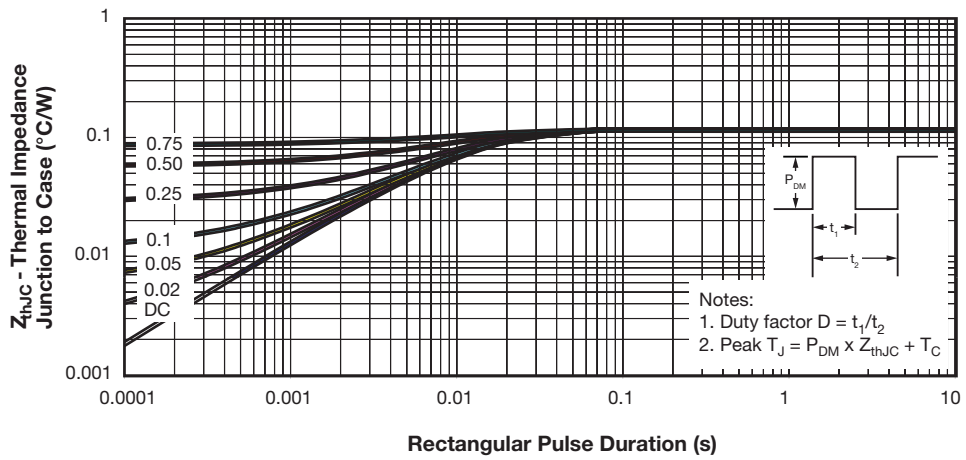


Fig. 16 - Maximum Thermal Impedance Z_{thJC} Characteristics (IGBT)

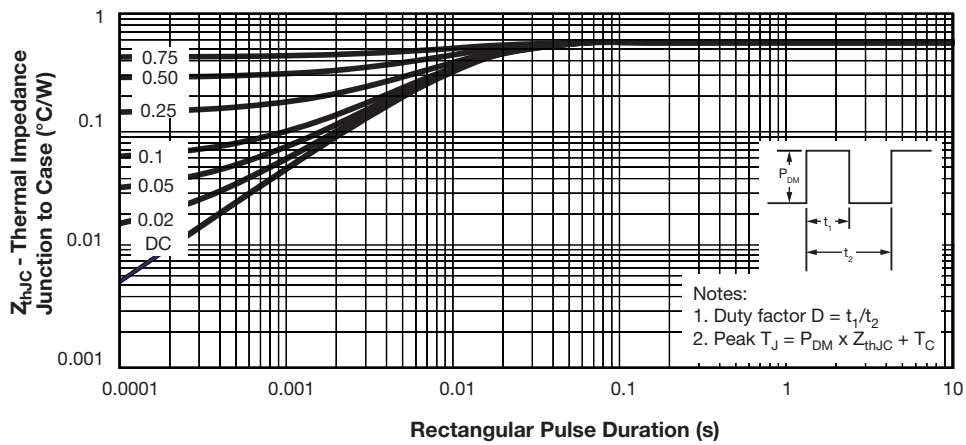


Fig. 17 - Maximum Thermal Impedance Z_{thJC} Characteristics (Diode)

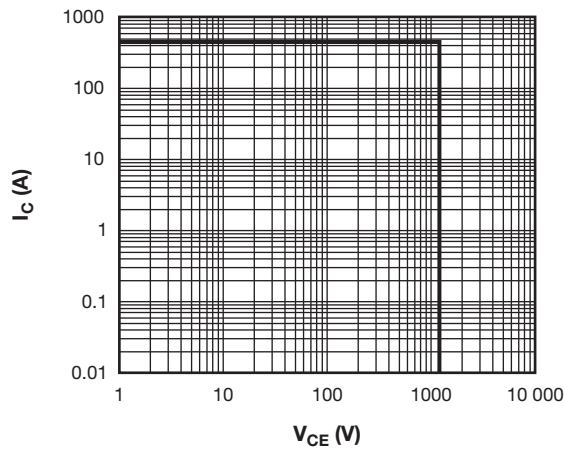


Fig. 18 - IGBT Reverse Bias SOA, $T_J = 150\text{ }^\circ\text{C}$, $V_{GE} = 15\text{ V}$

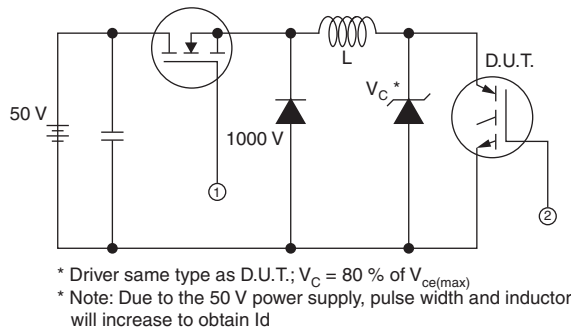


Fig. 19 - Clamped Inductive Load Test Circuit

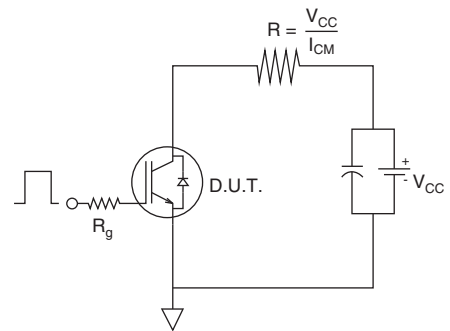


Fig. 19b - Pulsed Collector Current Test Circuit

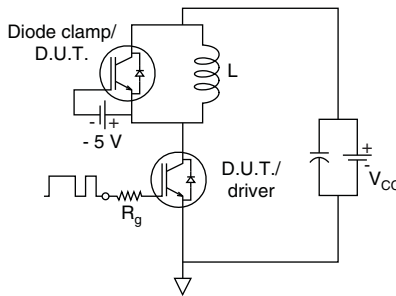


Fig. 20a - Switching Loss Test Circuit

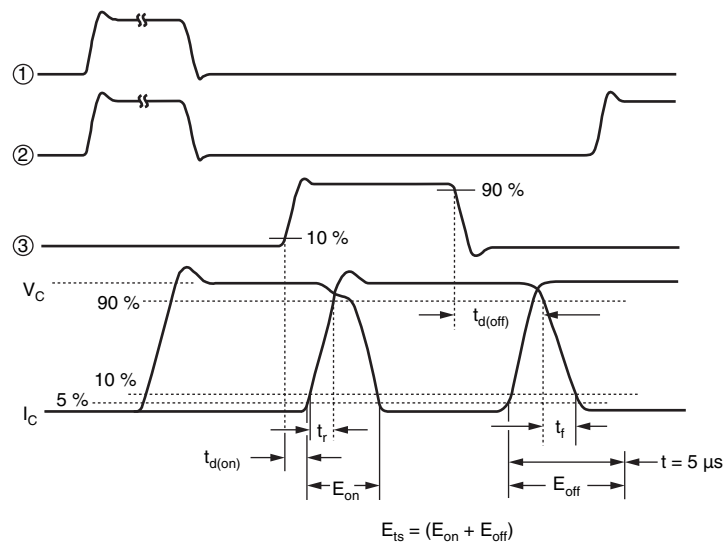
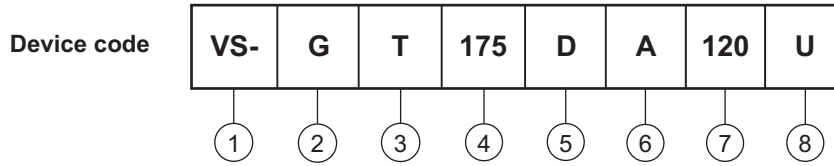


Fig. 20b - Switching Loss Waveforms Test Circuit



ORDERING INFORMATION TABLE



- 1** - Vishay Semiconductors product
- 2** - Insulated Gate Bipolar Transistor (IGBT)
- 3** - Trench IGBT technology
- 4** - Current rating (175 = 175 A)
- 5** - Circuit configuration (D = Single switch with antiparallel diode)
- 6** - Package indicator (A = SOT-227)
- 7** - Voltage rating (120 = 1200 V)
- 8** - Speed/type (U = Ultrafast)

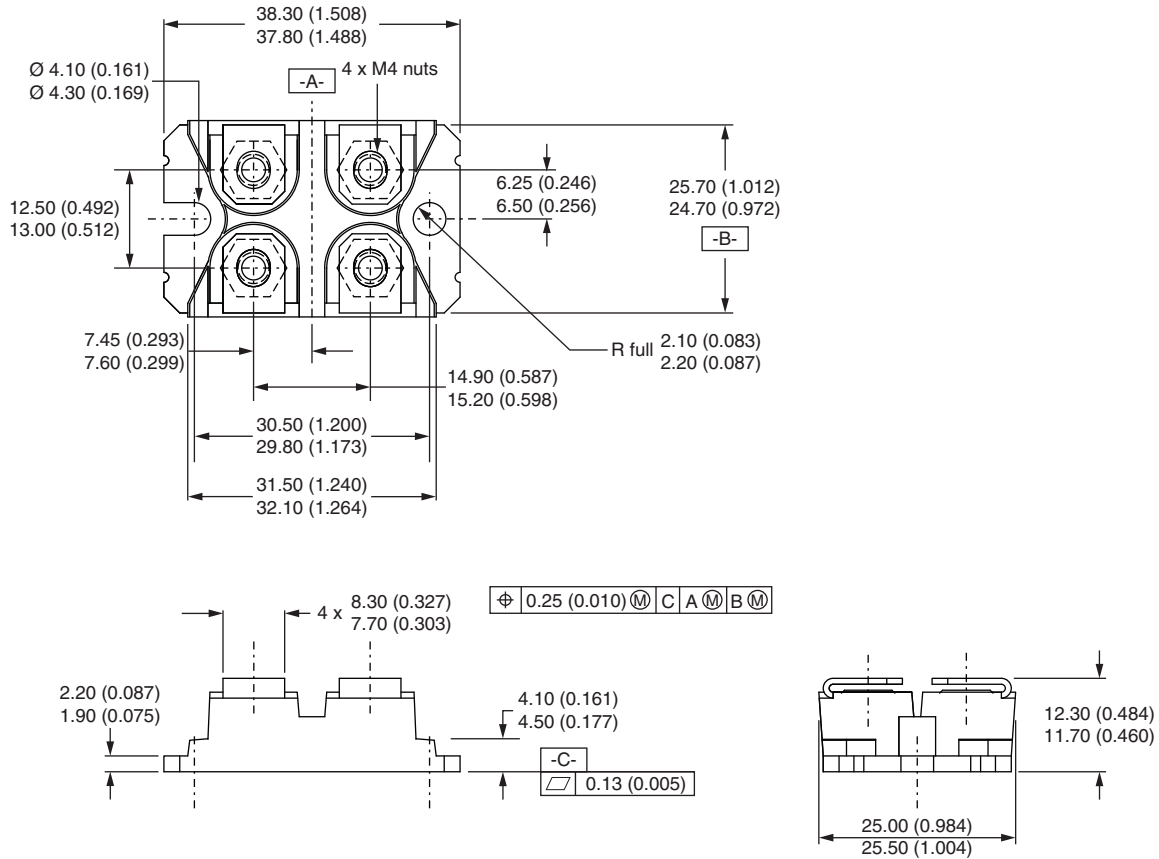
CIRCUIT CONFIGURATION		
CIRCUIT	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Single switch diode	D	

LINKS TO RELATED DOCUMENTS	
Dimensions	www.vishay.com/doc?95423
Packaging information	www.vishay.com/doc?95425



SOT-227 Generation II

DIMENSIONS in millimeters (inches)



Note

- Controlling dimension: millimeter



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