

# LPC407x/8x

## ARM Cortex-M4

Up to 120 MHz  
with NVIC, WIC, MPU,  
Debug Trace (ETM)

## Floating Point Unit

### CORE

GPDMA

GPIO  
Up to 165

USB PLL

CRC Engine

## Power Management Unit

PMU, power modes, BOD, single  
Vdd power supply, POR

## Clock Generation Unit

12 MHz, 1% IRC OSC, 1-24 MHz  
System OSC, System (CPU) PLL

### SYSTEM

32-bit Timers (4)

WWDT

Real-time Clock  
Battery pin, event recorder

Systick Timer

### TIMERS

## Flash

Up to 512 kB

## SRAM

Up to 96 kB

## EEPROM

Up to 4 kB

## ROM

with ROM drivers

### MEMORY

Multilayer Bus Matrix

Ext Mem Ctrl

SPIFI

Graphic LCD

FS USB 2.0

Ethernet MAC

CAN 2.0B (2)

SD/MMC

I<sup>2</sup>S (2)

Motor Ctrl PWM, QEI

SSP/SPI (3), I<sup>2</sup>C (3), UART (5)

### INTERFACES

ADC

8-channel, 12-bit

DAC

10-bit

Comparator (2)

### ANALOG