



DMNH4026SSD

40V 175°C DUAL N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV _{DSS}	Rds(on) max	Ι _D T _A = +25°C		
40V	24mΩ @V _{GS} = 10V	7.5A		
40 V	$32m\Omega @V_{GS} = 4.5V$	6.5A		

Description

This new generation MOSFET is designed to minimize the on-state resistance (R_{DS(ON)}) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- Motor Control
- Backlighting
- **Power Management Functions**
- **DC-DC Converters**

Features

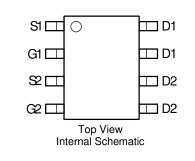
- Rated to +175°C Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching Ensures More Reliable and Robust End Application
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- An Automotive-Compliant Part is Available Under Separate Datasheet (DMNH4026SSDQ)

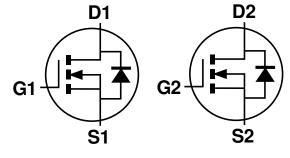
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram Below
- Terminals: Finish Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 (C3)
- Weight: 0.074 grams (Approximate)



Top View





Equivalent Circuit

Ordering Information (Note 4)

Part Number	Case	Packaging
DMNH4026SSD-13	SO-8	2,500/Tape & Reel

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green"

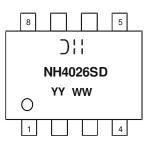
and Lead-free.

Notes:

3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



);; = Manufacturer's Marking NH4026SD = Product Type Marking Code YYWW = Date Code Marking YY = Year (ex: 16 = 2016) WW = Week (01 to 53)



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	40	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) $V_{GS} = 10V$	Steady State	$T_A = +25^{\circ}C$ $T_A = +100^{\circ}C$	ID	7.5 5.3	А
Maximum Continuous Body Diode Forward Current (Note 6)			Is	2.5	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I _{DM}	60	A
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	18	A
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	18	mJ

Thermal Characteristics

Characteristic		Symbol	Value	Unit	
Total Power Dissipation (Note 5)	T _A = +25°C	PD	1.5	W	
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Р	101	°C/W	
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	R _{θJA}	59	C/W	
Total Power Dissipation (Note 6)	T _A = +25°C	PD	2.0	W	
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	Devi	74	°C/W	
Thermal Resistance, Junction to Ambient (Note 6)	t<10s	R _{θJA}	43		
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	10.5			
Operating and Storage Temperature Range		TJ, TSTG	-55 to +175	°C	

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

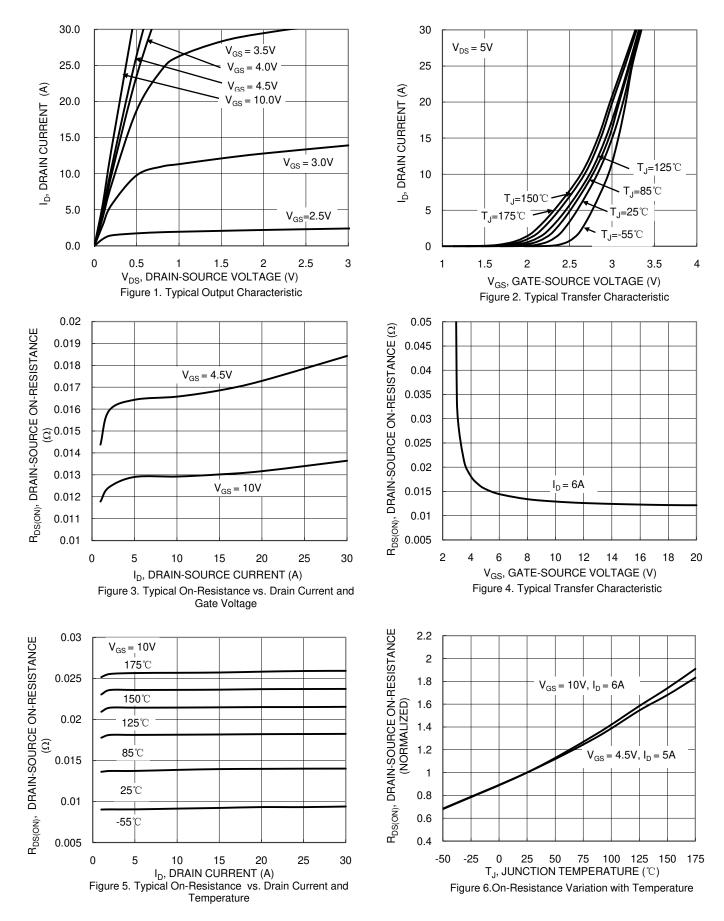
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)	1 - 1					
Drain-Source Breakdown Voltage	BV _{DSS}	40	_	_	V	$V_{GS} = 0V, I_D = 250 \mu A$
Zero Gate Voltage Drain Current	I _{DSS}		_	1	μA	$V_{DS} = 40V, V_{GS} = 0V$
Gate-Source Leakage	I _{GSS}		_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 8)	•					÷
Gate Threshold Voltage	V _{GS(TH)}	1	—	3	V	$V_{DS} = V_{GS}, I_D = 250 \mu A$
Static Drain-Source On-Resistance	Passau	_	15	24	mΩ	$V_{GS} = 10V, I_D = 6A$
Static Drain-Source On-Resistance	R _{DS(ON)}		20	32	11122	$V_{GS} = 4.5V, I_D = 5A$
Diode Forward Voltage	V _{SD}		0.7	1.0	V	$V_{GS} = 0V, I_{S} = 1.0A$
DYNAMIC CHARACTERISTICS (Note 9)						·
Input Capacitance	C _{iss}	_	1060	—		$\label{eq:VDS} \begin{split} V_{DS} &= 20V, V_{GS} = 0V, \\ f &= 1.0 MHz \end{split}$
Output Capacitance	Coss	_	84	—	pF	
Reverse Transfer Capacitance	Crss	_	58	_		
Gate Resistance	Rg		1.6	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$
Total Gate Charge (V _{GS} = 4.5V)	Qg		8.8	—		
Total Gate Charge (V _{GS} = 10V)	Qg		19.1	—	nC	$V_{DS}=20V,\ I_{D}=8A$
Gate-Source Charge	Q _{gs}	_	3.0	_	no	
Gate-Drain Charge	Q _{gd}		2.5	_		
Turn-On Delay Time	t _{D(ON)}		5.3	_		
Turn-On Rise Time	t _R		7.1	_		$V_{DD} = 25V, R_L = 2.5\Omega$ $V_{GS} = 10V, R_g = 3\Omega$
Turn-Off Delay Time	t _{D(OFF)}		15.1	_	ns	
Turn-Off Fall Time	t _F	_	4.8	_		
Body Diode Reverse Recovery Time	t _{RR}		10.5	_	ns	I _F = 8A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q _{RR}	_	4.15	_	nC	I _F = 8A, di/dt = 100A/µs

Notes:

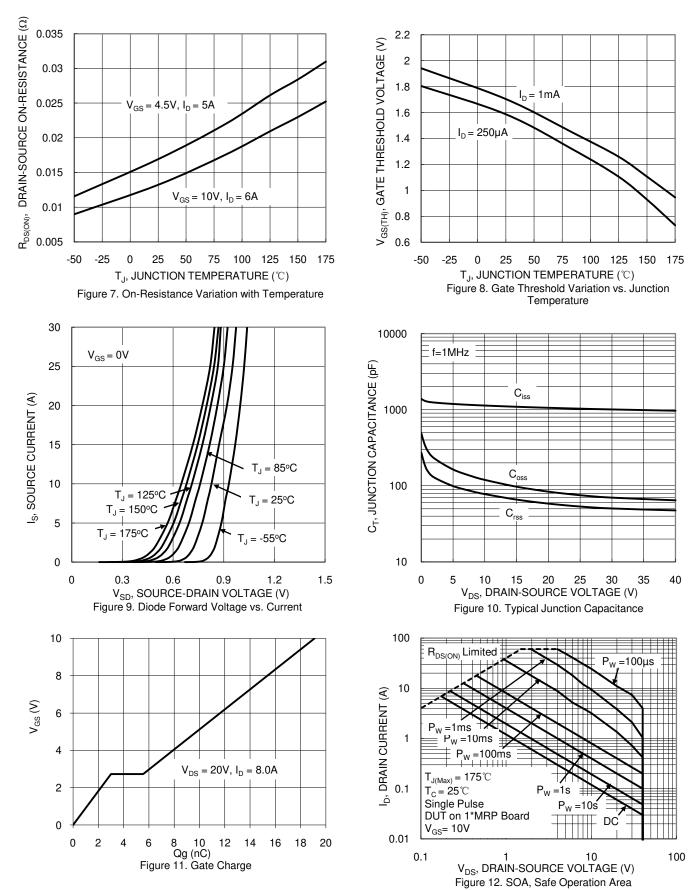
5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate
7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep T_J = +25°C.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.



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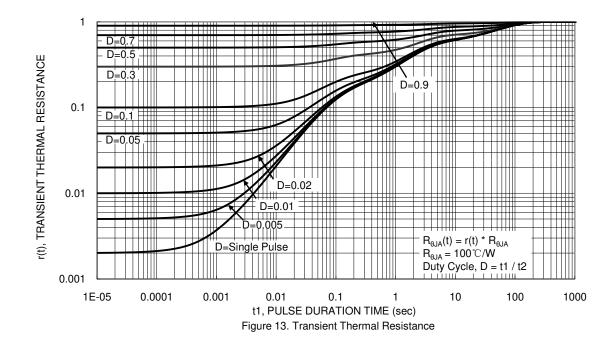






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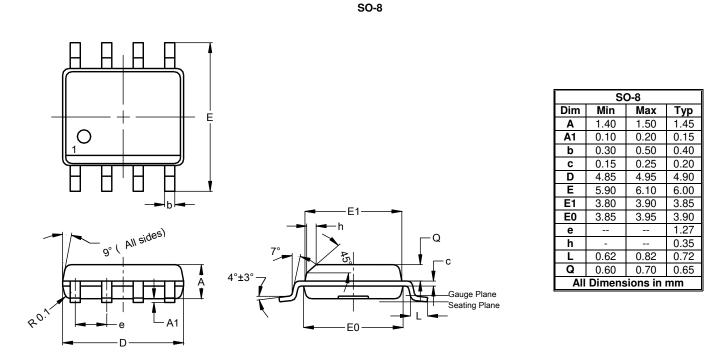






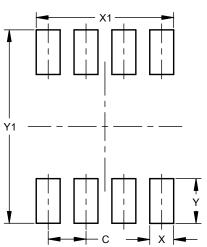
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-8

Dimensions	Value (in mm)
С	1.27
Х	0.802
X1	4.612
Y	1.505
Y1	6.50



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