

## Product Summary

<b>BV<sub>DSS</sub></b>	<b>R<sub>DS(ON)</sub> max</b>	<b>I<sub>D</sub> max</b> <b>T<sub>A</sub> = +25°C</b>
30V	20mΩ @ V <sub>GS</sub> = 10V	10A

## Description

This MOSFET has been designed to minimize the on-state resistance (R<sub>DS(ON)</sub>) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

## Applications

- General Purpose Interfacing Switch
- Power Management Functions
- DC-DC Converters
- Analog Switch

## Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Low Input/Output Leakage
- Low Gate Resistance
- Fast Switching Speed
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **An Automotive-Compliant Part is Available Under Separate Datasheet ([DMG4822SSDQ](#))**

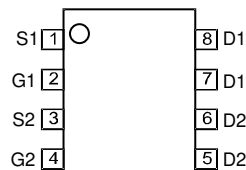
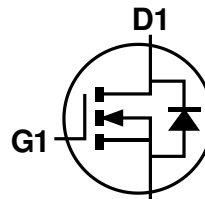
## Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish — NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208④
- Weight: 0.072 grams (Approximate)

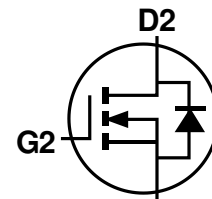
SO-8



Top View


 Top View  
Internal Schematic


N-Channel MOSFET



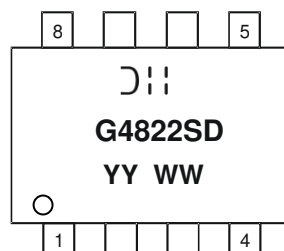
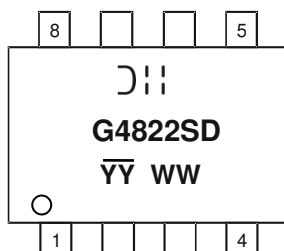
N-Channel MOSFET

## Ordering Information (Note 4)

Part Number	Case	Packaging
DMG4822SSD-13	SO-8	2,500/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
  2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
  3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
  4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

## Marking Information



DII = Manufacturer's Marking  
 G4822SD = Product Type Marking Code  
 YYWW = Date Code Marking  
 YY or YY = Year (ex: 19 = 2019)  
 WW = Week (01 to 53)

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V <sub>DSS</sub>	30	V	
Gate-Source Voltage	V <sub>GSS</sub>	±25	V	
Continuous Drain Current (Note 5) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C	10	A
		T <sub>A</sub> = +85°C	6.6	A
Pulsed Drain Current (Note 6)	I <sub>DM</sub>	60	A	
Avalanche Current (Notes 7 & 8)	I <sub>AR</sub>	1.68	A	
Repetitive Avalanche Energy L = 0.3mH (Notes 7 & 8)	E <sub>AR</sub>	12.8	mJ	

**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P <sub>D</sub>	1.42	W
Thermal Resistance, Junction to Ambient (Note 5)	R <sub>θJA</sub>	88.4	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 9)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	30	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	—	—	1	μA	V <sub>DS</sub> = 30V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±25V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 9)</b>						
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	—	3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	—	13.4	20	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 8.5A
		—	19.5	31		V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 6A
Forward Transfer Admittance	Y <sub>fs</sub>	—	20	—	mS	V <sub>DS</sub> = 5V, I <sub>D</sub> = 8.5A
Diode Forward Voltage	V <sub>SD</sub>	—	0.4	1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = 1A
<b>DYNAMIC CHARACTERISTICS (Note 10)</b>						
Input Capacitance	C <sub>iss</sub>	—	478.9	—	pF	V <sub>DS</sub> = 16V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	96.7	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	61.4	—	pF	
Gate Resistance	R <sub>g</sub>	—	1.1	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Q <sub>g</sub>	—	5	—	nC	V <sub>GS</sub> = 10V, V <sub>DS</sub> = 15V, I <sub>D</sub> = 8.5A
Total Gate Charge (V <sub>GS</sub> = 10V)	Q <sub>g</sub>	—	10.5	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	1.8	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	1.6	—	nC	
Turn-On Delay Time	t <sub>D(ON)</sub>	—	2.9	—	ns	V <sub>DS</sub> = 15V, V <sub>GS</sub> = 10V, R <sub>L</sub> = 1.8Ω, R <sub>G</sub> = 3Ω
Turn-On Rise Time	t <sub>R</sub>	—	7.9	—	ns	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	—	14.6	—	ns	
Turn-Off Fall Time	t <sub>F</sub>	—	3.1	—	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
  - Device mounted on minimum recommended pad layout test board, 10μs pulse duty cycle = 1%.
  - Repetitive rating, pulse width limited by junction temperature.
  - I<sub>AR</sub> and E<sub>AR</sub> ratings are based on low frequency and duty cycles to keep T<sub>J</sub> = +25°C.
  - Short duration pulse test used to minimize self-heating effect.
  - Guaranteed by design. Not subject to product testing.

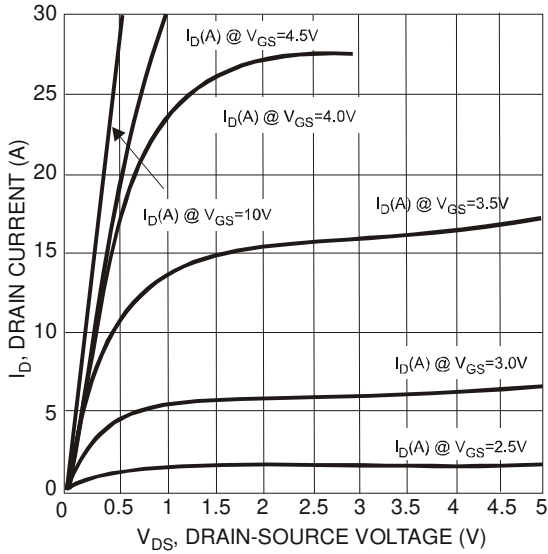


Fig. 1 Typical Output Characteristic

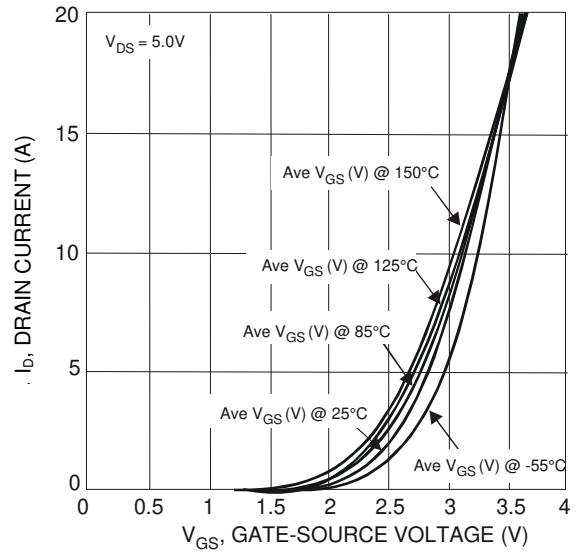


Fig. 2 Typical Transfer Characteristics

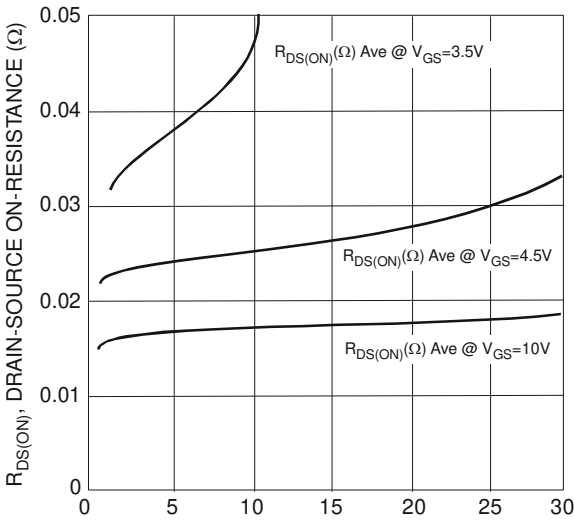


Fig. 3 Typical On-Resistance vs. Drain Current and Gate Voltage

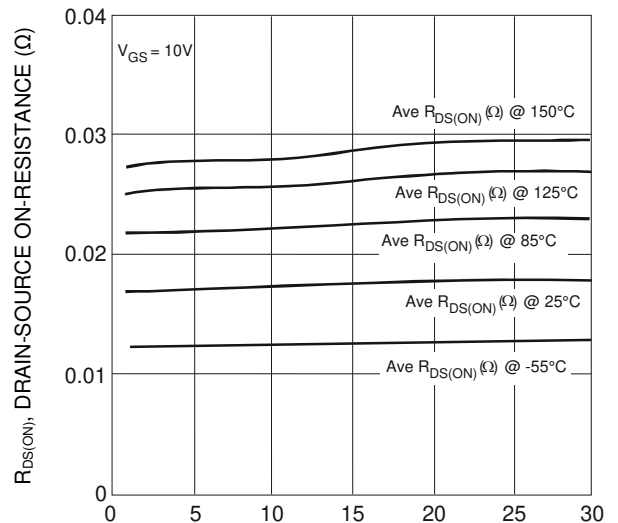


Fig. 4 Typical On-Resistance vs. Drain Current and Temperature

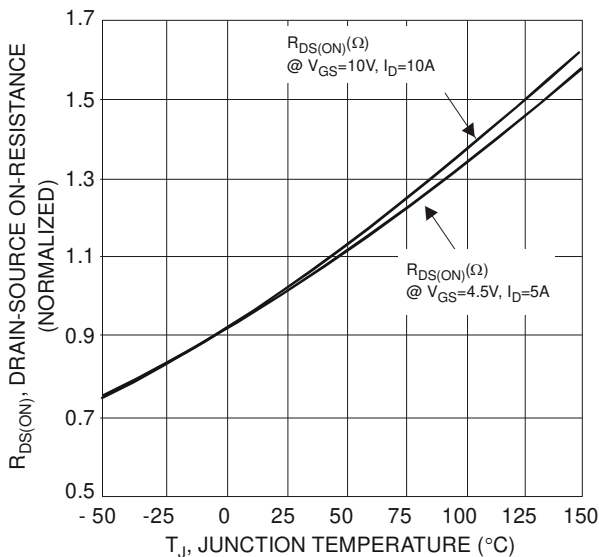


Fig. 5 On-Resistance Variation with Temperature

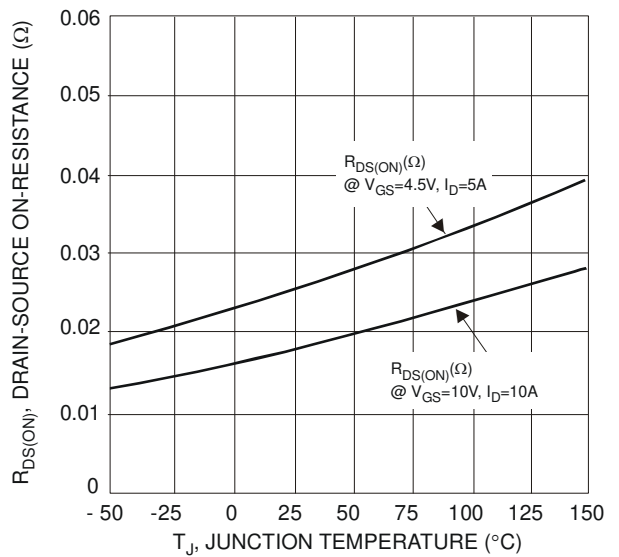


Fig. 6 On-Resistance Variation with Temperature

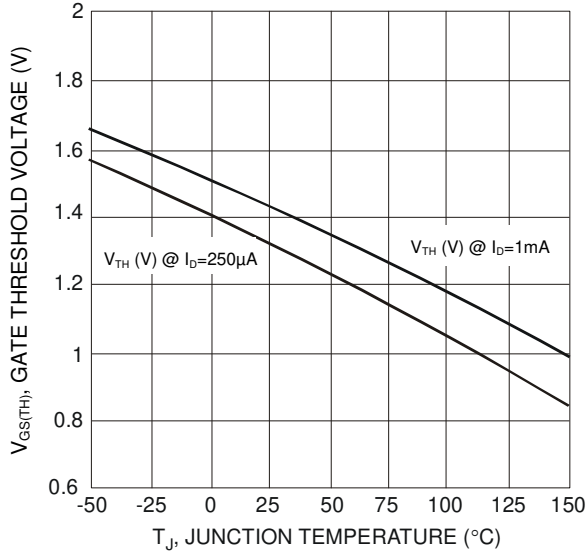


Fig. 7 Gate Threshold Variation vs. Junction Temperature

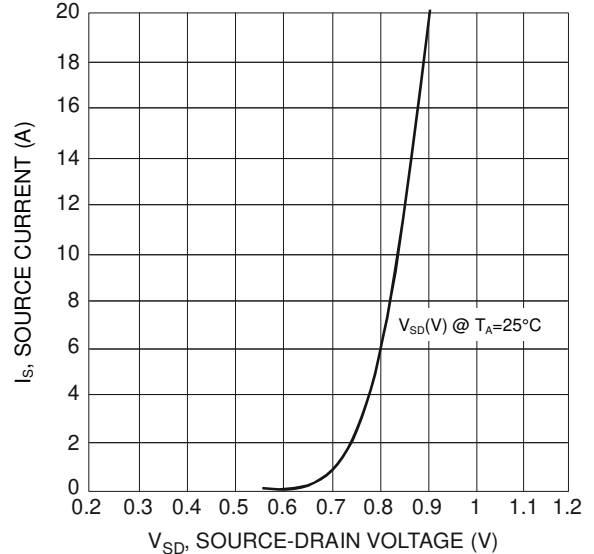


Fig. 8 Diode Forward Voltage vs. Current

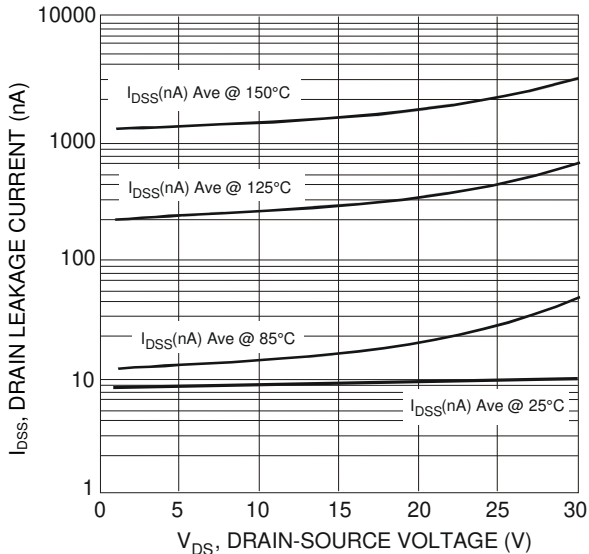


Fig. 9 Typical Drain-Source Leakage Current vs. Voltage

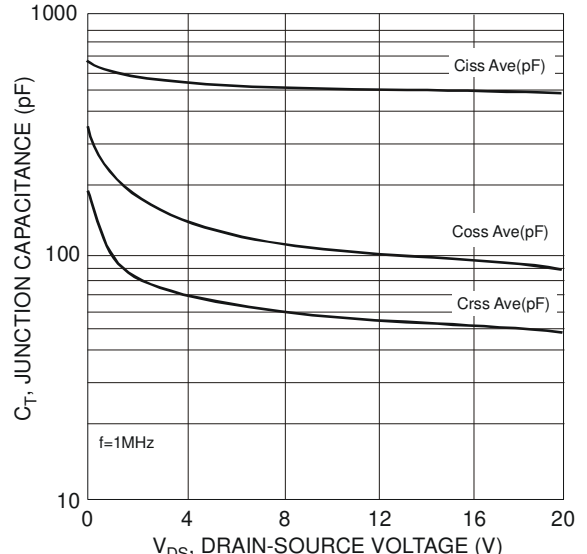


Fig. 10 Typical Junction Capacitance

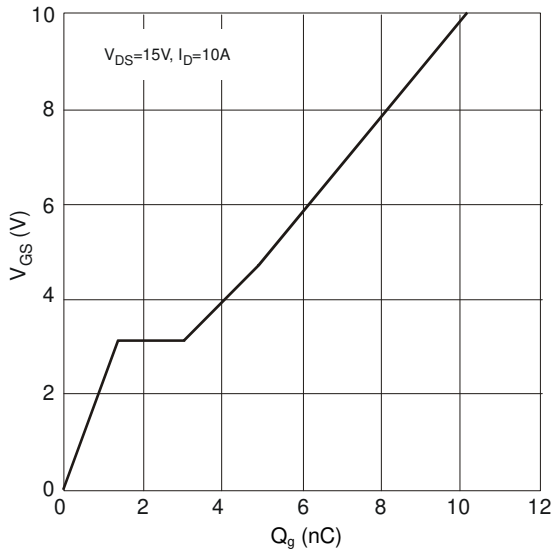


Fig. 11 Gate Charge

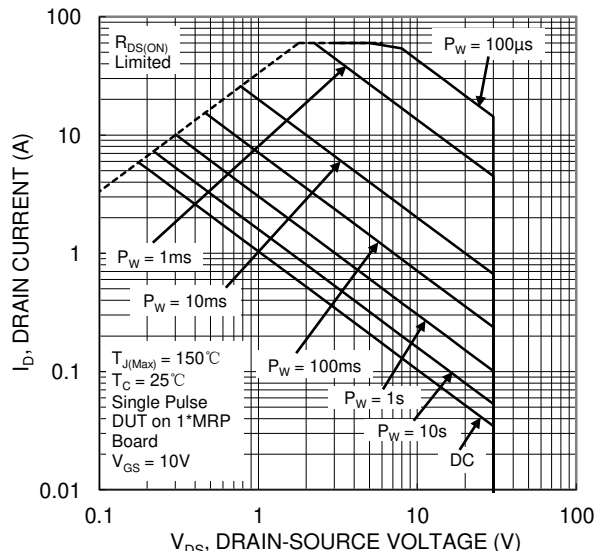


Fig. 12 SOA, Safe Operation Area

NEW PRODUCT

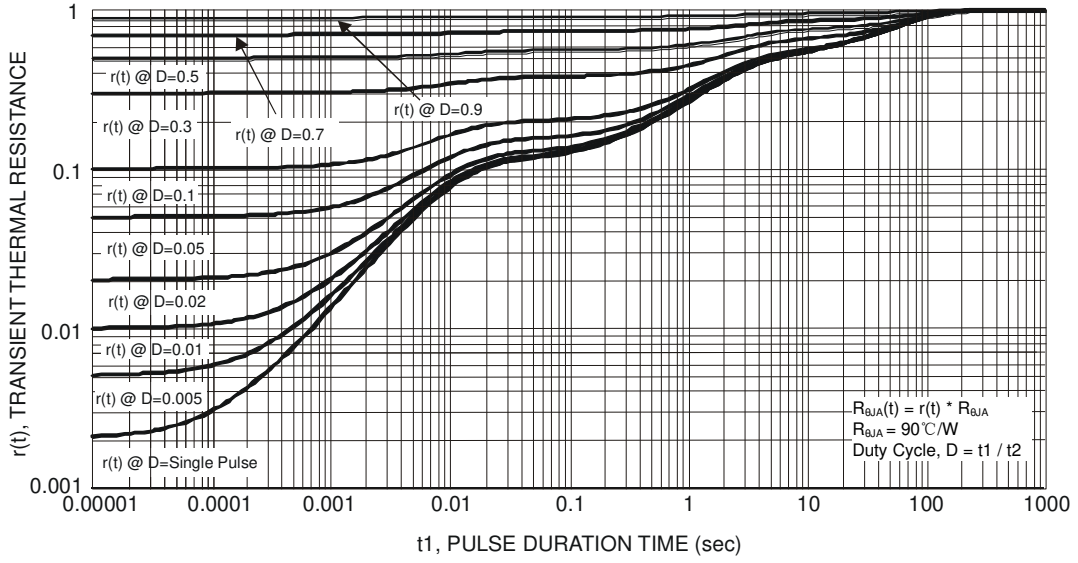
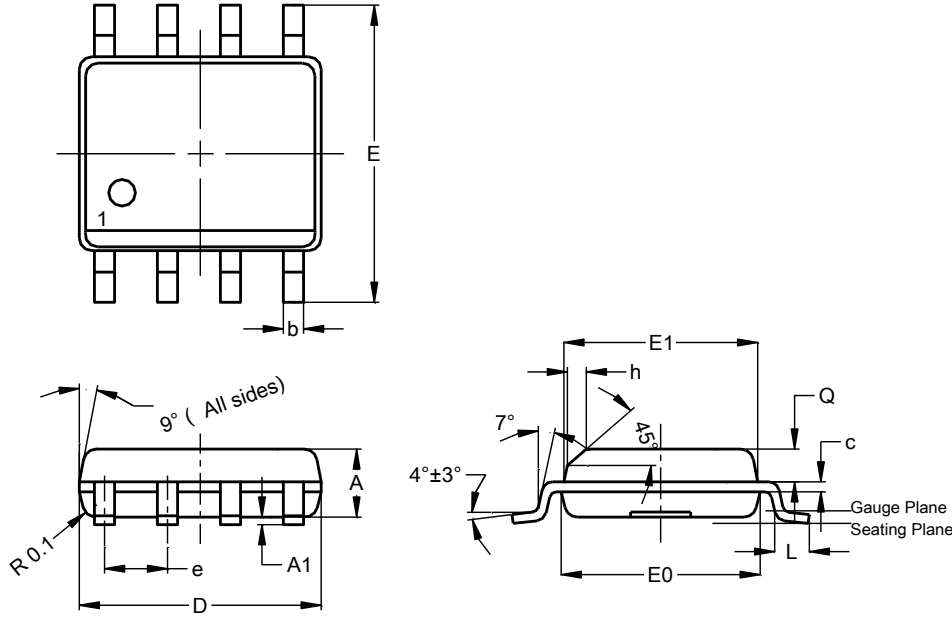


Fig. 13 Transient Thermal Resistance

**Package Outline Dimensions**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SO-8**



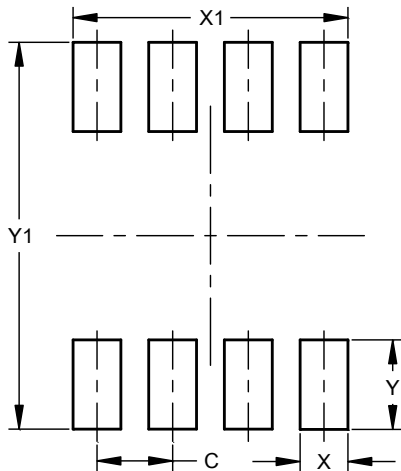
SO-8			
Dim	Min	Max	Typ
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	-	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
<b>All Dimensions in mm</b>			

NEW PRODUCT

**Suggested Pad Layout**

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**SO-8**



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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