2SB1030A

Silicon PNP epitaxial planar type

For low-frequency amplification Complementary to 2SD1423A

Features

- Optimum for high-density mounting
- Allowing supply with the radial taping

Absolute Maximum Ratings $T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V _{CBO}	-60	V
Collector-emitter voltage (Base open)	V _{CEO}	-50	V
Emitter-base voltage (Collector open)	V _{EBO}	-7	V
Collector current	I _C	- 0.5	A
Peak collector current	I _{CP}	-1	Α
Collector power dissipation	P _C	300	mW
Junction temperature	T _j	150	°C
Storage temperature	T _{stg}	-55 to +150	°C



- 2. Collector
- 3. Base

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = -10 \mu {\rm A}, I_{\rm E} = 0$	-60			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = -2 \text{ mA}, I_{\rm B} = 0$	-50	0		V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = -10 \mu {\rm A}, I_{\rm C} = 0$	-70			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = -20 \text{ V}, I_E = 0$	2°		-0.1	μΑ
Collector-Emitter cutoff current (Base open)	I _{CEO}	$V_{\rm CE} = -20$ V, $I_{\rm B} = 0$			-1	μΑ
Forward current transfer ratio	$h_{\rm FE1}$ *	$V_{CE} = -10 \text{ V}, I_C = -150 \text{ mA}$	85		340	
	h _{FE2}	$V_{CE} = -10 \text{ V}, I_C = -500 \text{ A}$	40			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = -300 \text{ mA}, I_{\rm B} = -30 \text{ mA}$		- 0.35	- 0.60	V
Transition frequency	\mathbf{f}_{T}	$V_{CB} = -10$ V, $I_E = 50$ mA, f = 200 MHz		120		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		3.5	15.0	pF

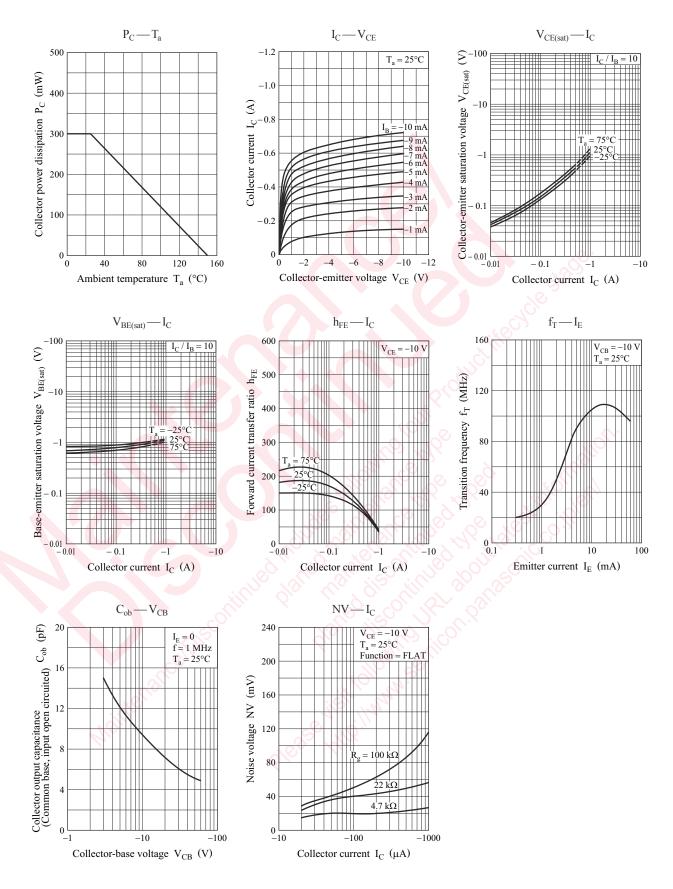
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

2. *: Rank classification

Rank	Q	R	S
$h_{\rm FE1}$	85 to 170	120 to 240	170 to 340

2SB1030A

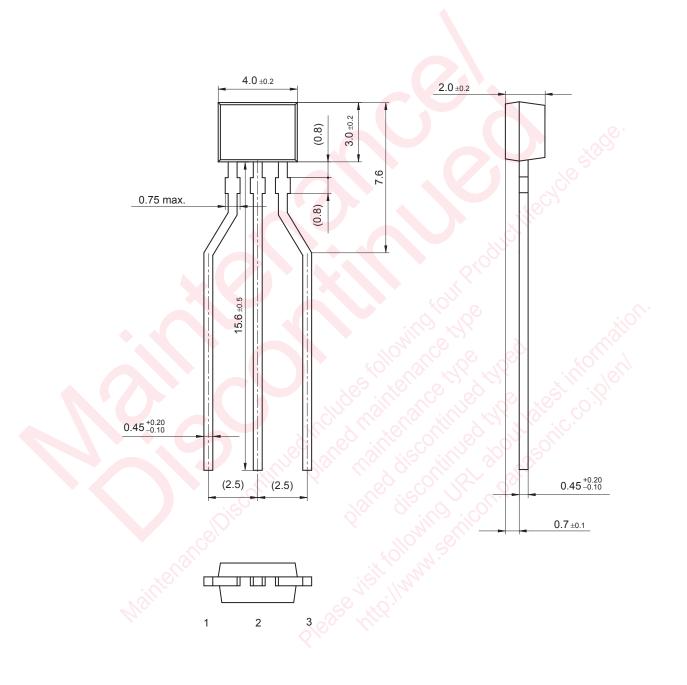
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NS-B1

Unit: mm



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