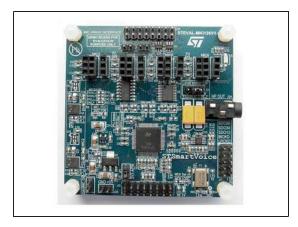


STEVAL-MKI126V1

STSmartVoice demonstration board based on MP45DT02

Data brief



Features

- 2 on-board MEMS MP45DT02 microphones
- Capable of driving up to 6 digital MEMS microphones
- 3 independent I2S outputs
- Up to 10 independent 32-bit user programmable biquads per channel
- The board is fully compatible with the:
 - STEVAL-MKI129V1/V2/V3
 - STEVAL-MKI131V1/V2/V3
 - STEVAL-MKI116V1
 - STEVAL-MKI117V1/V2
 - STEVAL-CCA035V1
- Controllable via APWorkbench software suite
- RoHS compliant

Description

The STEVAL-MKI126V1 demonstration board allows the connection of up to six microphones using the sockets provided, or through the connection of a dedicated six-microphone array.

The digital interface of the board allows the interfacing of the MIC output to the most widely used audio receivers and digital amplifiers, or permits evaluation of the MIC with generic audio measurement equipment. The filtered PWM signals allow the connection of a headset to listen to audio received through the microphones.

The fully digital path ensures a high level of processing with sound preconditioning, filtering and voice enhancement.

The main function of the STEVAL-MKI126V1 is to convert the PDM signals from the microphones into more common I²S and PWM signals.

The I²S signal is routed both on a general and interface connector. The appropriately filtered PWM signals provide an analog interface.

The STEVAL-MKI126V1 is equipped with two MP45DT02 microphones and an STA321MPL scalable digital microphone processor.

Block diagram STEVAL-MKI126V1

1 Block diagram

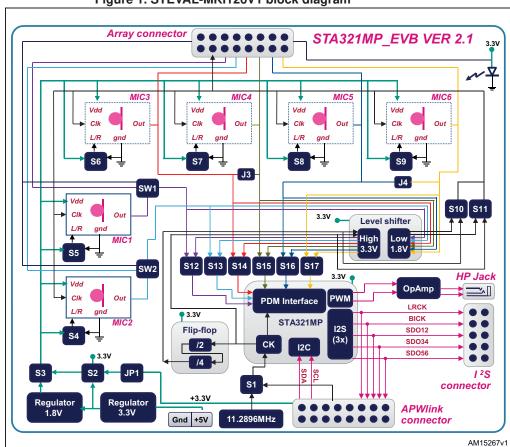


Figure 1. STEVAL-MKI126V1 block diagram

2 Schematic diagrams

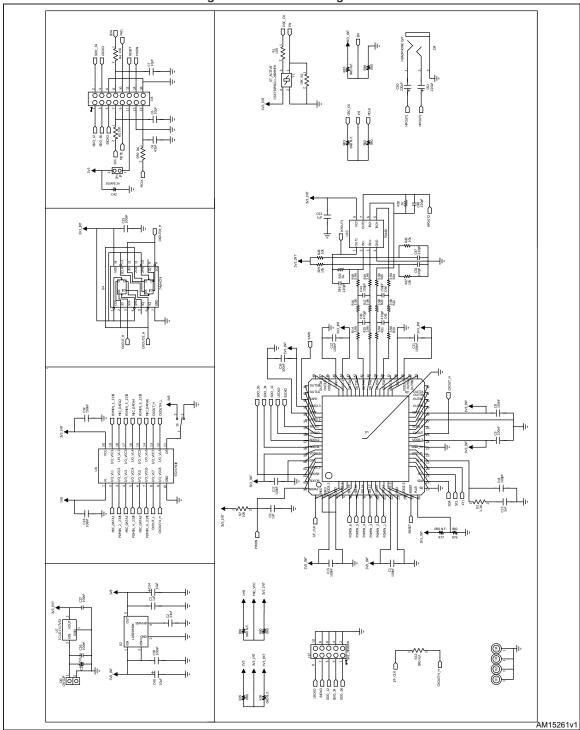


Figure 2. Schematic diagram 1/5

Schematic diagrams STEVAL-MKI126V1

MIC_DATA2_BOARD MIC_CK C22 100nF R9 100R 15pF C21 100F 30 15pF 40 MP34DT01 VDD 꿈 DOUT CK DATA S CLK MP45DT01 MP34DB01 U11 U13 GND 41. 5) Dan GND GND GND DATA \preceq -||-838 080 MIC_DATA1_BOARD ▲ MIC_VCC 20 100nF C10 100F R8 100R C28 T 15pF MP34DT01 VDD 1 K 500 9 상 상 MP45DT01 MP34DB01 U9 DATA(D) **U12** CND VDD DATA 4 C_K R37 ORO N.F. R36 AW ORO AM15262v1

Figure 3. Schematic diagram 2/5



0R0 - **W** R44 0R0 -₩-R51 ORO N.F. MIC_VCC 925 Ô Ó 0 MIC_DATA4 — C38 MIC_DATA6 15pF <u>ا ا</u> ORO N.F. WA R49 MIC_VCC 125 00N3 927) 131 0 MIC_DATA3 C36 15pF A A A |||-MIC_DATA5 AM15264v1

Figure 4. Schematic diagram 3/5

Schematic diagrams STEVAL-MKI126V1

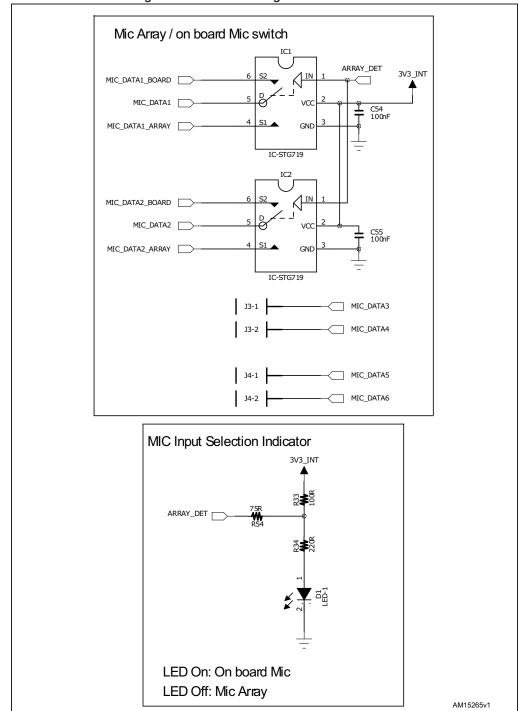
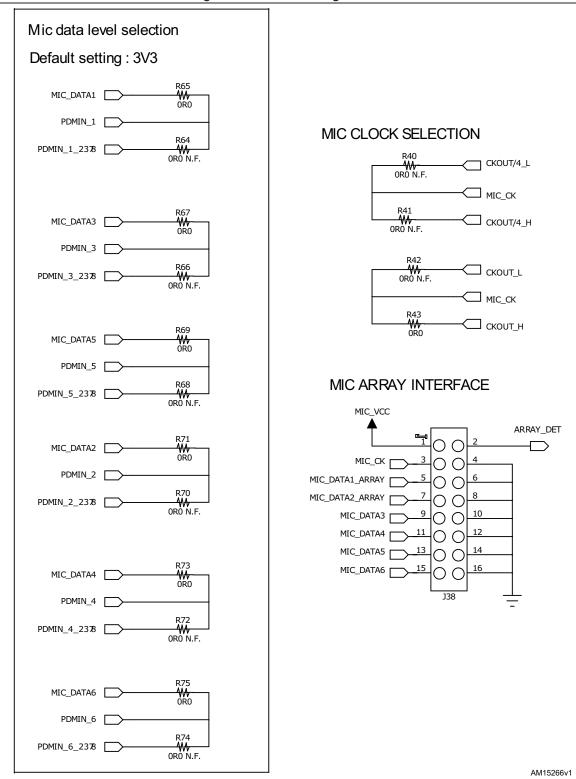


Figure 5. Schematic diagram 4/5



Figure 6. Schematic diagram 5/5



Revision history STEVAL-MKI126V1

3 Revision history

Table 1. Document revision history

Date	Revision	Changes
18-Sep-2012	1	Initial release.
24-Jun-2013	2	Title has been changed.

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