

VOLTAGE -CONTROLLED CRYSTAL OSCILLATOR (VCXO) **OUTPUT : CMOS**



Product Number Q3614CE00xxxx00

VG-4231CE

 Frequency range 	:	3 MHz to 50 MHz * * 50MHz is not included in Output frequency range.
 Supply voltage 	:	3.3 V (PSCM / CSCM) 2.8 V (PSBM / CSBM)
_		1.8 V (PQEM / CQEM)
 Frequency control range 		±140 × 10 ⁻⁶ (*SCM /*SBM) ±120 × 10 ⁻⁶ (*QEM)
 Low current consumption External dimensions 		1.0 mA Typ. (27 MHz , 3.3 V) 3.2 × 2.5 × 1.05 mm

Specifications (characteristics)

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Item	Symbol		Specifications		Conditions / Remarks			
nem	Symbol	PSCM / CSCM	PSCM / CSCM PSBM / CSBM PQEM / CQEM					
Output frequency range	Fo			Please contact us about available frequencies.				
Output hequency range	10			24 10112 10 30 101112	50MHz is not included in Output frequency range.			
Supply voltage	Vcc	3.3 V ± 0.3 V	2.8 V ± 0.2 V	1.8 V ± 0.2 V				
Storage temperature	T_stg		-40 °C to +125 °C		Storage as single product.			
Operating temperature	T_use	As per below table						
Frequency tolerance	f_tol	As per below table			C : Vc=1.65 V / B : Vc=1.40 V / E : Vc=0.90 V			
Current consumption	lcc	7 mA Max.	6.8 mA Max.	1.2 mA Max.	No load condition			
Frequency control range	f_cont	S:± 140 × 10 ⁻⁶ Min. Q:± 120 × 10 ⁻⁶ Min.			$Vc = 1/2 Vcc \pm 1/2 Vcc$			
Modulation characteristics	BW	15 kHz Min.			± 3 dB (at 1 kHz)			
Input resistance	Rin	M : 5 MΩ Min.			DC level			
Frequency change polarity		Positive polarity			Vc=0 V to Vcc			
Symmetry	SYM	40 % to 60 %			CMOS load:50 % Vcc level			
Output valtage	Vон	Vcc-0.4 V Min.			Іон=-3.0 mA			
Output voltage	Vol	0.4 V Max.			IoL= 3.0 mA			
Output load condition (CMOS)	L_CMOS	15 pF Max.			CMOS load			
Rise time and Fall time	tr / tf	4 ns Max. 6 ns Max. CMOS load: 20 % Vcc to 80 % V			CMOS load: 20 % Vcc to 80 % Vcc level			
Start-up time	t_str	5 ms Max.			Time at 90 % Vcc to be 0 s			
Frequency aging	f_age	± 5 × 10⁻⁶ Max.			+25 °C, 5 years			

Please keep Vc pin open or ground while powering up Vcc.

(1)

50MHz is not included in Output frequency range.

Product Name (Standard form) VG-4231 CE 27.000000MHz C S C - M 456 7 (2) 3

(56:SE,QC,QB are not available)

①Model 2 Package type 3 Frequency 4 Frequency tolerance / Operating temperature ⑤Frequency control range ⑥Supply voltage ⑦Input resistance (M: 5 MΩ Min.)

(Unit:mm)

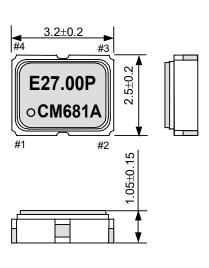
45	④Fre	quency tolerance / Operating temperature	5F	requency control range (Absolute pull range*)	6St	upply voltage
CS	С	±30 × 10 ⁻⁶ / -20 to +70 °C	S	±140 × 10 ⁻⁶ Min. (±100 × 10 ⁻⁶ Min.)	Е	1.8 V Typ.
PS	Р	±37 × 10 ⁻⁶ / -40 to +85 °C	S	±140 × 10 ⁻⁶ Min. (±95 × 10 ⁻⁶ Min.)	В	2.8 V Typ.
CQ	С	±30 × 10 ⁻⁶ / -20 to +70 °C	Q	±120 × 10 ⁻⁶ Min. (±80 × 10 ⁻⁶ Min.)	С	3.3 V Typ.
PQ	Р	±37 × 10 ⁻⁶ / -40 to +85 °C	Q	±120 × 10 ⁻⁶ Min. (±75 × 10 ⁻⁶ Min.)		

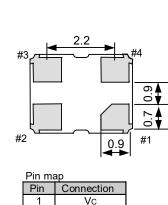
* Absolute pull range = Frequency control range- (Frequency tolerance + 5 years Aging + Free fall + Vibration)

External dimensions

Footprint (Recommended)

(Unit:mm)





GND

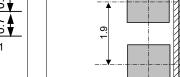
OUT

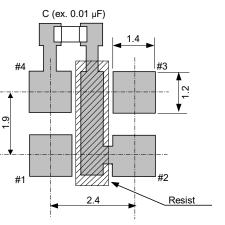
Vcc

2

3

4





To maintain stable operation, provide a $0.01 \mathrm{uF}$ to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

SEIKO EPSON CORPORATION

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Compliant	Contains Pb in products exempted by EU RoHS directive.
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