

# XDS100

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## What is the XDS100?

- The XDS100 emulator is Texas Instruments' ultra-low-cost USB-interface JTAG hardware reference design.
- The XDS100 emulator provides JTAG access to Texas Instruments' JTAG based devices.
- It is compatible with Code Composer Studio™ development environment.
- The hardware is manufactured by TI 3rd parties.
- The XDS100 is available as a discrete emulator, or can be embedded on a development card (DSK, EVM, etc.).
- There are 2 versions of the XDS100: 1) XDS100v1 and 2) XDS100v2. The XDS100v1 is the original XDS100 design. The XDS100v2 is an updated XDS100 design with more capability.
- Performance of the XDS100 is lower than the XDS510 and XDS560. This means that program and data downloads will take a longer time than with XDS510 and XDS560 emulators. Also, stepping in "C" and "Assembly" will be slower with XDS100 when compared to XDS510 or XDS560 emulators.

## General Features

The Texas Instruments' reference design for both the XDS100v1 and XDS100v2 have the following features (Please check your vendor for hardware details):

- Debug features (Emulation Connect/Disconnect, Read/Write memory, Read registers, Load program, Run, Halt, Step, Software and Hardware Breakpoint support, Real-Time Mode)
- Compatible with Code Composer Studio™.
- Support for targets with 1.8v and 3.3v IO voltages.
- Support for "JTAG reset"/"wait-in-reset" boot-modes using the two EMU pins sampled by the nTRST pin.
- Support for "Power-on reset" boot-modes using the two EMU pins sampled by the TVD pin.
- Support for the configuration of the EMU pin features through Code Composer Studio™ Setup "connection properties" dialogs similar to those for the XDS560 Rev-D cable.
- Support for "target power-loss detection" via the TVD pin even when Code Composer Studio™ is not running, and applying boot-modes at Code Composer Studio™ start-up.

## XDS100v1 Features

The Texas Instrument' reference design for XDS100v1 has the following features. Please check with your vendor as to whether the all of the below features are supported:

- All of the XDS100#General\_Capabilities
  - Support for USB Full Speed (12 Mbits/s)
  - Support for multiple FTDI devices (CCSv4 only)
  - Support targets with 14-pin TI JTAG connector used by Texas Instruments embedded processors.
  - Support for the following processors cores: TMS320C28x, TMS320C54x, TMS320C55x, TMS320C64x+, and TMS320C674x
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## XDS100v2 Features

The Texas Instrument' reference design for XDS100v2 has the following features. Please check with your vendor as to whether the all of the below features are supported:

- All of the XDS100#General\_Capabilities
- Support for USB High Speed (480 Mbit/s)
- Support targets with 14-pin TI JTAG connector or 20-pin TI JTAG connector as used by Texas Instruments embedded processors. (Please check your vendor for hardware details)
- Support for the following processor cores: TMS320C28x, TMS320C54x, TMS320C55x, TMS320C64x+, TMS320C674x, ARM 9, ARM Cortex R4, ARM Cortex A8, ARM Cortex A9.
- Supports cable-break detection
- Supports target power loss detection
- Support for multiple FTDI devices
- Adaptive clocking
- LED light to indicate active USB connection
- Support for Code Composer Studio v4 and newer (Does not support Code Composer Studio v3.3)
- Note: As of 4/28/2010, the CPLD update was issued. Please see [XDS100#Q:\\_How\\_can\\_I\\_update\\_the\\_CPLD\\_on\\_my\\_XDS100v2.3F](#)

## Features NOT supported

- It does not support Real Time Data eXchange (RTDX) or High Speed RTDX (HSRTDX)
- It does not provide hardware support for TCLKR external clocking
- Cores NOT supported: ARM 7, ARM 11, ARM Cortex M3, TMS320C24x, TMS320C55x+, TMS320C670x, TMS320C671x, TMS320C672x, TMS320C64x (note that TMS320C64x+ is supported) and other cores not listed as supported.
- It cannot read the value of the EMU0/1 pins, so long profiling operations that use the EMU0/1 pins to handle counter overflows will not be possible.
- JTAG Clock (TCLK) frequency other than default JTAG frequency (1Mhz)

## XDS100 Installation Instructions

### Installation for Code Composer Studio v4.x

**Code Composer Studio v4.x Support** (XDS100v1 and XDS100v2 hardware)

<i>Processor Family</i>	<i>XDS100 Hardware Version</i>	<i>Code Composer Studio Release/Version</i>	<i>Notes</i>
TMS320C28xx	v1, v2	Code Composer Studio v4.1 and newer	No additional SW required for XDS100 support
TMS320C54xx	v1, v2	- as above -	
TMS320C55xx	v1, v2	- as above -	
TMS320C674x	v1, v2	- as above -	
TMS320C64x+	v1, v2	- as above -	
ARM9	v2	- as above -	
ARM Cortex A9	v2	Code Composer Studio v4.2 and newer	
ARM Cortex A8	v2	- as above -	
ARM Cortex R4	v2	- as above -	

A. Install Code Composer Studio 4.x (XDS100 SW support is included) as per the table above **before** connecting XDS100 USB hardware.

B. Connect the XDS100 hardware

1. Make sure the Code Composer Studio 4.x is installed FIRST **before** plugging in the XDS100 HW to the PC.
2. Connect USB cable from the PC to the XDS100 hardware. Connect the JTAG to the target board (be careful to plug it in correctly: pin 1 should go to pin 1. Red strip usually indicates the side of pin 1)
3. You will notice small popups to inform user that USB hardware is recognized and installed correctly. No input are required.

C. Setup Code Composer Studio v4.x

1. Start Code Composer Studio and create a new target configuration. See Quick Tips#Target\_Setup.C2.A0
2. Select XDS100 as connection type (either XDS100 v1 or XDS100 v2)
3. Select device.

### Installation for Code Composer Studio v3.3 (XDS100v1 Hardware Only)

Code Composer Studio v3.3 Support (XDS100v1 HW only)

<i>Processor Family</i>	XDS100 Hardware Version	<i>Code Composer Studio Release/Version</i>	<i>Notes</i>
TMS320C28xx	v1	CCS v3.3 Platinum Edition (TI part #: TMDSCCSALL-1)	Included with CCS Service Release 12 and newer.
TMS320C28xx	v1	CCS v3.3 for C2000 Full Version (version #3.3.78.2) (TI part #: TMDSCCS2000-1) <sup>[1]</sup>	XDS100 SW required. Download here <sup>[2]</sup> .
TMS320C28xx	v1	CCS v3.3 for C2000 DSK Version (version #3.3.81.28, 3.3.83.16 or newer)	XDS100 SW included in CCS Installation. No additional SW required.
TMS320C674x	v1	CCS v3.3 Platinum Edition (TI part #: TMDSCCSALL-1)	Included with CCS Service Release 12 and newer.

**Note:** Code Composer Studio v3.3 ONLY supports XDS100v1 hardware emulator. XDS100v2 hardware is NOT supported on Code Composer Studio v3.3.

A. Install Code Composer Studio and XDS100 Software before connecting XDS100 USB hardware.

1. Install the Code Composer Studio version, service release and/or XDS100 SW as detailed in the table below prior to connecting the XDS100 SW.

B. Connect the XDS100 hardware

1. Make sure the XDS100 Software is installed FIRST per the table above **before** plugging in the XDS100 HW to the PC.
2. Connect USB cable from the PC to the XDS100 hardware. Connect the JTAG to the target board (be careful to plug it in correctly: pin 1 should go to pin 1. Red strip usually indicates the side of pin 1)
3. You will notice small popups to inform user that USB hardware is recognized and installed correctly. No input are required.

C. Setup Code Composer Studio v3.3

1. Select CC Setup icon from the desktop.
2. From the filters panel, select the platform "xds100usb emulator". This will give you a list of predefined configurations. Select the one appropriate for your device
3. Save your configuration and exit CC Setup

4. Start Code Composer Studio
5. In Code Composer Studio, go Debug-->Connect. Note: "connect to target" may take long time (>5 seconds) for the first time XDS100 USB is connected to target. It is not the case with subsequent "connect" operations.

### What are the known issues?

- While loading a large program or program section, the file load status bar may appear frozen when it is not frozen. The status bar is showing section load start and completion.
- When a second FTDI device is plugged in (ex: FT232RL), and it receives a lower chain number, the XDS100 SW will address it instead of the FT2232 used by the XDS100. Solution: only plug the XDS100 HW into the PC. This has been fixed with Code Composer Studio v4 support for the XDS100v2.
- XDS100 SW Driver does not install properly under Windows Vista 64 bit using CCSv3.3
- If "Option" under CCSv3.3 "connect to target at CCS startup" is enabled and c674x target board is connected, then the following error is reported when you try to "Disconnect" for the 1st time. It works fine on 2nd attempt to disconnect. Note, this has been fixed for CCS v4.

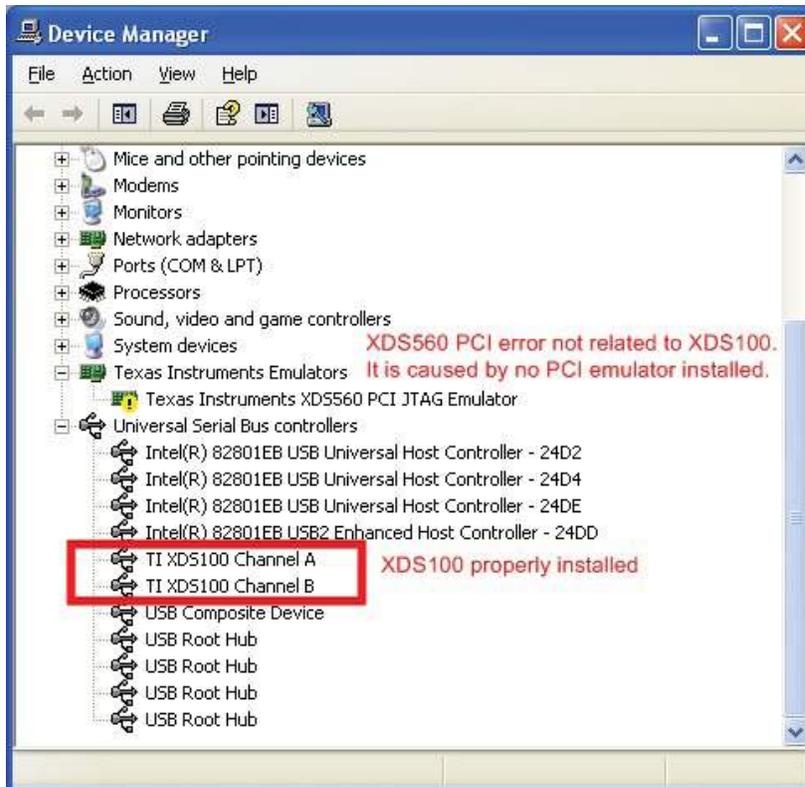
```
Error:
Error 0xA0002020/-150
Error during: Execution, Control,
This error was generated by TI's USCIF driver.

SC_ERR_POD_FAIL <-150>
This utility failed to operate the adapter for a custom emulator.
The adapter returned an error for unknown reasons.
```

- When selecting OMAPL137 configuration in CCS 4.0.2, the GEL files cause the connection to be hung. Remove the GEL file from the target configuration.
- ETB for OMAPL137/8 is not working. Error of "ETB definitions cannot be loaded. Device not supported." is reported. ETB11 for OMAPL137/138 is not supported at this time.
- When installing XDS100v2 patch via update manager, CCS says that the feature is not digitally signed. Continue installation.
- Target power loss detection is not working in CCS v4.1/v4.1.x. This has been fixed in CCS v4.2.
- Adaptive clocking use with OMAPL138/ARM9 cores connection reliability varies by card. This results in connection errors. A fix has been identified by upgrading the CPLD on XDS100v2 designs. Please see XDS100#Q:\_How\_can\_I\_update\_the\_CPLD\_on\_my\_XDS100v2.3F

### Troubleshooting

- Check whether the installation process was followed.
- Please check the FAQ questions below. (Troubleshooting FAQ)
- **Please check the section regarding Troubleshooting CCS.**
- Check your Windows System Devices. When properly installed, the XDS100 should look like the below image. If the XDS100 does not show up in the Windows device manager, then it is likely that the XDS100 was not programmed properly (contact manufacturer) or that there is a conflict with another FTDI based USB device (see FAQ below for details).
- Check USB cable
- Power cycle your target
- You may want to check the VID/PID EEPROM was programed correctly



## Roadmap

- SW Update for XDS100v2 Hardware to support ARM7, ETB
- SW Update for improved performance
- IEEE 1149.7 support

## Where to get an XDS100 / Where to buy an XDS100

### XDS100v2

- Ashling's Opella-XDS100v2 is now available. Check it out here <sup>[3]</sup> and buy here <sup>[4]</sup>
- Blackhawk has one available <sup>[5]</sup> (and our latest model supports both CCS v3.3 and v4., so no more deciding which model to buy)
- Embest has one here <sup>[6]</sup>
- Spectrum Digital, Inc., offers an XDS100v2 USB JTAG Emulator here <sup>[7]</sup>
- TI Estore has a 14 pin TI or a 20 pin compact TI version available <sup>[8]</sup>.

## XDS100v1

- Blackhawk offers a USB 2.0 HIGH-SPEED XDS100-class JTAG Emulator with dual headers (14 and 20cTI) here <sup>[9]</sup>
- Olimex offers an XDS100 emulator here <sup>[10]</sup>.
- Realtime offers an XDS100 emulator here <sup>[11]</sup>.
- SeedDSP offers an XDS100 emulator here <sup>[12]</sup> and here <sup>[13]</sup>.
- C28x has the XDS100 is integrated onto the c28x Piccolo controlstick here <sup>[14]</sup> and here <sup>[15]</sup>.
- 5505 has the XDS100 integrated onto the 5505 eZDSP USB stick here <sup>[16]</sup>
- ZLG (China) <sup>[17]</sup>
- Bilkon <sup>[18]</sup>
- Pantech Solution Pvt Ltd. <sup>[19]</sup>
- Lierda has one (China) <sup>[20]</sup>

## How to make an XDS100 / How to build an XDS100 hardware emulator

- You can make your own XDS100 by using the schematic and programming instructions below.

### XDS100v2 (Recommended)

#### Q: Where can I get the design / schematic / BOM for the XDS100v2?

- A: It is available from here <sup>[21]</sup> (registration required).
- The design files (schematic, gerbers, etc.) are drawn in the program KiCAD. PDF schematic is included. Bill of Materials (BOM) is included. MPROG script file is included. CPLD VHD and JED sources are included.
- Please read the included readme for details.
- Note as of 4/28/2010, the design package was updated with new CPLD source code. This is to fix the issues related to Adaptive Clocking on ARM9.

#### Q: What should the USB device's UID (VID/PID) be?

- A: The EEPROM needs to be programmed for a VID=0403 PID=A6D0. The device name string should be, "Texas Instruments Inc.XDS100 Ver 2.0".
- A: FTDI has a utility called MProg. See here <sup>[22]</sup>. A sample MPROG file is included in the XDS100v2 design package. In order to program the XDS100, you'll need a .ept file. you can download it here

#### Q: XDS100-based C5505 and C5515 EzDSP USB Stick does connect when using "Target Connect"

I get: "Error connecting to the target: Error 0x80000240/-600 Fatal Error during: Initialization, OCS"

- A: This can happen if the FDTI USB driver have not been properly uninstalled / re-installed (i.e. if CCSv4 uninstall did not complete properly). I resolved it by going to Device Manager then right click on:

TI XDS100 Channel B -> Uninstall then TI XDS100 Channel A -> Uninstall Then it re-installed itself when re-connecting the XDS100-based USB stick and "Target Connect" worked afetr re-starting CCSv4

**Q: I want to put an XDS100v2 on my EVM / DSK / card. Can I do this?**

- A: Yes, but please make sure to re-use the entire XDS100 design as-is to ensure compatibility with the software! Removing the CPLD implementing with discrete logic will result in a design that is just as complex and expensive as using the CPLD.

**Q: Can I use port B as a UART?**

- A: Yes. The TI XDS100v2 reference design uses the port A of the FTDI2232H to be for JTAG. This leaves the port B available for use either to program the CPLD and/or for use as a UART. The XDS100v2 design puts the CPLD JTAG programming on a selected number of pins (see XDS100v2 readme for details). This allows the CPLD to be programmed via the FTDI device, greatly simplifying manufacturing.
- Please note that there is a known issue with the current XDS100v2 reference design with the placement of the BCBUS. If you plan to use the port B, please check the readme of the XDS100v2 reference design for details. Generally, a serial port on BCBUS should connect the CPLD as follows: BCBUS1 -> CPLD TCK, BCBUS2 -> CPLD TDI, BCBUS5 -> CPLD TDO, BCBUS6 -> CPLD TMS.
- Please note that using UART mode simultaneously with Code Composer Studio v4 has NOT been tested on the XDS100v2 design and is not officially supported. Experience with C2000 ControlCards which implement this functionality indicate the FTDI driver can handle such an interaction. \*The C2000 control card does implement a serial port simultaneous with JTAG, but it is done with an XDS100v1 design. There is a thread on the forum at [23]. The FTDI driver gives each half of the FTDI chip a separate USB handle and USB endpoint, so they are essentially independent. The FTDI supplied software is the software that is needed to get this support; there is no TI specific software needed for this support.

**Q: I see both 14 pin and 20 pin compact TI headers. Do I need both?**

- A: Both are in the reference schematic for testing Code Composer Studio support. In the TI prototypes, we populate just one of the 2 headers. This allows use to test the extended capability available with the 20 pin connection. Please use the header that is needed for your target card.

**XDS100v1 (Legacy)****Q: Where can I get the schematic for the XDS100v1?**

- A: It is available from here <sup>[24]</sup> (registration required).
  - A: The C2000 control card schematics implement an XDS100 design optimized for C28x processors. The design is available as part of their baseline SW packages. These can be downloaded here <sup>[25]</sup>
-

**Q: What should the USB device's UID (VID/PID) be?**

- A: The EEPROM needs to be programmed for a VID=0403 PID=A6D0. The device name string should be, "Texas Instruments Inc. XDS100 Version 1.0".

**Q: Ok, I built an XDS100, but how do I program the EEPROM for manufacturing?**

- A: FTDI has a utility for this called MProg. See here <sup>[22]</sup>.
- A: For 28x development cards, you may want to see this [l post <sup>[26]</sup>]

## Frequently Asked Questions (FAQ)

### General FAQ

**Q: Where can I find out more about Emulators which are compatible with TI devices?**

- A: A general overview of TI emulation capabilities is here <sup>[27]</sup>. For additional performance and capabilities, you may wish to review the XDS510 and XDS560 product lines which support TI devices.

**Q: I would like to purchase a faster emulator, which one is recommended?**

- A: TMS320C28x: The Spectrum Digital C2000 XDS510LC JTAG emulator is available, details may be found here <sup>[28]</sup>. The Blackhawk C2000 USB controller (TI part #TMDSEMU2000U) is available from TI here <sup>[29]</sup>.
- A: TMS320C674x: The Blackhawk XDS560(TI part #TMDSEMU560U for USB and TI part #TMDSEMU560PCI for PCI) are available from TI here <sup>[30]</sup>. The Spectrum Digital XDS510(TI part #TMDSEMU510U for USB) is available from TI here <sup>[31]</sup>.

**Q: On the c28x, C674x, and C64x+ processor, does the XDS100 support Real-Time Mode?**

- A: Yes. Learn more about Real-Time Mode.

**Q: How to maximize performance of XDS100 under CCS?**

A: A couple of things can be done to improve performance under CCS. Generally, the goal is to minimize the amount of information transferred for every action

- Make the disassembly window smaller or close the disassembly window. Stepping and general performance are better in the "C" language environment if the disassembly window is closed.
  - Minimize the number of registers shown.
  - Minimize the number of watch windows/variable windows.
  - Turn off mixed C/disassembly mode.
  - Make sure you are plugged into a High Speed USB2.0 port (XDS100v2)
-

**Q: Does the XDS100 support debugging the C672x processor?**

- A: No.

**Q: Does the XDS100 support debugging the C24x processor?**

- A: No.

**Q: Does DSP/BIOS Real Time Analysis with RTDX work?**

- Q: When using DSP/BIOS and the Real Time Analysis setup for RTDX, I get the cpu graph and real time logs updating. Does this mean RTDX is working/supported on the XDS100?
- A: RTDX is not supported on the XDS100. Please see the section What features does the XDS100 NOT support

**Q: Can I use Advanced Event Triggering with XDS100?**

- A: Yes.

**Q: Can I plug more than one XDS100 (multiple XDS100) into a PC?**

- A: With CCS v4.1 (with the XDS100v2 SW) and above you can plug more than one XDS100 on a single PC. This is not supported with CCSv3.3.
- Each XDS100 must have a unique serial number. Run xds100serial.exe installed under CCSv4.1 "`<install_dir>/ccsv4/common/uscif/utility`" to get the serial number of each connected XDS100 emulator. Create Target Configuration for 2 emulators, Under "connection properties", change "emulator selection" to "select by serial number" option. Enter the corresponding serial number for each emulator. Save and launch debugger.
- With older versions of CCS (ex: CCS v3.3), there is a known issue (see above) which prevents the CCS XDS100 driver from seeing more than one XDS100 in a system. If more than one is in a system, only the first one is recognized.

**Q: Can I use the XDS100 with Stellaris Cortex M3 devices?**

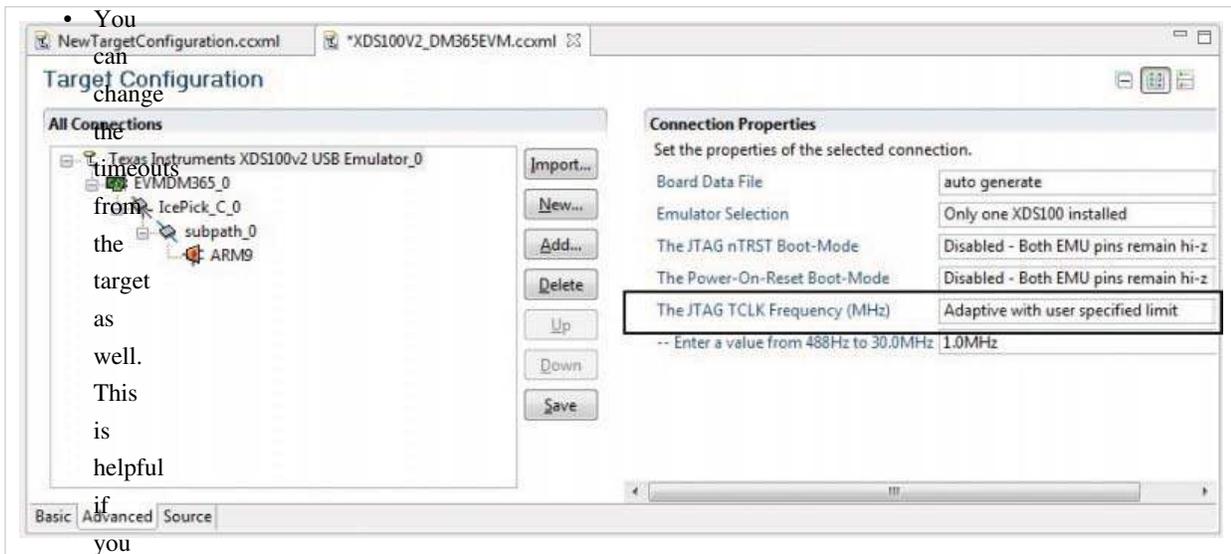
A: No. Stellaris is not supported with CCS v4.1. You may also want to look at: TI tools support for Cortex M3

**Q: Can I use the XDS100v2 as JTAG simultaneously with the second port being a serial port (UART)?**

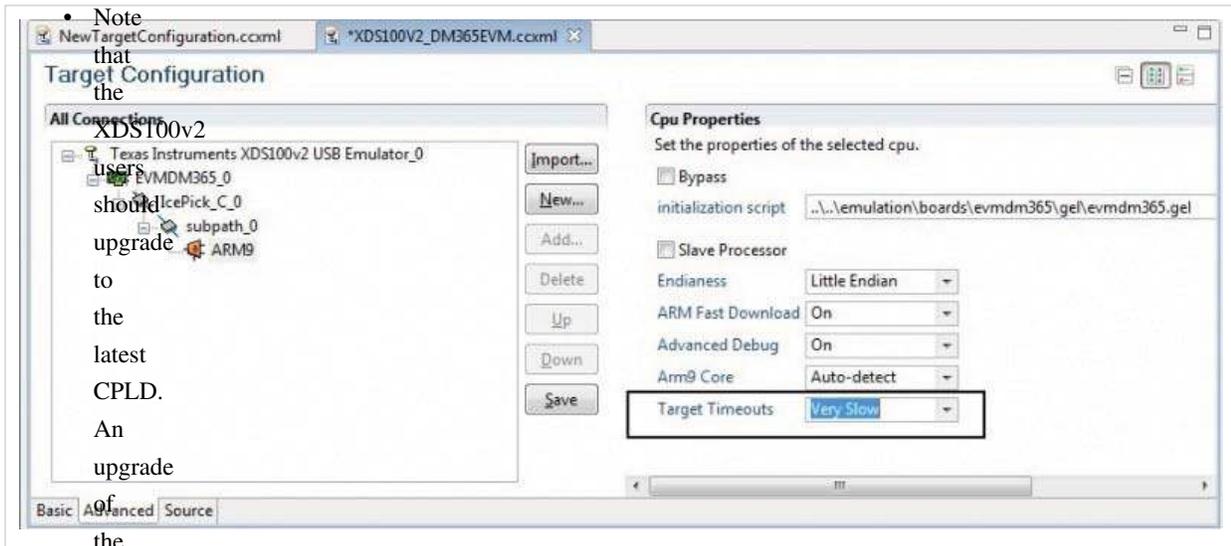
- A: Please check for additional details in the XDS100v2 hardware design section. See XDS100#Q:\_Can\_I\_use\_port\_B\_as\_a\_UART.3F.

**Q: How can I turn on adaptive clocking?**

- A: You need to select the Adaptive Clocking option in the setup box (CCS v4.x).
-



if you get many timeout messages.



the CPLD

will improve reliability for adaptive clocking connections. To update the XDS100v2 CPLD, please see [XDS100#Q:\\_How\\_can\\_I\\_update\\_the\\_CPLD\\_on\\_my\\_XDS100v2.3F](#)

#### Q: My XDS100v2 does not work reliably with the OMAPL138 / DM365 / ARM926 core

- A: This can be caused by a variety of issues such as where the PLL is programmed to the use of Adaptive Clocking emulator. Usually, it is seen as a problem around the PLL setup. This is because the core PLL clock is changing. Example error message:

```
ARM9: GEL Output: Setup PLL0...
```

```
ARM9: Trouble Writing Memory Block at 0x1c40900 on Page 0 of Length
0x4: 0x00000004/-2030 @ marker 1026 Error during: Register, Access to
an unknown or invalid register was attempted.
```

```
ARM9: GEL: Error while executing OnTargetConnect(): target access failed.
```

A couple of things to try:

1. Setup the PLL by executing code (ex: UBoot, etc.) instead of using the GEL file. (preferred)
2. Turn on adaptive clocking XDS100#Q:\_How\_can\_I\_turn\_on\_adaptive\_clocking.3F and setup the timeouts to very slow. In this mode, it is still better to "run" the code and not "step" it.
3. Make sure you have the latest CPLD version. To update the XDS100v2 CPLD, please see XDS100#Q:\_How\_can\_I\_update\_the\_CPLD\_on\_my\_XDS100v2.3F

**Q: Can I use the XDS100 with OMAP-L138/C6748 EVM board?**

- A: See How to connect to the OMAP-L138/C6748 EVM board using CCS?
- A: See also XDS100#Q:\_My\_XDS100v2\_does\_not\_work\_reliably\_with\_the\_OMAPL138\_.2F\_DM365\_.2F\_ARM926\_core.

**Q: Does XDS100 work with Window 7 64 bit?**

- The current driver in XDS100v2 only works with Windows 7 (64 bit) if .exe is run in Windows XP compatibility mode.

**Q: Can I use the XDS100 without CCS? Is there an API to access it's functions?**

- Not at this time.
- We have some requests for an API to read/write targets, run/halt/step, load memory and handle STDIO. If you have requests, please post them in the support forum.
- If you need API for accessing CCS, without a GUI, you may want to consider Debug Server Scripting

## Installation FAQ

**Q: Where are the drivers for the hardware? Windows is asking for the drivers.**

- A: Please see Installation instructions and troubleshooting section.

**Q: What operating systems are supported?**

- A: Please see System\_Requirements for Operating System support for CCS.

**Q: I do not see my F28x Piccolo device listed in the CCS setup menu.**

- A: You have an older version of the XDS100 SW/CCS. The F28x Piccolo device configuration files are included in the latest versions of the SW. Please see the installation notes above.

**Q: What does the windows messages look like in Windows XP upon successful installation?**

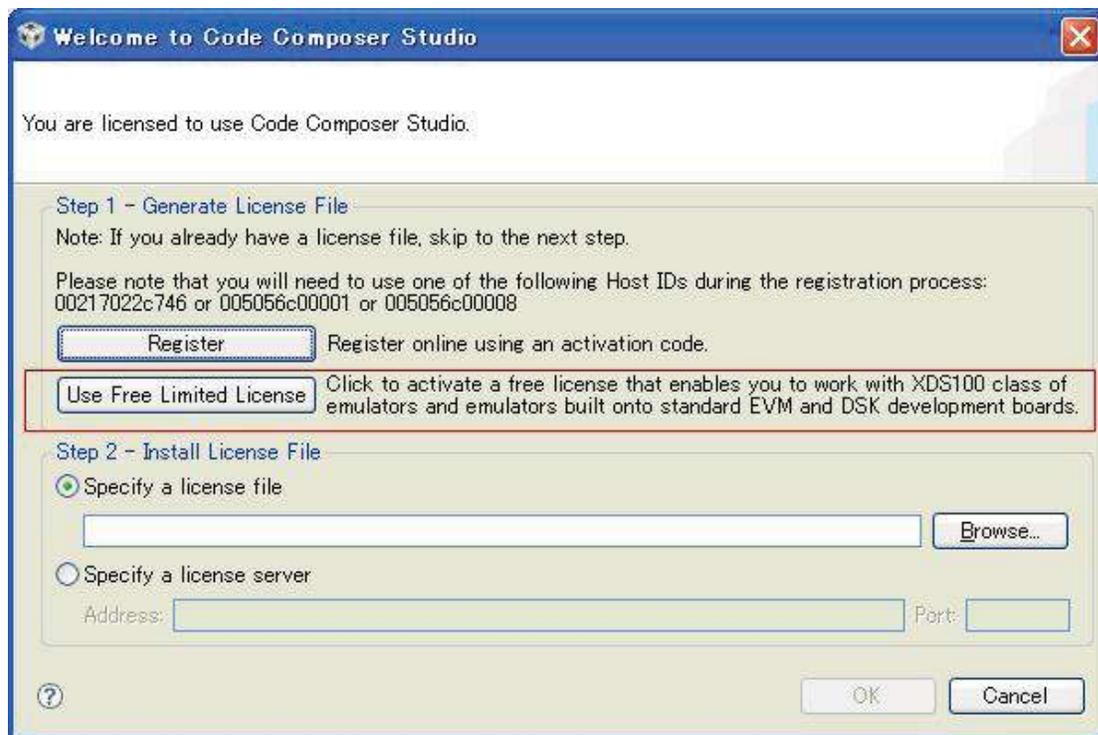
- A: See here <sup>[32]</sup>. Note that after the first installation, there are no subsequent messages in Windows XP from the USB driver.

**Q: I am using CCSv3.3 today. Can I get a driver to support XDS100 for my processor?**

A: All new XDS100 development is being done on Code Composer Studio v4. Existing processors supported by XDS100 in CCSv3.3 will continue to be supported, but newer processors will not be added.

**Q: Can I use Code Composer Studio v4 with XDS100?**

A: There is a free license for Code Composer Studio v4 when used with XDS100. Code Composer Studio v4 can be downloaded from the Code Composer Studio v4 page. When you install CCS, you need to select the correct license. See below on how to select the correct license. This license is designed to be use with XDS100 only, and not XDS510 or XDS560 emulators. Please see [Activating\\_CCS#Generate\\_and\\_Install\\_a\\_License\\_File](#) for details.

**Troubleshooting FAQ****Q: Why is the download frozen?**

- A: While loading a large program or program section, the file load status bar may appear frozen when it is not frozen. The status bar is showing section load start and completion.

**Q: Windows is recognizing the XDS100 as "Dual RS232 USB Serial Bridge" instead of the TI XDS100 JTAG emulator.**

- Q: I have a C2000 Experimenter kits/Piccolo USB Stick that includes the onboard USB JTAG emulation, but the onboard USB JTAG emulation is being recognized by Windows as "Dual RS232 USB Serial Bridge" instead of the TI XDS100 JTAG emulator.
- A: See this post <sup>[26]</sup> on the forums.
- A: You may want to check [XDS100#Q:\\_How\\_can\\_I\\_check\\_if\\_the\\_VID.2FPID\\_for\\_the\\_EEPROM\\_are\\_programmed\\_correctly.3F](#) if your VID/PID are programmed correctly

**Q: I used the FTCClean utility on my computer and now I can't connect to the XDS100! What can I do?**

- A: Running FTCClean will remove the windows FTDI drivers for the XDS100. The TI XDS100 FTDI drivers were installed with the CCS installation or service patch. The XDS100 drivers are signed for the VID/PID combination. To recover, please re-install the CCS version/path you were last using before running FTCClean which included the TI signed XDS100 drivers.

**Q: My XDS560 stopped working when I installed the XDS100...**

- A: Note that the XDS100 software is included and installed with CCS v3.3 SR12 or newer and/or CCSv4. For CCS v3.3 and SR12, it is recommended that users install the new Blackhawk XDS560 driver from Blackhawk website. There is no need to install a new Blackhawk driver with CCS v4.

**Q: My JTAG is not working with CCS...**

A: Please check: Debugging JTAG Connectivity Problems

**Q: I see in my Windows Device Manager an error for a TI XDS560 emulator.**

A: The XDS100 will not be affected by this. Please see XDS560#Q:\_Why\_does\_my\_Windows\_Device\_Manager\_show\_a\_warning\_for\_the\_TI\_XDS560\_PCI\_Emulator.3F

**Q: I see a Windows device manager problem with Texas Instruments XDS560 PCI JTAG emulator. Does this affect the XDS100?**

A: No. This error is caused by the Texas Instruments XDS560 PCI JTAG emulator not being installed. This service may be used by other XDS560 emulators. A proper setup in Windows device manager will look like the image in XDS100#Troubleshooting.

**Q: Can I use an XDS100v2 hardware with a older version of Code Composer Studio? (Ex: CCS v3.3)**

- A: No. The XDS100v2 will not work with older versions of Code Composer Studio that do not support the XDS100v2. Code Composer Studio v3.3 is not supported by the XDS100v2 hardware. For Code Composer Studio v4 users, please upgrade to the latest software versions as listed in the software installation section. If you use a XDS100v2 hardware unit with a version of CCS which does not support it, the software will just see a zero-bit scan chain. If you use DBGJTAG to test the JTAG scan path, it will look like the below, with a zero-bit scan path on both IR and DR.

```
C:\ccsv4\ccsv4\common\uscif>dbgjtag -f
"C:\ccsv4\ccsv4\DebugServer\bin\win32\BrdDat\ccBoard0.dat" -rv -S
pathlength
-----[Print the reset-command software
log-file]-----

This utility will use the original port address '0'.
The controller does not use a programmable FPGA.
The emulator adapter uses the JIO interface.
The emulator adapter is named 'jioserdesusb.dll'.
The emulator adapter is version '35.34.0.7'.
The controller has a version number of '4' (0x00000004).
The controller has an insertion length of '0' (0x00000000).
This utility will now attempt to reset the controller.
This utility has successfully reset the controller.
```

```

-----[Print the reset-command hardware
log-file]-----

The scan-path will be reset by toggling the JTAG TRST signal.
The controller is the FTDI FT2232 with USB interface.
The link from controller to target is direct (without cable).
The software is configured for FTDI FT2232 features.
The controller cannot monitor the value on the EMU[0] pin.
The controller cannot monitor the value on the EMU[1] pin.
The controller cannot control the timing on output pins.
The controller cannot control the timing on input pins.
The scan-path link-delay has been set to exactly '0' (0x0000).
This utility will use the original port address '0'.
The emulator adapter uses the JIO interface.
The emulator adapter is named 'jioserdesusb.dll'.
The emulator adapter is version '35.34.0.7'.
The controller has a version number of '4' (0x00000004).
The controller has an insertion length of '0' (0x00000000).
The local memory has a word capacity of '1048576' (0x00100000).

-----[Perform the standard path-length test on the JTAG IR and
DR]-----

This path-length test uses blocks of 512 32-bit words.

The test for the JTAG IR instruction path-length succeeded.
The JTAG IR instruction path-length is 0 bits.

The test for the JTAG DR bypass path-length succeeded.
The JTAG DR bypass path-length is 0 bits.

```

**Q: On connecting the USB, I get the error message "The file FTDIBUS.SYS on FTDI USB Drivers Disk is needed"**

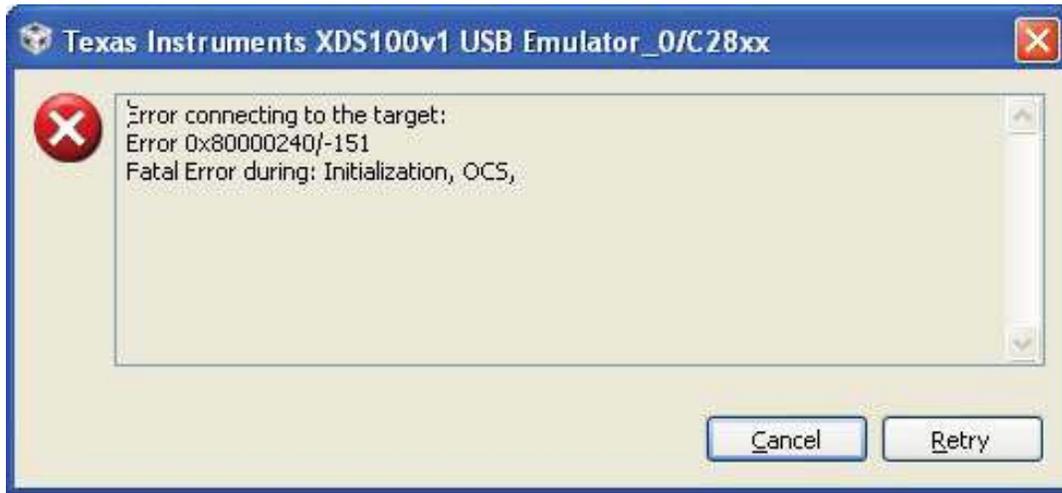
- A: The USB drivers are likely missing. Please see the installation instructions above to install the software before trying to connect the XDS100.
- A: It may also be that the FTDI EEPROM is not properly programmed. Please contact your XDS100 vendor to have the problem corrected. You could also see the section on Xds100#How\_to\_make\_an\_XDS100 to program the EEPROM.

**Q: I got a -150 SC\_ERR\_POD\_FAIL error, what does this mean?**

- For an XDS100, this means that Code Composer Studio is having a problem talking to the XDS100. The problem is with the communication between the adapter and the FTDI chip. Either the FTDI driver has returned an error, or the chip failed to return the expected number of bytes to Code Composer Studio. In practice, this has usually been found to be a problem with the FTDI drivers. Either the wrong drivers were installed, or there is a conflict with FTDI drivers used for another device, or a software process that hasn't completely terminated and is holding the device open. In the case where a software process hasn't terminated, it may be that Code Composer Studio may not have terminated properly. Check the Windows Task Manager that Code Composer Studio related

processes are terminated prior to re-starting.

**Q: I got an "Error connecting to the target: Error 0x80000240/-151 Fatal Error during: Initialization, OCS" when trying to connect to the target in Code Composer Studio v4**



- A: This can occur for several reasons. With XDS100 systems, this can occur because the EEPROM was not programmed properly. Please check with your XDS100 manufacturer.
- A: More details on checking your EEPROM are  
XDS100#Q:\_How\_can\_I\_check\_if\_the\_VID.2FPID\_for\_the\_EEPROM\_are\_programmed\_correctly.3F

**Q: I was following Debugging JTAG Connectivity Problems and I a -151 SC\_ERR\_POD\_OPEN error with Dbgjtag.**

```

C:\WINDOWS\system32\cmd.exe

C:\>"C:\Program Files\Texas Instruments\ccsv4\common\uscif\dbgjtag.exe" -f "C:\Program Files\Texas Instruments\ccsv4\DebugServer\bin\win32\BrdDat\ccBoard0.dat"
-rv -S pathlength

-----[Print the reset-command software log-file]-----

This utility has selected an XDS510 class product.
This utility will load the adapter 'jioserdesusb.dll'.
This utility will operate on port address '0'.

An error occurred while soft opening the controller.

-----[An error has occurred and this utility has aborted]-----

This error is generated by TI's USCIF driver.

The value is '-151' (0xffffffff69).
The title is 'SC_ERR_POD_OPEN'.

The explanation is:
This utility failed to open the adapter for a custom emulator.
The adapter returned an error.

C:\>

```

- A: This can occur for several reasons. With XDS100 systems, this can occur because the EEPROM was not programmed properly. Please check with your XDS100 manufacturer.
- A: More details on checking your EEPROM are  
XDS100#Q:\_How\_can\_I\_check\_if\_the\_VID.2FPID\_for\_the\_EEPROM\_are\_programmed\_correctly.3F

**Q: What happens when I plug a XDS100v2 in to my card but select XDS100v1 or XDS100 as the emulator?**

- A: The output from a path length test will look like it is passing, with a zero - bit path length. This is because the XDS100v2 has a loopback mode which is used in testing. When the XDS100v1 software is used, it invokes this loopback mode.

```

-----[Perform the standard path-length test on the JTAG IR and
DR]-----
This path-length test uses blocks of 512 32-bit words.
The test for the JTAG IR instruction path-length succeeded.

The JTAG IR instruction path-length is 0 bits.
The test for the JTAG DR bypass path-length succeeded. The JTAG DR
bypass path-length is 0 bits.

```

- From Code Composer Studio, the error may look like an unrecoverable emulation error:

```

Error connecting to the target:
Error 0x00001200/-1135
Error during: OCS, Target,
Unrecoverable emulation error

```

**Q: What happens when I plug a XDS100v1 into a card but select an XDS100v2 in CCS?**

- A: The output from path length test will vary with the target device. This is because the XDS100v2 uses the pins on the communications chip different from how it was used on the XDS100v1. On some devices such as c28x, it may cause the device to enter an unexpected bootmode. For the example with a 28x core, a 3-bit path length was seen vs a 38-bit path length (expected). This was because a bootmode was selected.

**Q: I just upgraded to CCS 4.1.2 and now my XDS100 is not working. It was working before!**

- A: Check your VID/PID value. If it does not match, you need to contact your manufacturer, or get the VID/PID EEPROM programmed properly.
- A: With CCS 4.1.2 and newer, a bug was fixed so that the XDS100 software could properly connect only to a XDS100 VID/PID values. In older versions of CCS, the XDS100 software would connect to any VID/PID value causing incompatibilities with non-XDS100 FTDI based devices plugged into the same PC.

**Q: I was using CCS v3.3 SR12 before and upgraded to CCS 4.1.2 (or newer) and now my XDS100 is not working. It was working before!**

- Please see previous topic

**Hardware FAQ****Q: Where can I find out about pinout of JTAG connectors?**

- A: Please check the page on JTAG Connectors.

**Q: What if I need electrical isolation?**

- A: If you are working with a target which has high voltages, you may want to use an isolation adapter. There is an isolation adapter available here <sup>[33]</sup>

**Q: What device voltages are supported by the XDS100v1?**

- A: The XDS100v1 reference design supports 1.8V and 3.3V targets. The SN74CBT3125 (U3) and SN74LVC2T45 (U5) perform the voltage buffering. The MCP6041 (IC5) and TLC393 (IC1B) are used for voltage sensing. The 74HC74(IC3A) and TLC393 (IC1A) form the power detect circuit by reaching the TVO pin.

**Q: I work with high voltages, where can I get an isolation adapter?**

- A: TI offers an isolation adapter (TI part #: TMD5ADP1414-ISO) here <sup>[33]</sup>.

**Q: Can I change the serial number on my XDS100v2?**

- A: Yes, using the FTDI MProg utility and one of the configurations in Media:xds100v2serial.zip so you can change the serial number of an XDS100v2 product if desired.
- If you plan to use 1 or more XDS100v2 emulators in CCSv4 simultaneously then each XDS100v2 must have a unique serial number.
- For ease of use Spectrum Digital generally programs the XDS100v2 with a fixed serial number as this is the most common use case and is consistent with other low cost Spectrum Digital products.

**Steps:**

- 1) Launch MProg.
- 2) Load the desired configuration.
  - For Fixed Serial Number: File->Open SDXDS100v2\_FIXED.EPT
  - For Autogenerated Serial Number: File->Open SDXDS100v2.EPT
- 3) Enable programming of pre-programmed devices if needed.
  - File->Edit and uncheck "Only Program Blank Devices"
- 4) Program the ROM
  - Device->Program

**Q: What frequencies are supported by XDS100v2? Can I change it?**

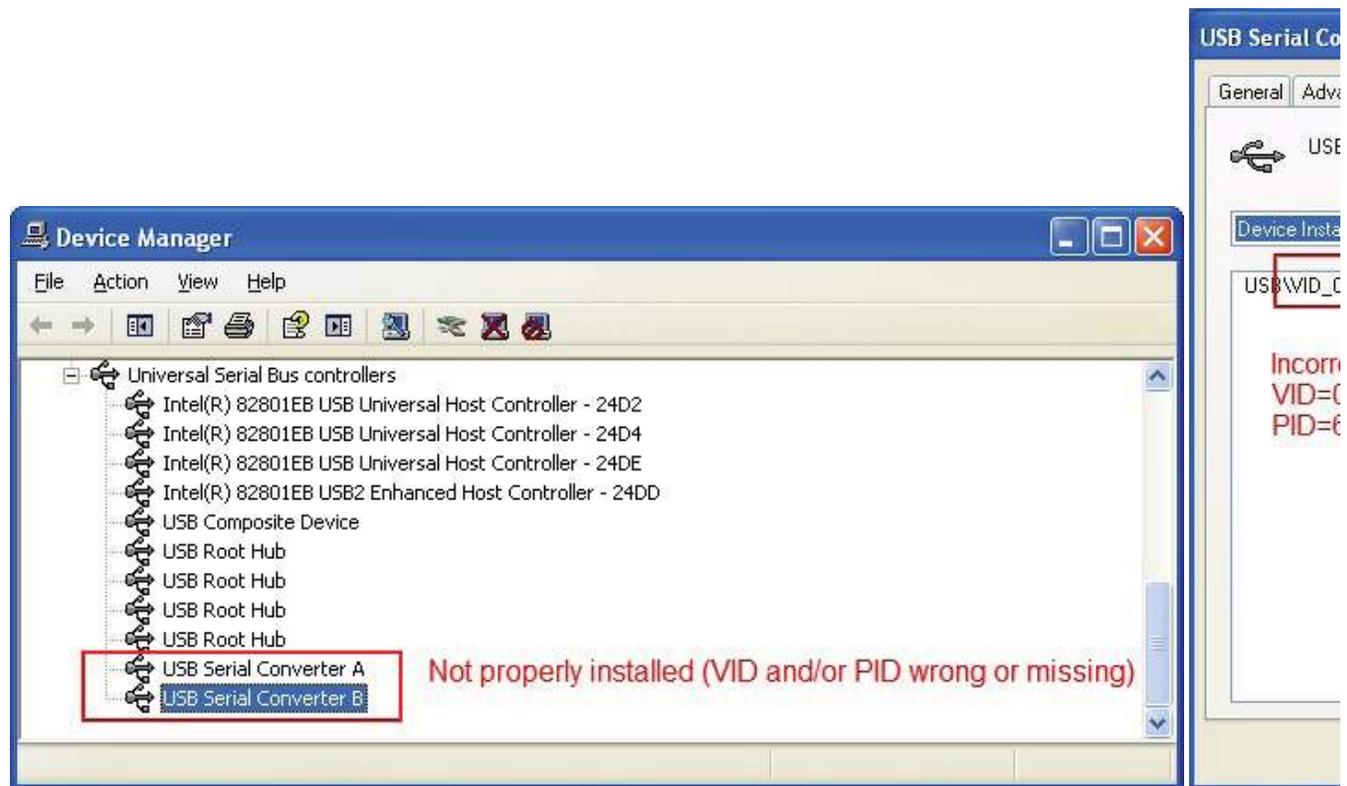
- A: The default frequency is 1Mhz. Other frequencies can be selected, but they are not supported.

**Q: On XDS100v1 design, what does the OE signal do? (pin 12 of FTDI)**

- A: On the XDS100v1 design, this OE signal on pin 12 of the FTDI chip comes from the ICA3A device (74HC74, flip flop). The ICA3A/IC1A section of the circuit samples the TVD signal to detect whether there is target power. This signal is read by the debugger via the GPIO on the FTDI. This signal has an opposite signal /OE which goes to U3. U3 provides a buffer which performs the voltage translation.

**Q: How can I check if the VID/PID for the EEPROM are programmed correctly?**

- A: You can find the VID/PID in windows by using the windows device manager and selecting details. You can also use the FTDI utilities such as MPROG or FT\_Prog. When using MPROG or FT\_Prog, you need to scan devices to read the ID.



- Expected XDS100 VID/PID should be as described in the sections how to make an XDS100
- A: If your hardware VID/PID does not match what is described in How to make an XDS100, please contact your hardware manufacturer. Using MPROG or FT\_Prog and the .ept file that can be downloaded from here, it is possible to reprogram the EEPROM. Refer to the instructions in the How to make an XDS100 section
- A: For some 28x based cards, a similar problem may occur

**Q: Does the XDS100v2 latch data on the rising or falling edge?**

- A: Falling edge.

**Q: How do I tell what the CPLD version is on my XDS100v2?**

- A: Please contact your manufacturer. They may be able to tell from the manufacturing ID. There is no ID in the cpld code, so it cannot be determined by software.

**Q: How can I update the CPLD on my XDS100v2?**

- A: If your manufacturer built the XDS100v2 according to the reference design, you will be able to upgrade the XDS100v2 CPLD via SW. If your manufacturer did not follow the XDS100v2 reference design, please contact your manufacturer directly.
- As of 4/28/2010, an enhancement to improve reliability with devices which require Adaptive Clocking (ex: ARM9) is included with the updated CPLD. The specific items implemented in this CPLD update are:
  1. TCK loopback when a power loss is detected
  2. Fixed definitions of certain signals from "inout" to "output" (EMUs and TMS). T\_SRST remains "inout".

- For XDS100v2 designs which have followed the TI reference design, the CPLD can be re-programmed through the USB by using the utility here <sup>[34]</sup>. Usage instructions are included in the installation.
- Blackhawk has placed XDS100v1/v2 updates here <sup>[35]</sup>. *This includes the CPLD update as well as instructions for updating a Blackhawk v1 USB100 device to v2.*
- Please note that this CPLD update is designed for use with CCS v4.1.1 and newer.

## Older Versions

XDS100 v1.0 RTM SW <sup>[36]</sup>: It is recommended that users upgrade to the latest versions of the XDS100 SW. Please check the Software section above. Do NOT install this if you are using CCS v3.3 SR12 or newer, a CCS which came with your C2000 Control Stick, or CCSv4.

## References

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