



Dual P-Channel 1.8-V (G-S) MOSFET

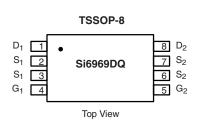
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
- 12	0.034 at V _{GS} = - 4.5 V	± 4.6		
	0.050 at V _{GS} = - 2.5 V	± 3.8		
	0.075 at V _{GS} = - 1.8 V	± 3.0		

FEATURES

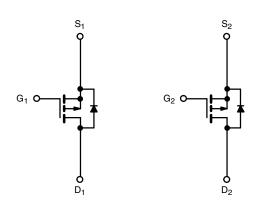
- · Halogen-free
- TrenchFET® Power MOSFETs: 1.8 V Rated



RoHS COMPLIANT



Ordering Information: Si6969DQ-T1-GE3 (Lead (Pb)-free and Halogen-free)



ABSOLUTE MAXIMUM RATINGS $T_A = 25$ °C, unless Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 12		
Gate-Source Voltage		V _{GS}	± 8	V	
Continuous Dusin Comment /T 450 90\8. b	T _A = 25 °C	± 4.6	± 4.6		
Continuous Drain Current (T _J = 150 °C) ^{a, b}	T _A = 70 °C	I _D	± 3.8		
Pulsed Drain Current		I _{DM}	± 30	Α	
Continuous Source Current (Diode Conduction) ^{a, b}		I _S	- 1.25		
M. D. D. J. J. ah	T _A = 25 °C	D_	1.1	w	
Maximum Power Dissipation ^{a, b}	T _A = 70 °C	P _D	0.72	VV	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Marrian de Ambiento	t ≤ 10 s	R _{thJA}		110	°C/W
Maximum Junction-to-Ambient ^a	Steady State	' 'thJA	115		

Notes:

a. Surface Mounted on FR4 board.

b. $t \le 10 \text{ s}$.

Si6969DQ

Vishay Siliconix



SPECIFICATIONS T _J = 25 °C, unless otherwise noted							
Parameter	Symbol	Test Conditions Min.		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.45			V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}$	V _{DS} = - 9.6 V, V _{GS} = 0 V		- 1		
		$V_{DS} = -9.6 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 70 ^{\circ}\text{C}$			- 25	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge$ - 8 V, V_{GS} = - 4.5 V	- 30			Α	
		$V_{GS} = -4.5 \text{ V}, I_D = -4.6 \text{ A}$		0.027	0.034		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = -2.5 \text{ V}, I_D = -3.8 \text{ A}$		0.037	0.050	Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -3.0 \text{ A}$		0.053	0.075		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 8 V, I _D = - 4.6 A		18		S	
Diode Forward Voltage ^a	V_{SD}	$I_S = -1.25 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.68	- 1.1	V	
Dynamic ^b							
Total Gate Charge	Q_g			21	40		
Gate-Source Charge	Q_{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -4.6 \text{ A}$		4.5		nC	
Gate-Drain Charge	Q_{gd}			3.5			
Turn-On Delay Time	t _{d(on)}			25	50		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		35	60		
Turn-Off Delay Time	t _{d(off)}	$I_D\cong$ - 1 A, V_{GEN} = - 4.5 V, R_G = 6 Ω		80	150	ns	
Fall Time	t _f			40	80		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.25 A, dI/dt = 100 A/μs		50	100		

Notes:

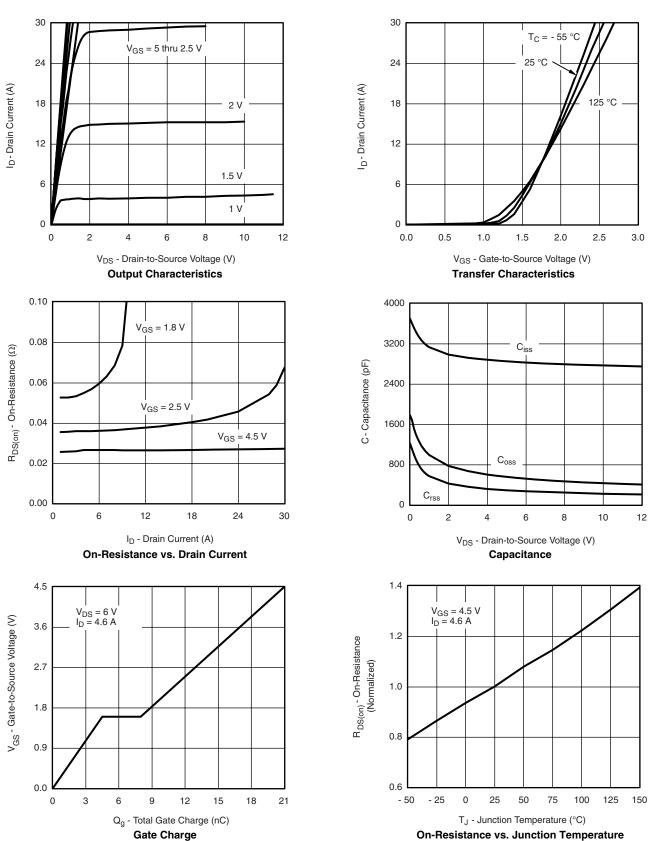
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.



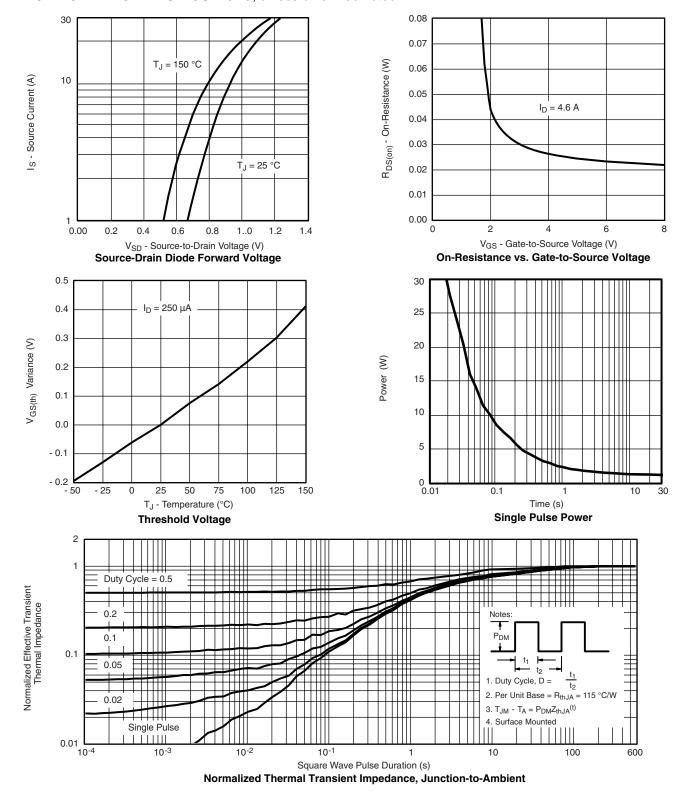
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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