

The Si51211/Si51218 EVB Quick Start Guide



Identifying the part on the EVB:

1. The part number on the EVB is indicated by the marked check box.

Key features of the EVB:

1. Implements frequency plans for Si51210/11/14 on the Si51211 EVB and the frequency plans for Si51218 on the Si51218 EVB
2. Supports controlling input pins through Clockbuilder Pro™
3. Supports output clock signal integrity measurement
4. Provides option to use external input clock, crystal or an on-board Si514 for input clock generation (the on-board Si514 is the input clock source by default)
5. Provides controls for setting VDDO to 1.8V, 2.5V or to 3.3V and for setting VDD to 2.5V or 3.3V.
6. Controlled by the Clockbuilder Pro™ software

Quick start guide to using the EVB:

1. Connect the EVB to PC directly into the USB port or via a cable
2. Run the Clockbuilder Pro™ software and create the frequency plan of interest.
3. When the plan is complete, you can use the EVB control GUI to program the plan to the EVB.
4. The EVB control GUI can also be used to control the voltage supplies and the input pins.
 - a. Please note that if VDD is disabled i.e. if the device is powered down, the plan will not be retained.
 - b. On power re-cycle, the Clockbuilder Pro™ software needs to be used to reprogram the EVB