2SD1819G

Silicon NPN epitaxial planar type

For general amplification Complementary to 2SB1218G

Features

- \bullet High forward current transfer ratio $h_{F\!E}$
- \bullet Low collector-emitter saturation voltage $V_{\mbox{CE(sat)}}$
- S-Mini type package, allowing downsizing of the equipment and automatic insertion through the tape pacing and the magazine pacing.

Absolute Maximum Ratings $T_a = 25^{\circ}C$ Parameter Symbol Rating Unit V_{CBO} V Collector-base voltage (Emitter open) 60 Collector-emitter voltage (Base open) V_{CEO} 50 V V Emitter-base voltage (Collector open) 7 V_{EBO} 100 Collector current I_C mA Peak collector current 200 mА I_{CP} Collector power dissipation P_{C} 150 mW °C Junction temperature Ti 150 °C Storage temperature Tstg -55 to +150

Package

- Code
- SMini3-F2
- Marking Symbol: Z
- Pin Name
 - 1: Base
 - 2: Emitter
 - 3: Collector

Electrical Characteristics $T_a = 25^{\circ}C \pm 3^{\circ}C$

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_{\rm C} = 10 \ \mu A, I_{\rm E} = 0$	60	0		V
Collector-emitter voltage (Base open)	V _{CEO}	$I_{\rm C} = 2 \text{ mA}, I_{\rm B} = 0$	50			V
Emitter-base voltage (Collector open)	V _{EBO}	$I_{\rm E} = 10 \ \mu A, I_{\rm C} = 0$	7			V
Collector-base cutoff current (Emitter open)	I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$			0.1	μΑ
Collector-emitter cutoff current (Base open)	I _{CEO}	$V_{CE} = 10 \text{ V}, I_B = 0$			100	μΑ
Forward current transfer ratio	h _{FE1} *	$V_{CE} = 10 \text{ V}, I_C = 2 \text{ mA}$	160		460	
	h _{FE2}	$V_{CE} = 2 V, I_C = 100 mA$	90			
Collector-emitter saturation voltage	V _{CE(sat)}	$I_{\rm C} = 100 \text{ mA}, I_{\rm B} = 10 \text{ mA}$		0.1	0.3	V
Transition frequency	f _T	$V_{CB} = 10 \text{ V}, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		3.5		pF

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

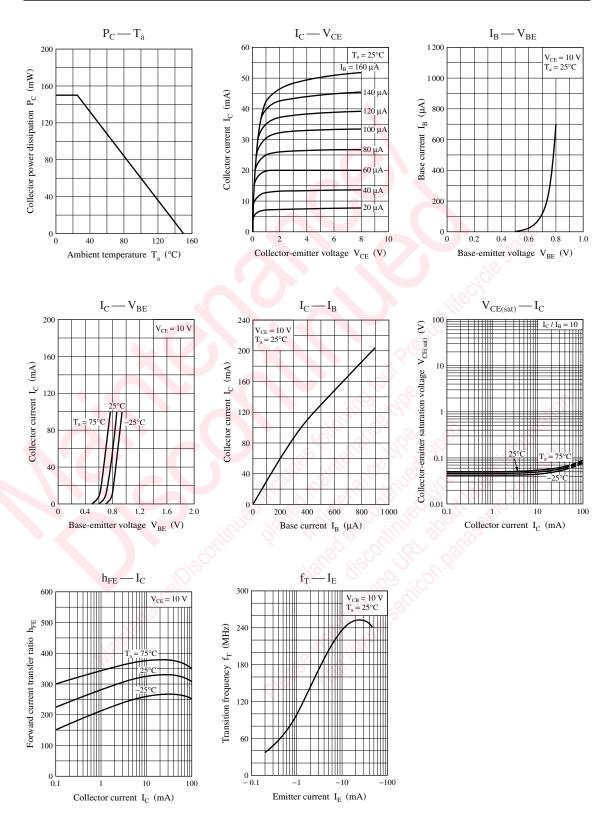
2. *: Rank classification

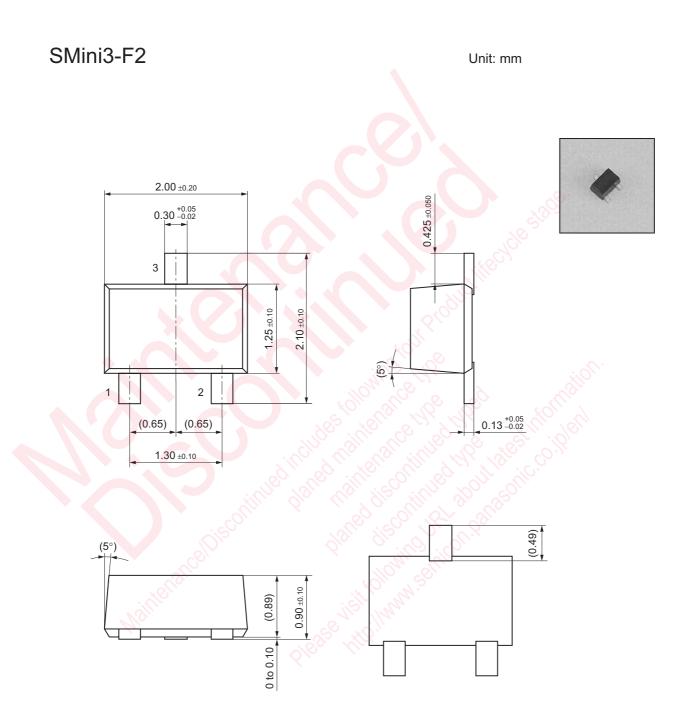
Rank	Q	R	S	No rank
h _{FE1}	160 to 260	210 to 340	290 to 460	160 to 460
Marking symbol	ZQ	ZR	ZS	Z

Product of no-rank is not classified and have no marking symbol for rank.

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