VS-GB75LP120N

Vishay Semiconductors

Molding Type Module IGBT, Chopper in 1 Package, 1200 V, 75 A



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ODUCT SUMMARY	
V _{CES}	1200 V
I_C at T_C = 80 °C	75 A
V _{CE(on)} (typical)	1.82 V

8 kHz to 30 kHz

INT-A-PAK

Half bridge

FEATURES

- High short circuit capability, self limiting to 6 x I_C
- 10 µs short circuit capability
- V_{CE(on)} with positive temperature coefficient Maximum junction temperature 150 °C
- · Low inductance case
- · Fast and soft reverse recovery anti-parallel FWD
- Isolated copper baseplate using DCB (Direct Copper Bonding) technology
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

- AC inverter drives
- Switching mode power supplies
- Electronic welders

DESCRIPTION

Vishay's IGBT power module provides ultra low conduction loss as well as short circuit ruggedness. It is designed for applications such as general inverters and UPS.

ABSOLUTE MAXIMUM RATINGS ($T_C = 25 \degree C$ unless otherwise noted)				
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS
Collector to emitter voltage	V _{CES}		1200	V
Gate to emitter voltage	V _{GES}		± 20	v
Collector current	1	T _C = 25 °C	170	
Collector current	I _C	T _C = 80 °C	75	
Pulsed collector current	I _{CM} ⁽¹⁾	t _p = 1 ms	150	А
Diode continuous forward current	I _F		75	
Diode maximum forward current	I _{FM}		150	
Maximum power dissipation	PD	T _J = 150 °C	658	W
Short circuit withstand time	t _{SC}	T _J = 125 °C	10	μs
RMS isolation voltage	VISOL	f = 50 Hz, t = 1 min	2500	V
l ² t-value, diode	l ² t	V_{R} = 0 V, t = 10 ms, T_{J} = 125 °C	1190	A ² s
Operating junction temperature range	TJ		-40 to +150	°C

Note

PR

at I_C = 75 A, 25 °C

Speed Package

Circuit

⁽¹⁾ Repetitive rating: pulse width limited by maximum junction temperature.

IGBT ELECTRICAL SPECIFICATIONS ($T_c = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TYP.		MAX.	UNITS	
Collector to emitter breakdown voltage	V _{(BR)CES}	T _J = 25 °C	1200	-	-	
Collector to emitter voltage	V _{CE(on)}	$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 75 \text{ A}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	1.82	-	v
Collector to enfitter voltage		$V_{GE} = 15 \text{ V}, \text{ I}_{C} = 75 \text{ A}, \text{ T}_{J} = 125 ^{\circ}\text{C}$	-	2.05	-	v
Gate to emitter threshold voltage	V _{GE(th)}	$V_{CE} = V_{GE}$, $I_C = 3.0$ mA, $T_J = 25$ °C	5.0	6.2	7.0	
Collector cut-off current	I _{CES}	$V_{CE} = V_{CES}, V_{GE} = 0 \text{ V}, \text{ T}_{J} = 25 ^{\circ}\text{C}$	-	-	1.0	mA
Gate to emitter leakage current	I _{GES}	$V_{GE} = V_{GES}, V_{CE} = 0 V, T_J = 25 ^{\circ}\text{C}$	-	-	400	nA

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1

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RoHS

COMPLIANT





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SWITCHING CHARACTERISTICS	5					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Turn-on delay time	t _{d(on)}		-	140	-	
Rise time	t _r		-	37	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 75 \text{ A}, \text{ R}_{g} = 4.7 \Omega,$	-	370	-	ns
Fall time	t _f	V _{GE} = ± 15 V, T _J = 25 °C	-	55	-	
Turn-on switching loss	E _{on}		-	7.2	-	
Turn-off switching loss	E _{off}		-	4.5	-	mJ
Turn-on delay time	t _{d(on)}		-	150	-	
Rise time	t _r		-	40	-	
Turn-off delay time	t _{d(off)}	$V_{CC} = 600 \text{ V}, \text{ I}_{C} = 75 \text{ A}, \text{ R}_{g} = 4.7 \Omega,$	-	400	-	ns
Fall time	t _f	V _{GE} = ± 15 V, T _J = 125 °Č	-	64	-	
Turn-on switching loss	E _{on}		-	9.0	-	mJ
Turn-off switching loss	E _{off}		-	7.4	-	IIIJ
Input capacitance	Cies		-	5.52	-	
Output capacitance	C _{oes}	V _{GE} = 0 V, V _{CE} = 25 V, f = 1.0 MHz	-	0.40	-	nF
Reverse transfer capacitance	C _{res}		-	0.26	-	
SC data	I _{SC}	$\label{eq:tsc} \begin{array}{l} t_{sc} \leq 10 \; \mu s, V_{GE} = 15 \; V, T_J = 125 \; ^{\circ}C, \\ V_{CC} = 900 \; V, V_{CEM} \leq 1200 \; V \end{array}$	-	420	-	А
Internal gate resistance	R _{gint}		-	3	-	Ω
Stray inductance	L _{CE}		-	-	30	nH
Module lead resistance, terminal to chip	R _{CC'+EE'}	T _C = 25 °C	-	0.75	-	mΩ

DIODE ELECTRICAL SPECIFICATIONS ($T_C = 25$ °C unless otherwise noted)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
Diode forward voltage	V _F	I _F = 75 A	T _J = 25 °C	-	2.05	-	v	
Didde forward voltage			T _J = 125 °C	-	2.25	-		
Diada rayaraa raaayany aharga	t _{rr}		T _J = 25 °C	-	100	-	μC	
Diode reverse recovery charge			T _J = 125 °C	-	125	-	μΟ	
Diada paak rayaraa raaayany ay rant	I _{rr}		I _F = 75 A, V _R = 600 V, dI _F /dt = -2000 A/μs,	T _J = 25 °C	-	80	-	А
Diode peak reverse recovery current		$V_{GF} = -15 V$	T _J = 125 °C	-	100	-	A	
Diada rayaraa raaayany anaray	E _{rec}	VGE - 10 V	T _J = 25 °C	-	3.0	-	ml	
Diode reverse recovery energy		⊏rec	T _J = 125 °C	-	6.0	-	mJ	

THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Operating junction temperature range	TJ		-40	-	150	℃	
Storage temperature range	T _{STG}		-40	-	125		
Junction to case IGBT (per 1/2 module)	D		-	-	0.19		
Diode (per 1/2 module)	R _{thJC}		-	-	0.48	K/W	
Case to sink	R _{thCS}	Conductive grease applied	-	0.05	-		
Mounting torque		Power terminal screw: M5	2.5 to 5.0)	Nm	
Mounting torque		Mounting screw: M6		3.0 to 6.0)		
Weight of module				150		g	

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Document Number: 94825



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Eor

40

0.4

20

25

30

35

50

Eoff

60

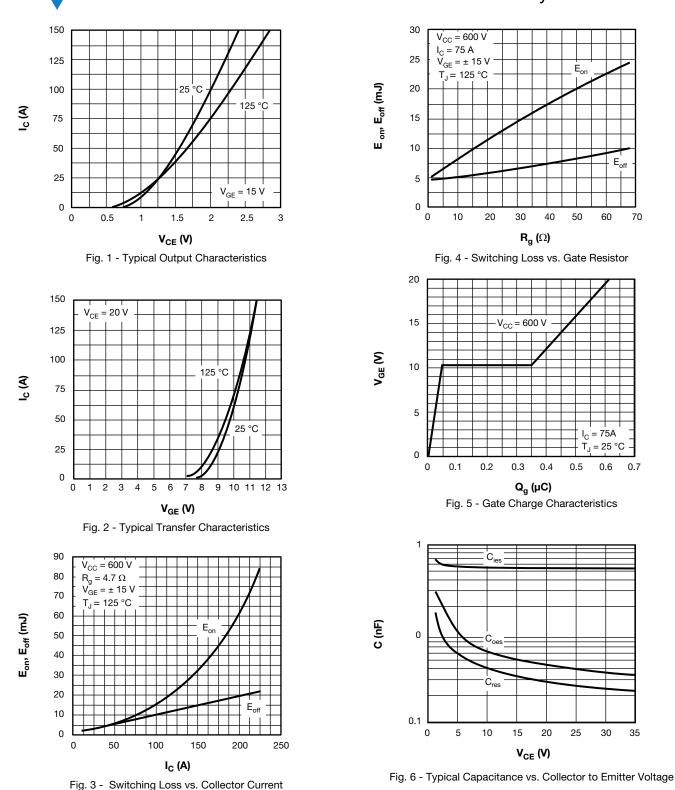
I_C = 75A = 25 °C

0.6

0.5

0.7

70



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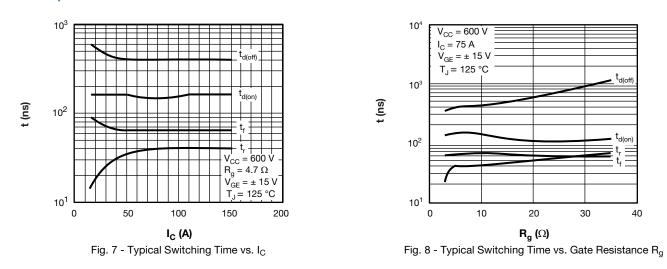
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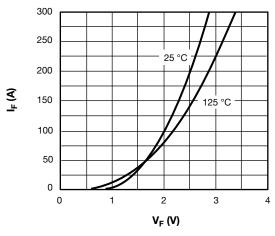
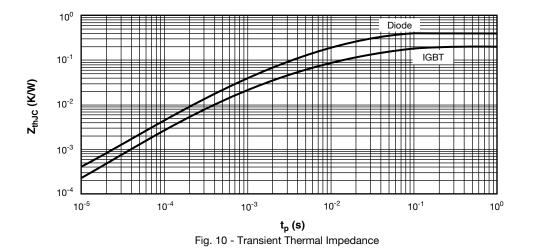


Fig. 9 - Diode Typical Forward Characteristics



 Revision: 10-Jun-15
 4
 Document Number: 94825

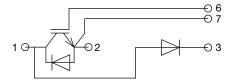
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CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95524			

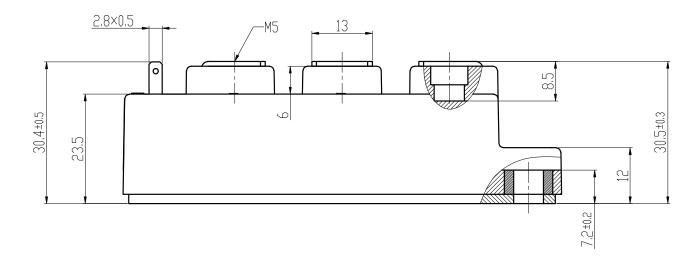
Outline Dimensions

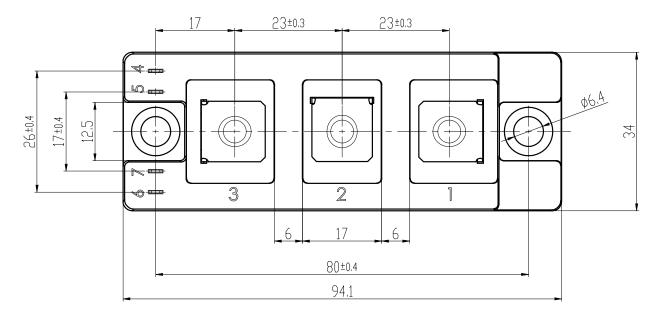


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DIMENSIONS in millimeters (inches)







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