TOSHIBA Diodes for Protecting against ESD

# DF5A5.6LJE

Product for Use Only as Protection against Electrostatic Discharge (ESD)

- \* This product is for protection against electrostatic discharge (ESD) only and is not intended for any other usage, including without limitation, the constant voltage diode application.
- The mounting of four devices on an ultra-compact package allows the number of parts and the mounting cost to be reduced.
- Low terminal capacitance: C<sub>T</sub> = 8.0 pF (typ.)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power dissipation	Р	100	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-50 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

1.6±0.05 1.2±0.05 5 .0±0.05 0.2±0.05 1.6±0.05 0.5 0.55±0.05 0.12±0.05 1.CATHODE1 2.ANODE 3.CATHODE2 4.CATHODE3 5.CATHODE4 ESV JEDEC \_\_\_\_ JEITA TOSHIBA 1-2W1A

Weight: 0.003 g (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### Electrical Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Zener voltage	VZ	$I_Z = 5 \text{ mA}$	5.3	5.6	6.0	V
Dynamic impedance	ZZ	I <sub>Z</sub> = 5 mA	_	_	50	Ω
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 3.5 V	_	_	1.0	μA
Terminal capacitance (between Cathode and Anode)	CT	$V_R = 0 V, f = 1 MHz$		8.0		pF

#### **Guaranteed Level of ESD Immunity**

Test Condition	ESD Immunity Level	
IEC61000-4-2 (Contact discharge)	$\pm$ 8 kV	

Criterion: No damage to device elements

Unit: mm

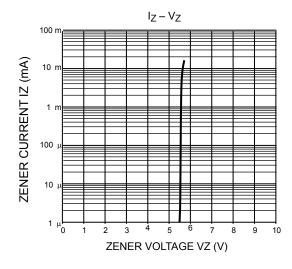
# **TOSHIBA**

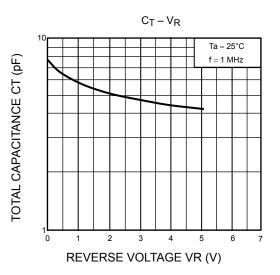
## Marking

Equivalent Circuit (Top View)









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