SCT4026DW7 N-channel SiC power MOSFET

Datasheet

V _{DSS}	750V
R _{DS(on)} (Typ.)	26mΩ
Ι _D ^{*1}	51A
P _D	150W

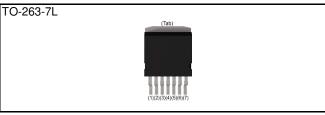
Features

- 1) Low on-resistance
- 2) Fast switching speed
- 3) Fast reverse recovery
- 4) Easy to parallel
- 5) Simple to drive
- 6) Pb-free lead plating ; RoHS compliant

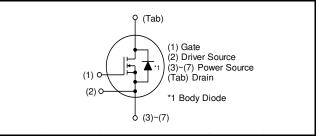
Application

- Solar inverters
- DC/DC converters
- · Switch mode power supplies
- Induction heating
- Motor drives

Outline



Inner circuit



Please note Driver Source and Power Source are not exchangeable. Their exchange might lead to malfunction.

Packaging specifications

Packing	Embossed tape
Reel size (mm)	330
Tape width (mm)	24
Basic ordering unit (pcs)	1000
Taping code	TL
Marking	SCT4026DW7
	Reel size (mm) Tape width (mm) Basic ordering unit (pcs) Taping code

●Absolute maximum ratings (T_{vj} = 25°C unless otherwise specified.)

P	arameter		Symbol	Value	Unit
Drain - source voltage			V _{DSS}	750	V
Continuous drain		$T_c = 25^{\circ}C$	ı ı *1	51	А
and source current	$V_{GS} = V_{GS_{on}}$	T _c = 100°C	= 100°C I_D, I_S^{*1}	36	А
Pulsed drain current	$V_{GS} = V_{GS_on}$	$T_c = 25^{\circ}C$	I _{D,pulse} *2	91	А
Body diode pulsed forwa	ard current	$T_c = 25^{\circ}C$	*1,*3 I _{S,pulse}	51	А
Body diode surge forward current $V_{GS} =$		$V_{GS} = 0 V$	*1,*4 I _{S,pulse}	91	А
Gate - source voltage (E	DC)		$V_{GSS_{DC}}$	-4 to +21	V
Gate - source surge volt	tage (t _{surge} < 300)ns)	V_{GSS_surge} *5	-4 to +23	V
Recommended turn-on	gate - source dr	ive voltage	V _{GS_on} *6	+15 to +18	V
Recommended turn-off gate - source drive voltage		V_{GS_off}	0	V	
Virtual junction temperature		T _{vj}	175	°C	
Range of storage temperature		T _{stg}	-40 to +175	°C	

•Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

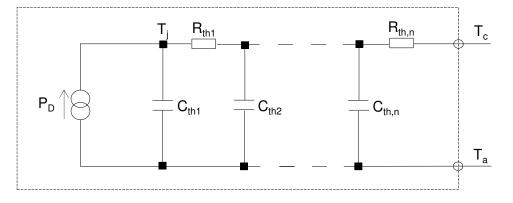
Doromotor	Symbol	Conditions	Values			Unit	
Parameter	Зушоог	Symbol Conditions –		Тур.	Max.	Unit	
Drain - Source breakdown	V	$V_{GS} = 0 V, I_{D} = 9.2 mA$				V	
voltage	V (BR)DSS	$T_{vj} = 25^{\circ}C$	750	-	-	v	
		$V_{GS} = 0 V, V_{DS} = 750V$					
Zero Gate voltage Drain current	$I_{\rm DSS}$	T _{vj} = 25°C	-	1	80	μA	
		T _{vj} = 150°C	-	10	-		
Gate - Source leakage current	I _{GSS+}	$V_{GS} = +21V$, $V_{DS} = 0V$	-	-	100	nA	
Gate - Source leakage current	0.00	$V_{GS} = -4V$, $V_{DS} = 0V$	-	-	-100	nA	
Gate threshold voltage	$V_{GS(th)}$ *7	$V_{DS} = 10V, I_{D} = 15.4mA$	2.8	-	4.8	V	
		$V_{GS} = 18V, I_{D} = 29A$					
Static Drain - Source on - state resistance	R _{DS(on)} *8	T _{vj} = 25°C	-	26	34	mΩ	
		T _{vj} = 150°C	-	44	-		
Gate input resistance	R _G	f = 1MHz, open drain	-	1	-	Ω	

Thermal resistance

Parameter	Symbol	Values			Unit
i arameter	Symbol	Min.	Тур.	Max.	Onit
Thermal resistance, junction - case	${\sf R_{thJC}}^{*9}$	-	0.79	1.0	K/W

•Typical Transient Thermal Characteristics

Symbol	Value	Unit	Symbol	Value	Unit
R _{th1}	5.1 ×10 ⁻²		C _{th1}	8.8 ×10 ⁻⁴	
R _{th2}	3.6 ×10 ⁻¹	K/W	C _{th2}	4.5 ×10 ⁻³	Ws/K
R _{th3}	3.8 ×10 ⁻¹		C _{th3}	1.3 ×10 ⁻¹	





•Electrical characteristics (T_{vj} = 25°C unless otherwise specified)

Doromotor	Symbol	Conditions	Values			L Incit
Parameter	Symbol Conditions		Min.	Тур.	Max.	Unit
Transconductance	g _{fs} *8	$V_{DS}=10V,I_{D}=29A$	-	16	-	S
Input capacitance	C_{iss}	$V_{GS} = 0V$	-	2320	-	
Output capacitance	C_{oss}	V _{DS} = 500V	-	111	-	pF
Reverse transfer capacitance	C_{rss}	f = 1MHz	-	9	-	
Effective output capacitance, energy related	$C_{o(er)}$	$V_{GS} = 0V$ $V_{DS} = 0V$ to 500V	-	143	-	pF
Total Gate charge	Q _g *8	$V_{DS} = 500V$ $I_{D} = 29A$	-	94	-	
Gate - Source charge	Q _{gs} *8	$V_{GS} = 18V$	-	20	-	nC
Gate - Drain charge	Q _{gd} *8	See Fig. 1-1, 1-2.	-	23	-	
Turn - on delay time	t _{d(on)} *8	$V_{DS} = 500V$ $I_{D} = 29A$	-	9.5	-	
Rise time	t _r *8	V _{GS} = +18V / 0V	-	22	-	ns
Turn - off delay time	t _{d(off)} *8	$R_G = 6.8\Omega$, L = 250µH E _{on} includes diode	-	45	-	115
Fall time	t _f *8	reverse recovery $L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF	-	13	-	
Turn - on switching loss	E _{on} *8	See Fig. 2-1, 2-2, 2-3.	-	213	-	
Turn - off switching loss	E _{off} *8		-	73	-	μJ



•Body diode electrical characteristics (Source-Drain) (T_{vj} = 25°C unless otherwise specified)

Parameter	Symbol Conditions		Values			Unit
Farameler	Зушоо	Conditions	Min.	Тур.	Max.	Unit
Forward voltage	V_{SD} *8	$V_{GS} = 0V, I_S = 29A$	-	3.3	-	V
Reverse recovery time	t _{rr} *8	I _F = 29A V _B = 500V	-	12	-	ns
Reverse recovery charge	Q _{rr} *8	di/dt = 2700A/μs	-	141	-	nC
Peak reverse recovery current	I _{rrm} *8	$L_{\sigma} = 50$ nH, $C_{\sigma} = 10$ pF See Fig. 3-1, 3-2.	-	24	-	А

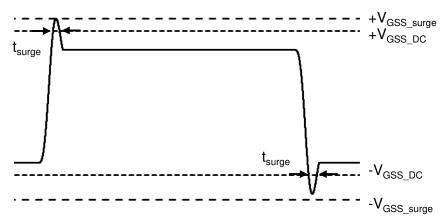
*1 Limited by maximum T_{vj} and for Max. R_{thJC} .

*2 Pulse width and duty cycle are limited by $T_{\nu j,\text{max}}$

*3 Only for body-diode, Repititive pulse, PW \leq 1.5µs, Duty cycle \leq 5%

*4 When used as a protective function, PW \leq 10µs

*5 Example of acceptable V_{GS} waveform



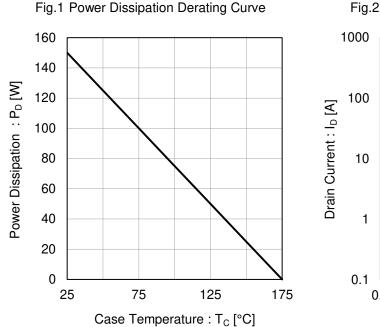
Please note especially when using driver source that V_{GSS_surge} must be in the range of absolute maximum rating.

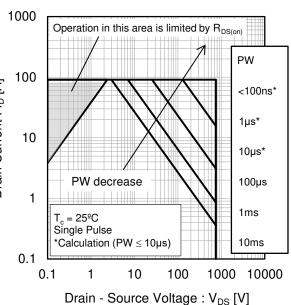
- *6 Please be advised not to use SiC-MOSFETs with V_{GS} below 10V as doing so may cause thermal runaway.
- *7 Tested after applying $V_{GS} = 21V$ for 100ms.
- *8 Pulsed
- *9 Measured conformable to JESD51-14.

See the application note "rthjc_measurement_and_usage_an-e.pdf". Link

 $URL: https://fscdn.rohm.com/en/products/databook/applinote/discrete/common/rthjc_measurement_and_usage_an-e.pdf$









Impedance vs. Pulse Width 1 Transient Thermal Impedance : 0.1 Duty = 1 Z_{thJC} [K/W] 0.5 0.01 0.2 Duty increase 0.1 0.05 0.001 0.02 0.01 Single pulse $T_c = 25^{\circ}C$ 0.0001 1E-6 1E-5 1E-4 1E-3 1E-2 1E-1 1E+0 1E+1 Pulse Width : PW [s]

Fig.3 Typical Transient Thermal

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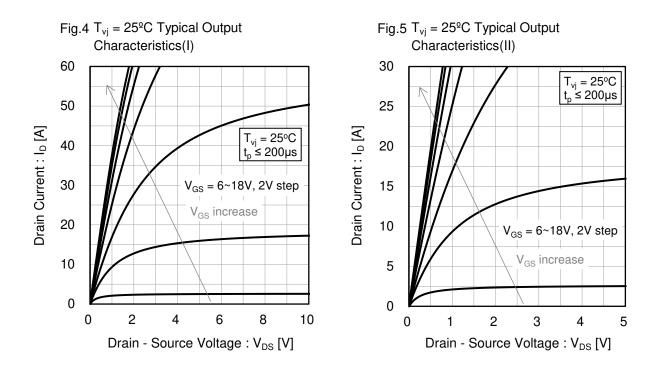
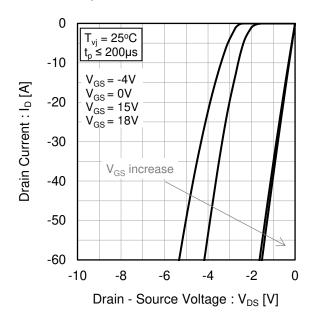
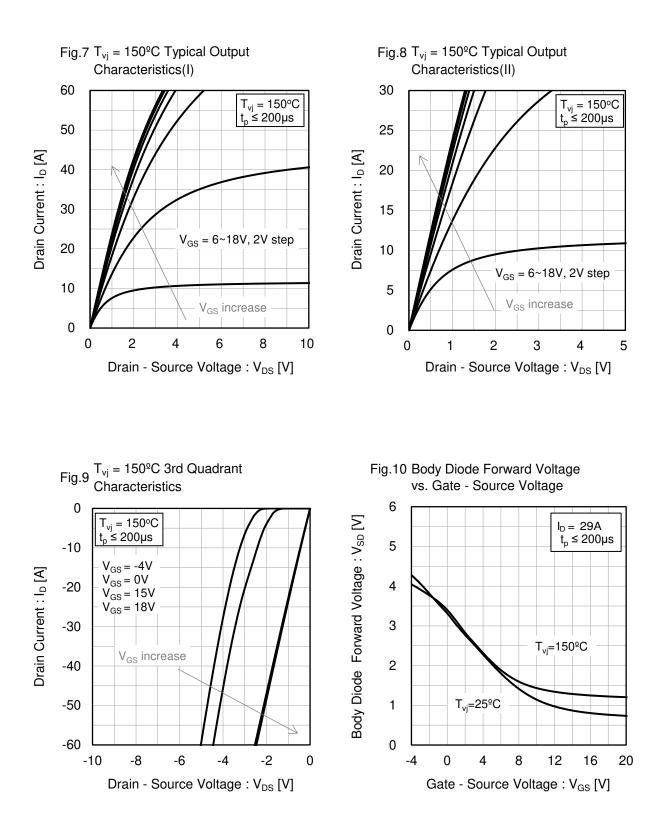


Fig.6 $T_{vj} = 25^{\circ}C$ 3rd Quadrant Characteristics



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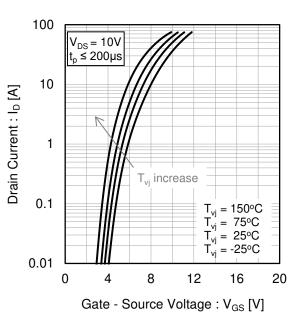
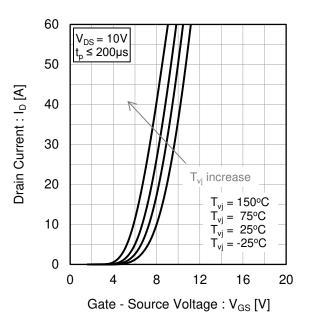


Fig.11 Typical Transfer Characteristics (I)

Fig.12 Typical Transfer Characteristics (II)



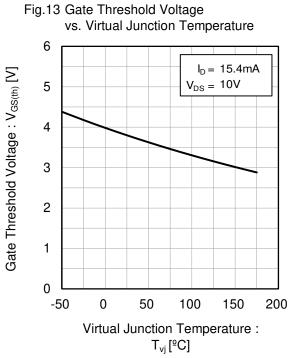
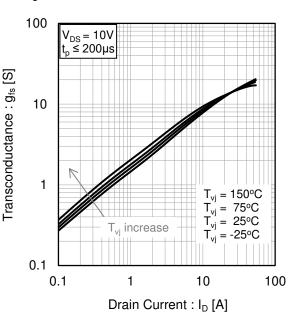
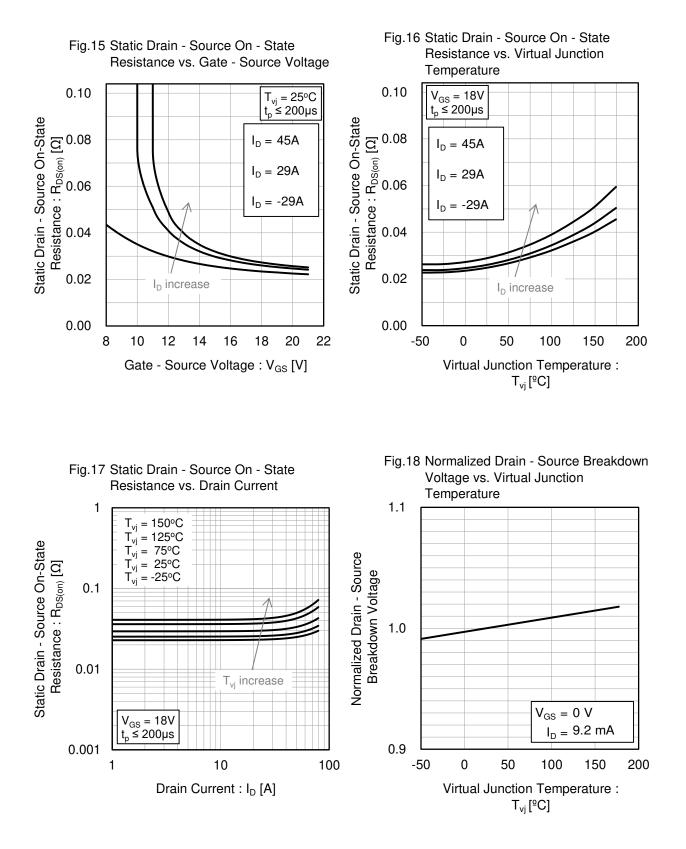


Fig.14 Transconductance vs. Drain Current



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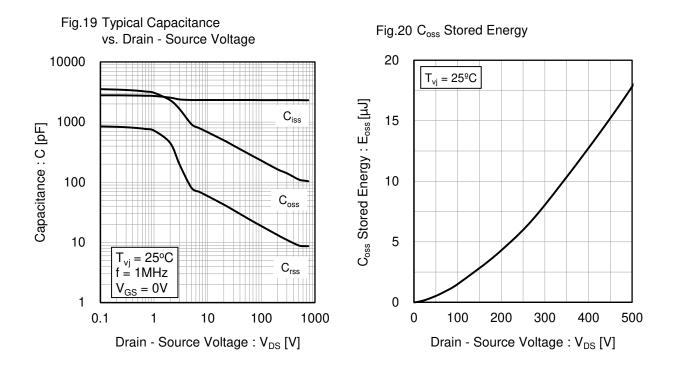
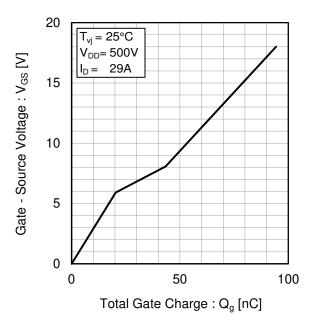
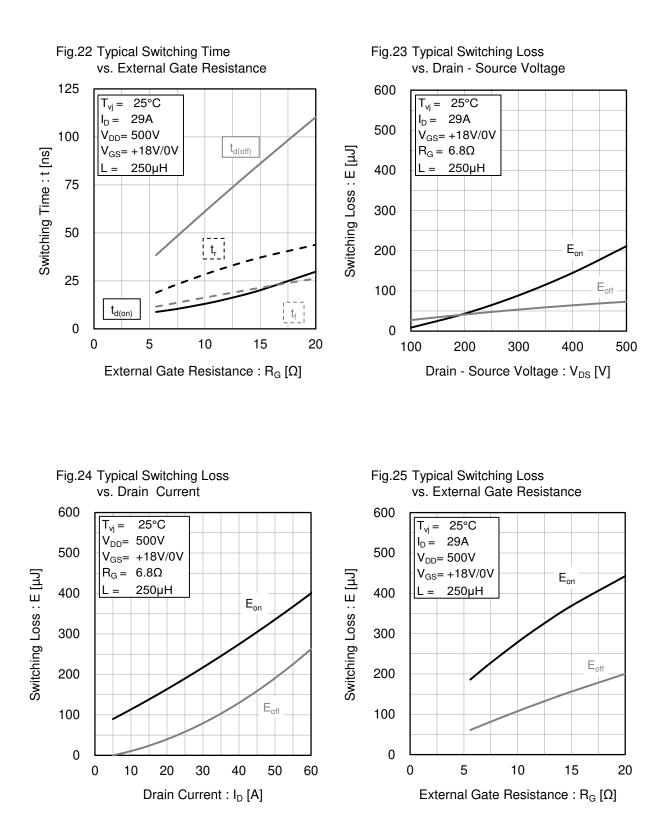


Fig.21 Dynamic Input Characteristics









Measurement circuits and waveforms

Fig.1-1 Gate Charge Measurement Circuit

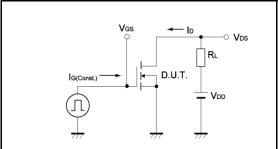


Fig.2-1 Switching Characteristics Measurement Circuit

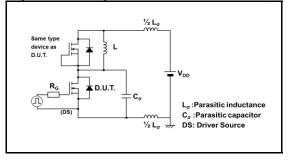


Fig.2-3 Waveforms for Switching Energy Loss

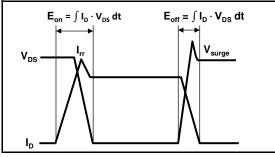


Fig.3-1 Reverse Recovery Time Measurement Circuit

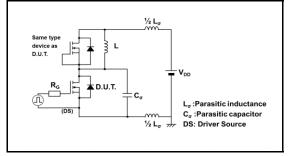
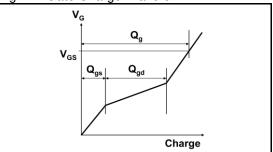


Fig.1-2 Gate Charge Waveform





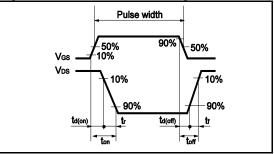
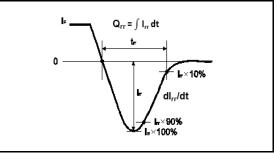
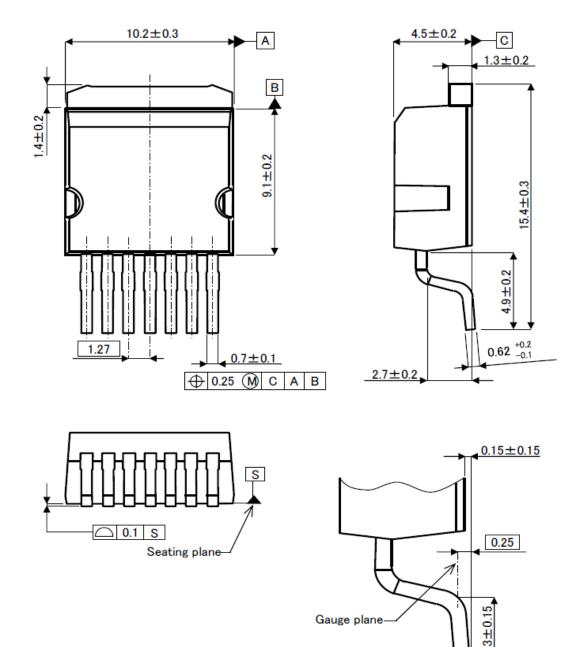


Fig.3-2 Reverse Recovery Waveform





Package Dimensions



Unit: mm

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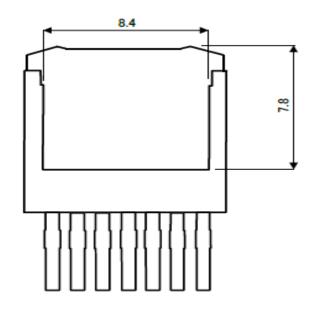
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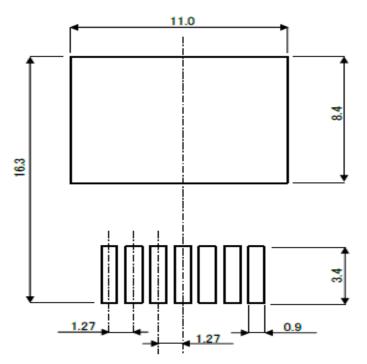
5

4° ±4°



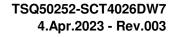


RECOMMENDED FOOTPRINT DIMENSIONS

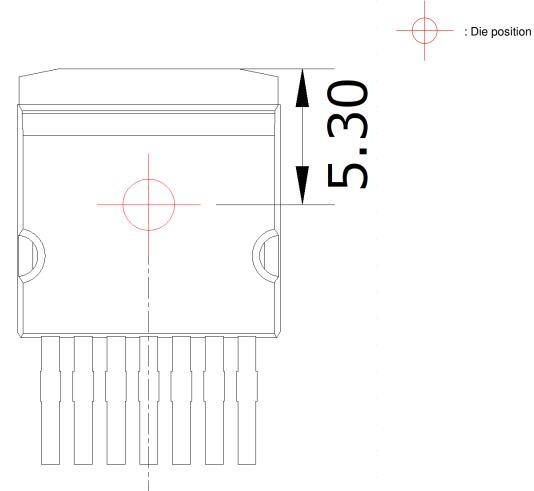


Unit: mm





Die Bonding Layout



•Front view of the packaging.

•Dimensions are design values.

·If the heat sink is to be installed, it should be in contact with the die bonding point.

Unit: mm



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