

Features

- Solid-State Silicon Technology
- Low Leakage Current
- Low Capacitance
- Low Clamping Voltage
- Halogen Free
- Epoxy Meets UL 94 V-0 Flammability Rating
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

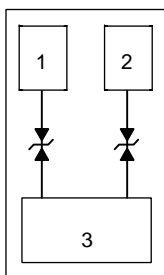
Maximum Ratings

- Operating Junction Temperature Range: -55°C to +125°C
- Storage Temperature Range: -55°C to +150°C

MCC Part Number	Device Marking
ESDSBL5V0LTB	2A

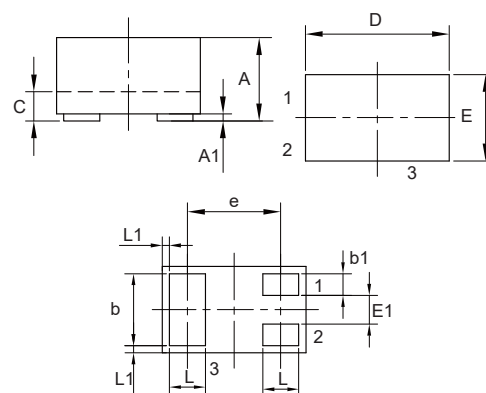
IEC61000-4-2(ESD)	Air Contact	±30KV ±30KV
IEC61000-4-4 (EFT) @5/50ns		40A
Peak Pulse Current(8/20µs)	I _{PP}	8A
Peak Pulse Power (8/20µs)	P _{PK}	96W

Circuit and Pin Schematic



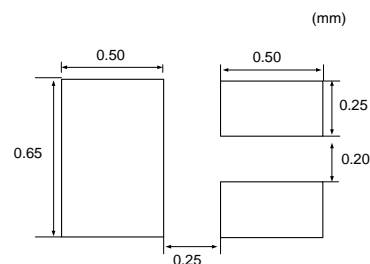
ESD Protection Device

DFN1006-3



DIM	INCHES		MM		NOTE
	MIN	MAX	MIN	MAX	
A	0.018	0.022	0.45	0.55	
A1	0.000	0.002	0.00	0.05	
b	0.018	0.022	0.45	0.55	
b1	0.004	0.008	0.10	0.20	
c	0.005	0.007	0.12	0.18	
D	0.037	0.041	0.95	1.05	
E	0.022	0.026	0.55	0.65	
E1	0.006	0.010	0.15	0.25	
e	0.026		0.65		TYP.
L	0.008	0.012	0.20	0.30	
L1	0.0002		0.05		TYP.

SUGGESTED SOLDER PAD LAYOUT



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Reverse Working Voltage	V_{RWM}				5	V
Reverse Leakage Current	I_R	$V_{RWM}=5V$			100	nA
Reverse Breakdown Voltage	V_{BR}	$I_T = 1mA$	5.3	6		V
Reverse Holding Voltage	V_{HOLD}	$I_{HOLD} = 50mA$	5.3	6		V
Clamping Voltage ^(Note 1)	V_C	$I_{PP}=16A, t_p=100ns$		10		V
Dynamic Resistance ^(Note 1)	R_{DYN}			0.2		Ω
Clamping Voltage ^(Note 2)	V_C	$V_{ESD}=8KV$		10		V
Clamping Voltage ^(Note 3)	V_C	$I_{PP}=1A, t_p=8/20\mu s$			8	V
		$I_{PP}=8A, t_p=8/20\mu s$			12	V
Junction Capacitance	C_J	$V_R=0V, f=1MHz$		10	13	pF
		$V_R=2.5V, f=1MHz$		8	11	pF

Note:

1. TLP Parameter: $Z_0=50\Omega, t_p=100ns, t_r=2ns$, Averaging Window from 60ns to 80ns. R_{DYN} is Calculated from 4A to 16
2. Contact Discharge Mode, According to IEC61000-4-2.
3. Non-repetitive Current Pulse, According to IEC61000-4-5.

Curve Characteristics

Fig. 1 - 8 X 20µs Pulse Waveform

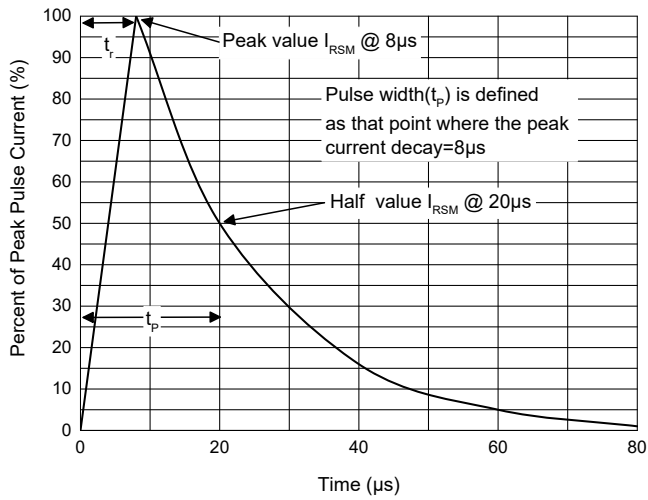


Fig. 2 - Non-Repetitive Peak Pulse Power

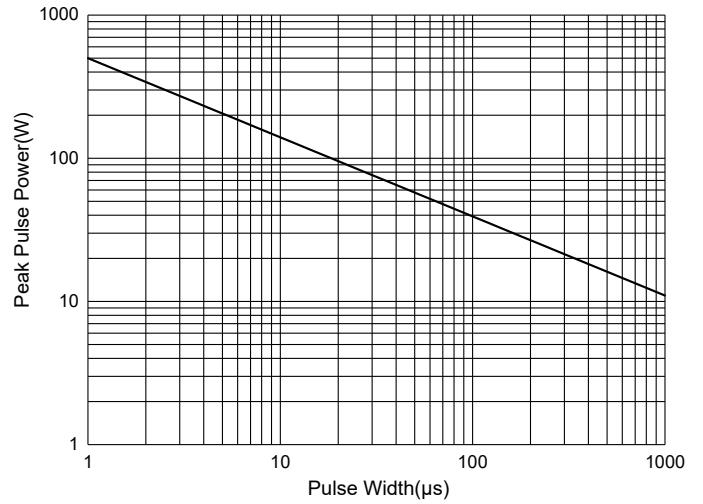


Fig. 3 - Capacitance Characteristics

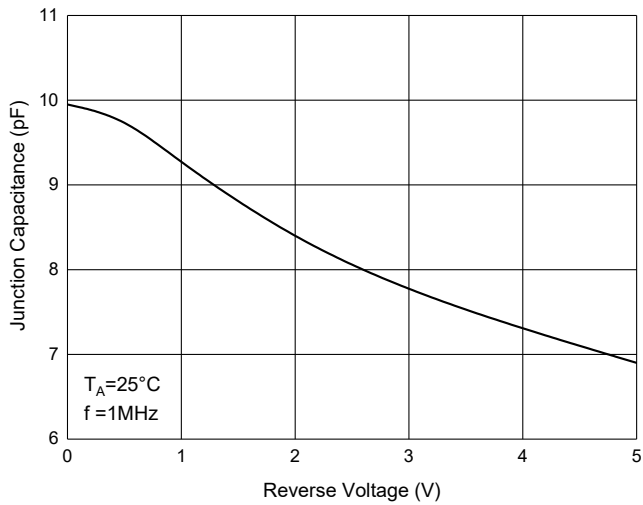


Fig. 4 - Clamping Voltage Characteristics

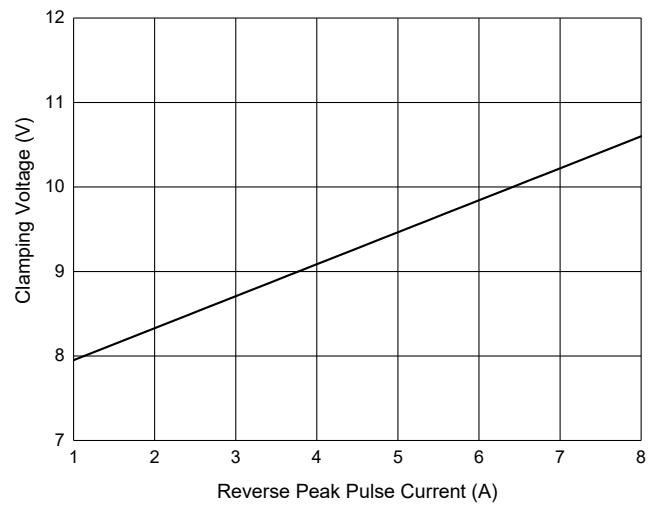


Fig. 5 - Pulse Derating Curve

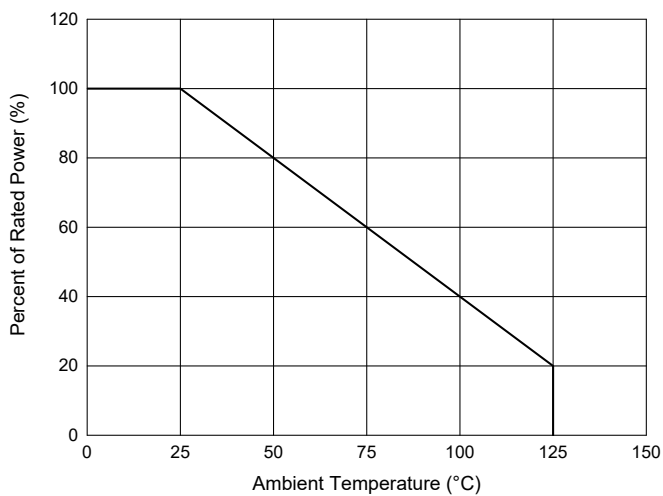
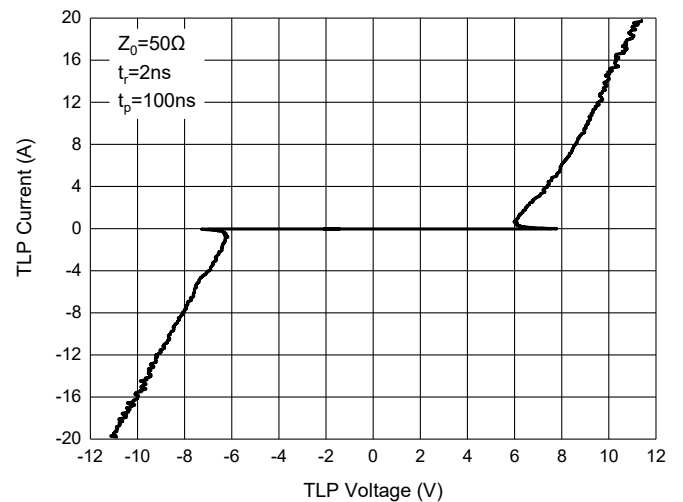


Fig. 6 - TLP Measurement



Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 10Kpcs/Reel

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