MASWSS0129



GaAs Broadband DPDT Diversity Switch DC - 6.0 GHz

Rev. V6

Features

- 802.11a + b/g Dual Band Applications
- Broadband Performance: DC 6.0 GHz
- Low Insertion Loss: 0.7 dB @ 2.4 GHz
 - 1.0 dB @ 4.9 to 6.0 GHz
- High Isolation: 43 dB @ 2.4 GHz
 - 30 dB @ 4.9 to 6.0 GHz
- Fast Switching Speed: 0.5 µm GaAs PHEMT
- Lead-Free 3 mm 12-lead PQFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS* Compliant Version of MASWSS0094

Description

M/A-COM's MASWSS0129 is a broadband GaAs PHEMT MMIC diversity switch available in a low cost, lead-free 3 mm 12-lead PQFN package. The MASWSS0129 is ideally suited for applications where very small size and low cost are required.

Typical applications are for WLAN IEEE 802.11a and 802.11b/g systems that employ two antennas for transmit and receive diversity. Other applications include cordless phones and base stations. Designed for high power, this DPDT switch maintains high linearity up to 6.0 GHz.

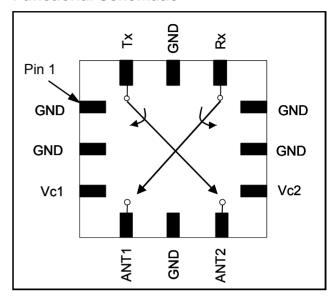
The MASWSS0129 is fabricated using a 0.5 micron gate length GaAs PHEMT process. The process features full passivation for performance and reliability.

Ordering Information ¹

Part Number	Package
MASWSS0129	Bulk Packaging
MASWSS0129TR	7 inch, 1000 piece reel
MASWSS0129TR-3000	13 inch, 3000 piece reel
MASWSS0129SMB	Sample Test Board (Includes 5 Samples)

^{1.} Reference Application Note M513 for reel size information.

Functional Schematic



Pin Configuration

Pin No.	Pin Name	Description		
1	GND	Ground		
2	GND	Ground		
3	V _{C1}	Control 1		
4	ANT1	Antenna Port 1		
5	GND	Ground		
6	ANT2	Antenna Port 2		
7	V _{C2}	Control 2		
8	GND	Ground		
9	GND	Ground		
10	Rx	Receive Port		
11	GND	Ground		
12	Тх	Transmit Port		
13	Paddle ²	RF and DC Ground		

The exposed pad centered on the package bottom must be connected to RF and DC ground.

^{*} Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

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Electrical Specifications: $T_A = 25^{\circ}C$, $Z_0 = 50\Omega$, Vc = 0 V / 3 V, 8 pF Capacitor ³

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Insertion Loss ⁴	2.4 GHz 2 - 4 GHz 4 - 5 GHz 4.9 - 6 GHz	dB dB dB dB		0.70 0.75 0.90 1.00	1.30 — — 1.55
Isolation (on/off or off/on)	2.4 GHz 4.9 - 6 GHz	dB dB	 25	43 30	_
Isolation (on/on or off/off)	2.4 GHz 4.9 - 6 GHz	dB dB	_	11 20	_
Return Loss	2.4 GHz 4.9 - 6 GHz	dB dB	_	23 15	_
IP2	Two Tone, 15 dBm/Tone, 5 MHz Spacing 2.4 GHz 5.3 GHz 5.8 GHz	dBm dBm dBm		93 86 84	
IP3	Two Tone, 15 dBm/Tone, 5 MHz Spacing 2.4 GHz 5.3 GHz 5.8 GHz	dBm dBm dBm		52 50 50	
Input P-1dB	2.4 GHz 5.3 GHz 5.8 GHz	dBm dBm dBm	_ _ _	33 31 31	
2 nd Harmonic	P _{IN} = 20 dBm 2.4 GHz 5.8 GHz	dBc dBc	_	-75 -77	_
3 rd Harmonic	P _{IN} = 20 dBm 2.4 GHz 5.8 GHz	dBc dBc	_	-69 -86	_
Trise, Tfall	10% to 90% RF 90% to 10% RF	nS nS	_	35 60	_
Ton, Toff	50% control to 90% RF, and 50% control to 10% RF	nS	_	90	_
Transients	_	mV	_	6	_
Control Current	Vc = 3 V	μA	_	6	25

^{3.} For positive voltage control, external DC blocking capacitors are required on all RF ports.

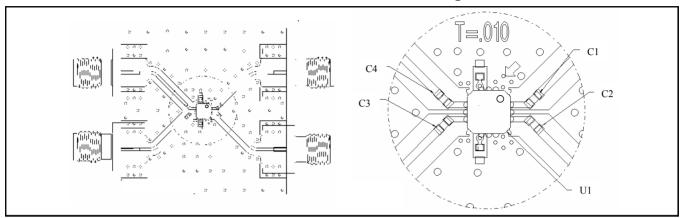
^{4.} Insertion loss can be optimized by varying the DC blocking capacitor value.



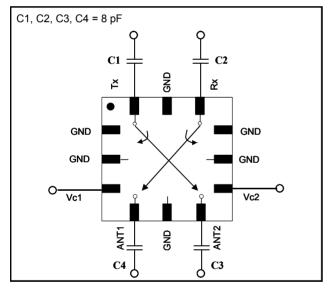
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Evaluation Board for Lead-Free 3 mm 12-lead PQFN Package



Application Schematic



Absolute Maximum Ratings 5,6

Parameter	Absolute Maximum		
Input Power @ 3 V Control	+31 dBm		
Input Power @ 5 V Control	+34 dBm		
Voltage	± 8.5 volts		
Operating Temperature	-40°C to +85°C		
Storage Temperature	-65°C to +150°C		

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.

Truth Table 7,8

Control V _c 1	Control V _c 2	ANT 1 - Rx	ANT 1 - Tx	ANT 2 - Tx	ANT 2 - Rx
1	0	On	Off	On	Off
0	1	Off	On	Off	On
1	1	Off	Off	Off	Off
0	0	Off	Off	Off	Off

- 7. 1 = +2.9 V to +5 V, 0 = 0 V + 0.2 V.
- Differential voltage, V(state 1) V(state 0), must be 2.7 V minimum and must not exceed 8.5 V.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

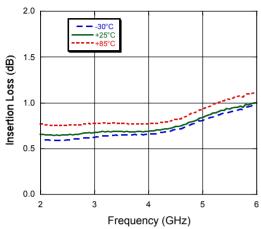


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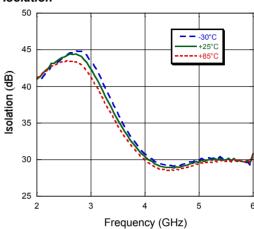
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Typical Performance Curves, 8 pF Blocking Caps

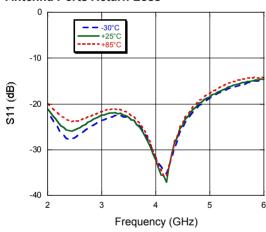
Insertion Loss



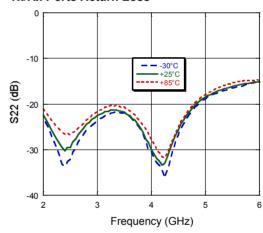
Isolation



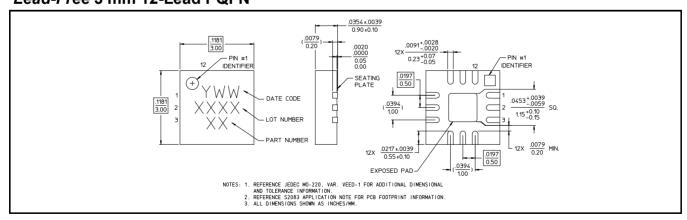
Antenna Ports Return Loss



Tx/Rx Ports Return Loss



Lead-Free 3 mm 12-Lead PQFN[†]



[†] Reference Application Note M538 for lead-free solder reflow recommendations.

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