TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (π-MOSIII)

# 2SK2845

Chopper Regulator, DC/DC Converter and Motor Drive Applications

- Low drain-source ON-resistance :  $R_{DS (ON)} = 8.0 \Omega$  (typ.)
- High forward transfer admittance  $|Y_{fs}| = 0.9 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 720 \ V)$
- Enhancement mode :  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Character	istic	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	900	V	
Drain-gate voltage ( $R_{GS}$ = 20 k $\Omega$ )		V <sub>DGR</sub>	900	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	۱ <sub>D</sub>	1	А	
	Pulse (Note 1)	I <sub>DP</sub>	3	A	
Drain power dissipatio	n (Tc = 25°C)	PD	40	W	
Single-pulse avalanch	e energy (Note 2)	E <sub>AS</sub>	324	mJ	
Avalanche current		I <sub>AR</sub>	1	А	
Repetitive avalanche e	energy (Note 3)	E <sub>AR</sub>	4.0	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature r	ange	T <sub>stg</sub>	−55 to 150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

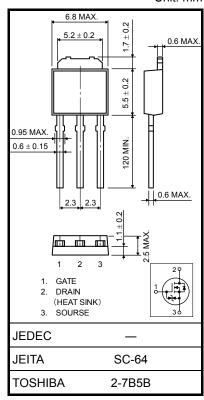
Characteristic	Symbol	Мах	Unit	
Thermal resistance, channel to case	R <sub>th (ch−c)</sub>	3.125	°C / W	
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	125	°C / W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

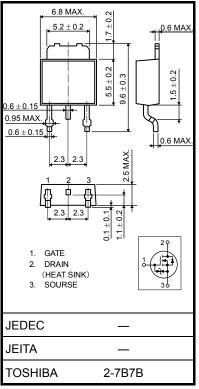
Note 2:  $V_{DD}$  = 90 V,  $T_{ch}$  = 25°C (initial), L = 594 mH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 1 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.36 g (typ.)



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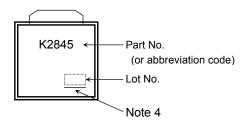
Electrical Characteristics (Ta = 25°C)

Chara	cteristic	Symbol	Test Condition	Min	Тур.	Мах	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = ±30 V, V <sub>DS</sub> = 0 V		—	±10	μA
Gate-source bre	akdown voltage	V <sub>(BR)</sub> GSS	I <sub>G</sub> = ±10 μA, V <sub>DS</sub> = 0 V	±30	_	_	V
Drain cutoff curr	rent	I <sub>DSS</sub>	V <sub>DS</sub> = 720 V, V <sub>GS</sub> = 0 V		_	100	μA
Drain-source bro	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	900	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	_	4.0	V
Drain-source Of	N-resistance	R <sub>DS (ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A	_	8.0	9.0	Ω
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 20 V, I <sub>D</sub> = 0.5 A	0.45	0.9	_	S
Input capacitance $C_{iss}$ Reverse transfer capacitance $C_{rss}$ $V_{DS}$ = 25 V, $V_{GS}$ =			_	350	_		
		C <sub>rss</sub>	V <sub>DS</sub> = 25 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	8	_	pF
Output capacitance		C <sub>oss</sub>			40	_	
Switching time	Rise time	tr	$V_{GS} \stackrel{10V}{}_{0V} \int_{V_{CS}} \stackrel{I_{D}=0.5A}{}_{R_{L}=400\Omega}$	_	20	_	
	Turn-on time	t <sub>on</sub>			70	_	ns
	Fall time	t <sub>f</sub>		_	30	_	
	Turn-off time	t <sub>off</sub>	Duty $\leq 1\%$ , t <sub>w</sub> =10 $\mu$ s		95	_	
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ 400 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 1 A		15	_	nC
Gate-source charge		Q <sub>gs</sub>			6	_	
Gate-drain ("Miller") charge		Q <sub>gd</sub>	]	_	9	_	

#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	1	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_		3	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 1 A, V <sub>GS</sub> = 0 V	_	-	-1.9	V
Reverse recovery time	t <sub>rr</sub>	- I <sub>DR</sub> = 1 A, V <sub>GS</sub> = 0 VdI <sub>DR</sub> / dt = 100 A / μs	_	750	_	ns
Reverse recovery charge	Q <sub>rr</sub>	$1DR = 1A$ , $VGS = 0$ $VdiDR / di = 100 A / \mu s$		3	_	μC

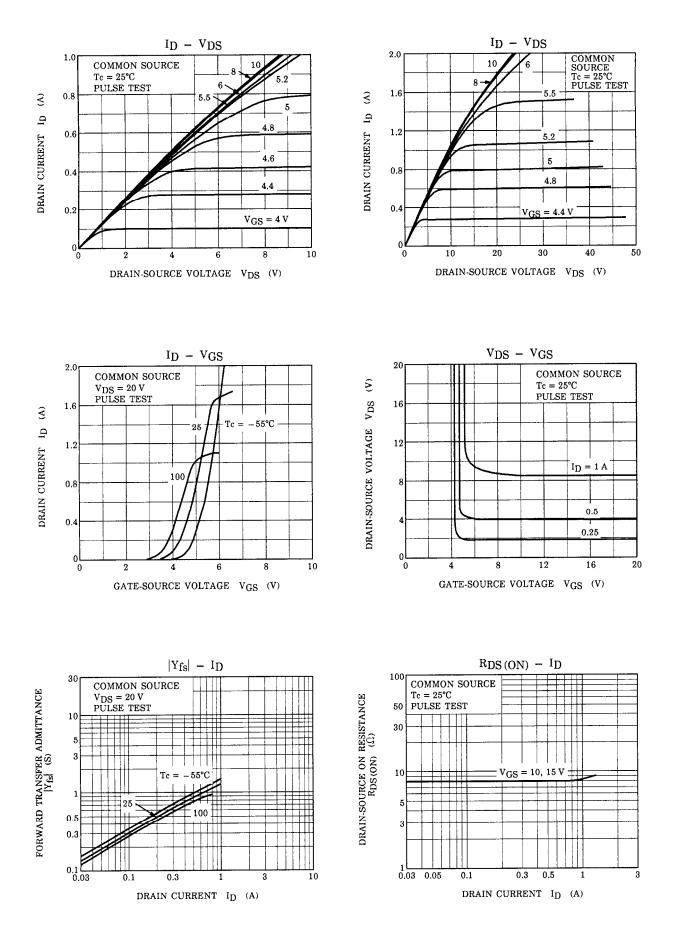
#### Marking



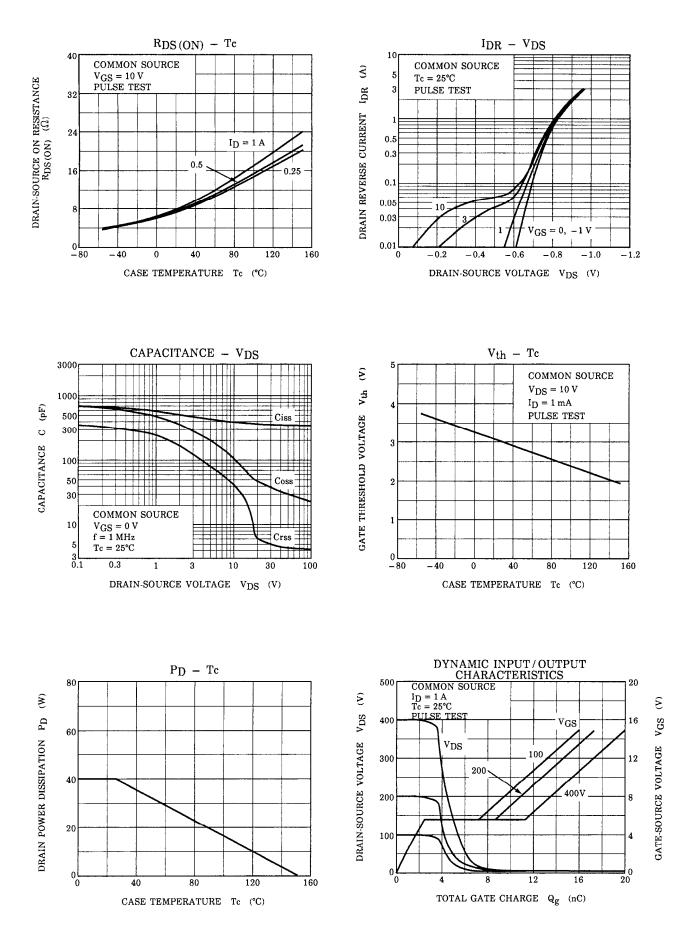
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

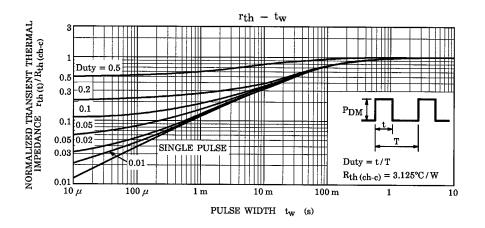
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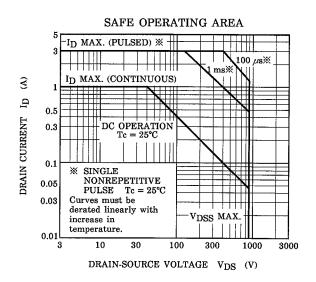
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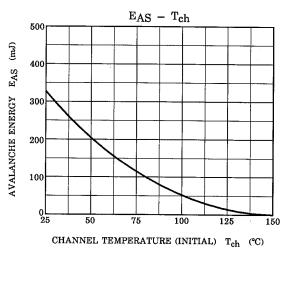


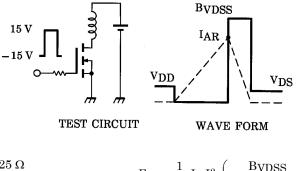
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 $\begin{array}{ll} \mathrm{R_{G}=25\ \Omega} \\ \mathrm{V_{DD}=90\ V,\ L=594\ mH} \end{array} \qquad \qquad \mathrm{E_{AS}=\frac{1}{2}\cdot L\cdot I^{2}\cdot \left(\frac{\mathrm{B_{VDSS}}}{\mathrm{B_{VDSS}-V_{DD}}}\right) } \end{array}$ 

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