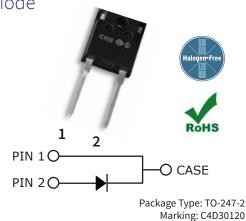


C4D30120H

4th Generation 1200 V, 30 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Increased Creepage / Clearance + HV-H3TRB Rugged

Applications

- Battery Chargers
- Solar & Renewable Energy Power Conversion
- Industrial Power Supplies
- Boost Diodes in PFC & DC-DC

| Parameter | Symbol | Value | Unit | Test Conditions | Note | |
|---|----------------------|-------|---------------|---|----------|--|
| Repetitive Peak Reverse Voltage | V _{RRM} | 1200 | | | | |
| DC Blocking Voltage | V _{DC} 1200 | | - V | | | |
| Continuous Forward Current | I _F | 94 | - - - A | T _J = 25 °C | | |
| | | 45 | | T _J = 135 °C | Fig. 3 | |
| | | 30 | | T _j = 155 °C | | |
| Repetitive Peak Forward Surge | I - | 121 | | T _c = 25 °C, t _p = 10 ms, Half Sine Pulse | | |
| Current | | 68 | | T _c = 110 °C, t _p = 10 ms, Half Sine Pulse | | |
| Non-Repetitive Forward Surge Current | | 233 | _ | $T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Pulse}$ | | |
| | FSM | 209 | _ | $T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Pulse}$ | | |
| Power Dissipation | P _{tot} - | 441 | - W | T _j = 25 °C | — Fig. 4 | |
| | | 191 | | T _j = 110 °C | | |
| i²t Value | ∫i²t - | 271 | - A²s | $T_{c} = 25 \text{ °C}, t_{p} = 10 \text{ ms}$ | | |
| | | 218 | | $T_{c} = 110 \text{ °C}, t_{p} = 10 \text{ ms}$ | | |

Maximum Ratings ($T_c = 25^{\circ}C$ unless otherwise specified)



Electrical Characteristics

| Parameter | Symbol | Тур. | Max. | Units | Test Conditions | Note | |
|---------------------------|----------------|-------|------|-------|---|-------------|--|
| Forward Voltage | V _F | 1.5 | 1.8 | — V | I _F = 30 A, T _J = 25 °C | - Fig. 1 | |
| | | 2.2 | 3 | | I _F = 30 A, T _J = 175 °C | | |
| Reverse Current | I _R | 40 | 250 | | $V_{R} = 1200 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$ | Fig. 2 | |
| | | 70 | 450 | — μΑ | V _R = 1200 V, T _J = 175 °C | - Fig. 2 | |
| Total Capacitive Charge | Q _c | 152 | | nC | $V_{R} = 800 \text{ V}, \text{ T}_{J} = 25 \text{ °C}$ | Fig. 5 | |
| Total Capacitance | | 2,177 | | | $V_{R} = 0 V, T_{J} = 25 °C, f = 1 MHz$ | | |
| | C | 136 | | pF | V_{R} = 400 V, T_{J} = 25 °C, f = 1 MHz | - Fig. 6 | |
| | | 100 | | _ | $V_{R} = 800 \text{ V}, \text{ T}_{J} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$ | | |
| Capacitance Stored Energy | E _c | 44 | | μJ | V _R = 800 V | Fig. 7 | |

Note:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

| Parameter | Symbol | Value | Units | Note |
|---|--------------------|-------------|--------|-----------------|
| Thermal Resistance, Junction to Case (Typ.) | $R_{_{\theta,JC}}$ | 0.34 | °C / W | |
| Operating Junction & Storage Temperature | T_{J},T_{stg} | -55 to +175 | * | Fig. 8 |
| Maximum Processing Temperature | T _{proc} | 325 | (| 10 min. Maximum |

Electrostatic Discharge (ESD) Classifications

| Parameter | Symbol | Value | |
|---------------------|--------|---------------------|--|
| Human Body Model | НВМ | Class 3B (≥ 8000 V) | |
| Charge Device Model | CDM | Class C3 (≥ 1000 V) | |

Typical Performance

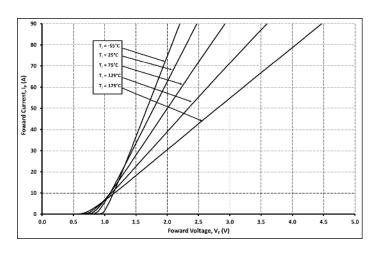


Figure 1. Forward Characteristics

Figure 2. Reverse Characteristics

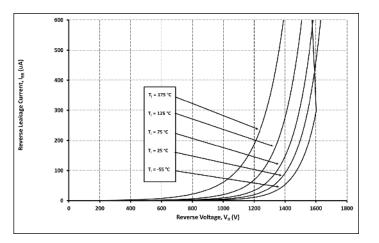
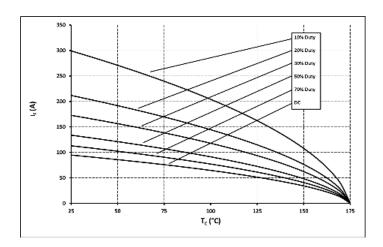


Figure 3. Current Derating





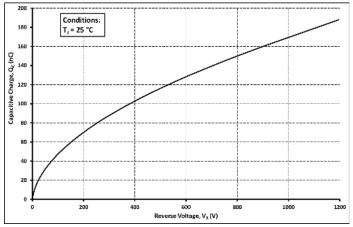


Figure 4. Power Derating

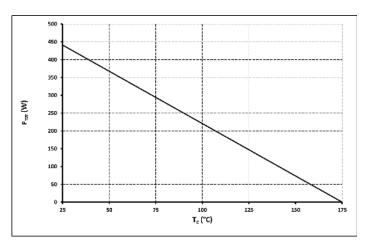
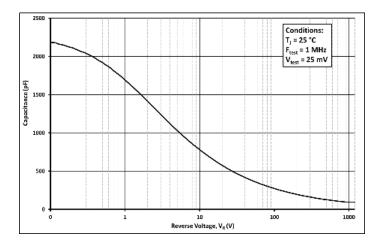


Figure 6. Capacitance vs. Reverse Voltage



Typical Performance

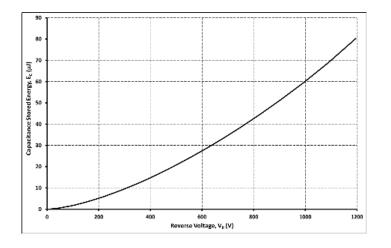
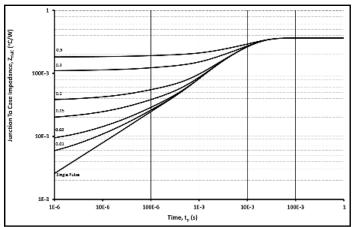


Figure 7. Capacitance Stored Energy

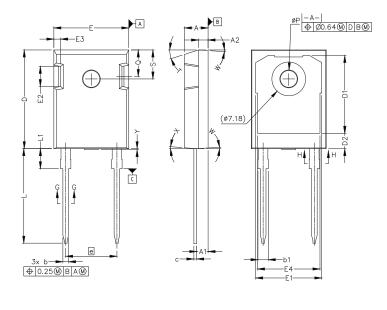






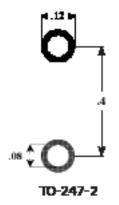
Package Dimensions

Package: TO-247-2 All dimensions in mm.



| 0.44 | MILLIM | ETERS | INCHES | | |
|------|------------|-----------|--------|----------|--|
| SYM | MIN | MAX | MIN | MAX | |
| А | 4.83 | 5.21 | .190 | .205 | |
| A1 | 2.29 | 2.54 | .090 | .100 | |
| A2 | 1.91 | 2.16 | .075 | .085 | |
| b' | 1.07 | 1.28 | .042 | .050 | |
| b | 1.07 | 1.33 | .042 | .052 | |
| b1 | 1.91 | 2.41 | .075 | .095 | |
| b2 | 1.91 | 2.16 | .075 | .085 | |
| c' | 0.55 | 0.65 | .022 | .026 | |
| с | 0.55 | 0.68 | .022 | .027 | |
| D | 20.80 | 21.10 | .819 | .831 | |
| D1 | 16.25 | 17.35 | .640 | .683 | |
| D2 | 2.86 | 3.16 | .112 | .124 | |
| Е | 15.75 | 16.13 | .620 | .635 | |
| E1 | 13.10 | 14.15 | .516 | .557 | |
| E2 | 3.68 | 5.10 | .145 | .201 | |
| E3 | 1.00 | 1.90 | .039 | .075 | |
| E4 | 12.38 | 13.43 | .487 | .529 | |
| e | 10.88 | 10.88 BSC | | .428 BSC | |
| L | 19.81 | 20.32 | .780 | .800 | |
| L1 | 4.10 | 4.40 | .161 | .173 | |
| øР | 3.51 | 3.65 | .138 | .144 | |
| Q | 5.49 | 6.00 | .216 | .236 | |
| S | 6.04 | 6.30 | .238 | .248 | |
| Т | 17.5° REF. | | | | |
| W | 3.5° REF. | | | | |
| Х | 4° REF. | | | | |
| Y | 0 | 0.50 | 0 | 0.020 | |

Recommended Solder Pad Layout



all units are in inches

Learn more about recommended soldering profiles in this application note.

C4D30120H



Notes

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