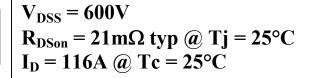
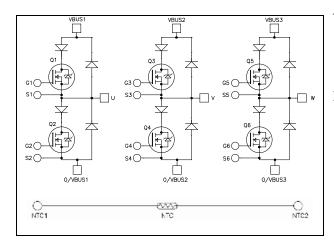


Triple phase leg CoolMOSTM Power Module





VBUS2

MOV/BUS2

Ĥo∕∨BUS1

0/VBUS3

Application

- Welding converters
- Switched Mode Power Supplies
- Uninterruptible Power Supplies
- Motor control

Features

CoolMOSTM

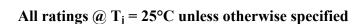
- Ultra low R_{DSon}
- Low Miller capacitance
- Ultra low gate charge
- Avalanche energy rated
- Very rugged

SiC Parallel Schottky Diode

- Zero reverse recovery
- Zero forward recovery
- Temperature Independent switching behavior
- Positive temperature coefficient on VF
- Kelvin source for easy drive
- Very low stray inductance
 - Symmetrical design
 - Lead frames for power connections
 - High level of integration
- Internal thermistor for temperature monitoring
- AlN substrate for improved thermal performance

Benefits

- Outstanding performance at high frequency operation
- Direct mounting to heatsink (isolated package)
- Low junction to case thermal resistance
- Solderable terminals both for power and signal for easy PCB mounting
- Very low (12mm) profile
- Each leg can be easily paralleled to achieve a phase leg of three times the current capability
- Module can be configured as a three phase bridge
- **RoHS Compliant**



NTC1

NTC2

These Devices are sensitive to Electrostatic Discharge. Proper Handling Procedures Should Be Followed. See application note APT0502 on www.microsemi.com



Absolute maximum ratings (Per CoolMOSTM)

Symbol	Parameter		Max ratings	Unit
$ m V_{DSS}$	Drain - Source Breakdown Voltage		600	V
Ţ	Continuous Drain Current $\frac{T_c = 25^{\circ}C}{T_c = 80^{\circ}C}$	$T_c = 25^{\circ}C$	116	
I_D		87	Α	
I_{DM}	Pulsed Drain current		400	
V_{GS}	Gate - Source Voltage		±20	V
R_{DSon}	Drain - Source ON Resistance		21	mΩ
P_{D}	Maximum Power Dissipation	$T_c = 25$ °C	625	W
I_{AR}	Avalanche current (repetitive and non repetitive)		13	A
E_{AR}	Repetitive Avalanche Energy		3	m I
E_{AS}	Single Pulse Avalanche Energy	_	1950	mJ

Electrical Characteristics (Per CoolMOSTM)

	Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
I	I_{DSS}	Zero Gate Voltage Drain Current	$V_{GS} = 0V, V_{DS} = 600V$			200	μΑ
I	R _{DS(on)}	Drain – Source on Resistance	$V_{GS} = 10V, I_D = 88A$		18.5	21	mΩ
	$V_{GS(th)}$	Gate Threshold Voltage	$V_{GS} = V_{DS}$, $I_D = 6mA$	2.4	3	3.6	V
	I_{GSS}	Gate – Source Leakage Current	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			200	nA

Dynamic Characteristics (Per CoolMOSTM)

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
C _{iss}	Input Capacitance	$V_{GS} = 0V ; V_{DS} = 100V$		13		nF
C_{oss}	Output Capacitance	f = 1MHz		0.72		III
Q_{g}	Total gate Charge	$V_{GS} = 10V$		580		
Q_{gs}	Gate – Source Charge	$V_{\text{Bus}} = 480 \text{V}$		72		nC
Q_{gd}	Gate – Drain Charge	$I_D = 88A$		300		
$T_{d(on)}$	Turn-on Delay Time	Inductive Switching @ 25°C		23		
$T_{\rm r}$	Rise Time	$V_{GS} = 13V$		10		
$T_{d(off)}$	Turn-off Delay Time	$V_{\text{Bus}} = 400V$ $I_{\text{D}} = 88A$		130		ns
T_{f}	Fall Time	$R_G = 0.8\Omega$		7		
Eon	Turn-on Switching Energy	Inductive switching @ 125°C $V_{GS} = 13V, V_{Bus} = 400V$ $I_D = 88A, R_G = 0.8\Omega$		1.2		Т
E_{off}	Turn-off Switching Energy			2.8		mJ
R_{thJC}	Junction to Case Thermal Resistance	2			0.20	°C/W



Series diode ratings and characteristics (Per series diode)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current	$V_R = 600V$				100	μΑ
I_F	DC Forward Current		$T_c = 80$ °C		75		A
**	Diode Forward Voltage	$I_F = 75A$	$T_j = 25$ °C		1.6	2	* 7
V_{F}			$T_{j} = 150^{\circ}C$		1.5		V
4	Payarga Pagayary Tima	$I_{\rm F} = 75A$ $V_{\rm F} = 300V$	$T_j = 25$ °C		100		200
t_{rr}	Reverse Recovery Time		$I_F = 75A$ $V_R = 300V$ $T_j = 150^{\circ}C$		150		ns
0	Q_{rr} Reverse Recovery Charge $di/dt = 2000A/\mu s$	$T_j = 25$ °C		3.6		nC	
Qrr		$T_j =$	$T_{j} = 150^{\circ}C$		7.6		IIC
R_{thJC}	Junction to Case Thermal Resistance					0.80	°C/W

SiC Parallel diode ratings and characteristics (Per parallel diode)

Symbol	Characteristic	Test Conditions		Min	Typ	Max	Unit
V_{RRM}	Maximum Peak Repetitive Reverse Voltage			600			V
I_{RM}	Maximum Reverse Leakage Current $V_p = 600V$ \vdash	$T_j = 25$ °C		30	180	μΑ	
-KWI		T	$T_j = 175$ °C		60	900	P4. 1
I_F	DC Forward Current		Tc = 100°C		30		A
V	Diode Forward Voltage	I = 20 A	$T_i = 25^{\circ}C$		1.6	1.8	V
V_{F}	Diode Forward Voltage	$I_F = 30A$	$T_{i} = 175^{\circ}C$		2	2.4	·
Qc	Total Capacitive Charge	$I_F = 30A, V_R = 600V$ $di/dt = 1000A/\mu s$			84		nC
С	Total Capacitance	$f = 1 MHz, V_R = 200 V$			195		рF
C	Total Capacitance	$f = 1 MHz, V_R =$	400V		150		hr.
R_{thJC}	Junction to Case Thermal Resistance					0.80	°C/W

Thermal and package characteristics

Symbol	Characteristic			Min	Тур	Max	Unit
V_{ISOL}	RMS Isolation Voltage, any terminal to case t =1 min, 50/60Hz			4000			V
T_{J}	Operating junction temperature range			-40		150*	
T _{STG}	Storage Temperature Range			-40		125	°C
$T_{\rm C}$	Operating Case Temperature			-40		100	
Torque	Mounting torque	To heatsink	M6	3		5	N.m
Wt	Package Weight		•			250	g

^{*} T_J = 175°C for series and parallel diodes

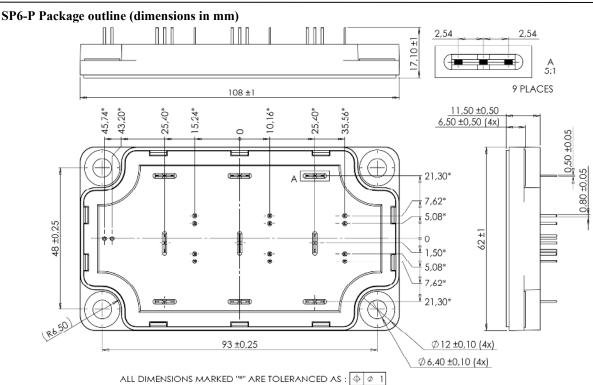
Temperature sensor NTC (see application note APT0406 on www.microsemi.com for more information). Pins NTC1 & NTC2 are only mounted on APTM100TA35SCTPG power module.

Symbol	Characteristic		Min	Тур	Max	Unit
R ₂₅	Resistance @ 25°C			50		kΩ
$\Delta R_{25}/R_{25}$				5		%
B _{25/85}	$T_{25} = 298.15 \text{ K}$			3952		K
$\Delta \mathrm{B/B}$		T _C =100°C		4		%

$$R_T = \frac{R_{25}}{\exp\left[B_{25/85}\left(\frac{1}{T_{25}} - \frac{1}{T}\right)\right]}$$

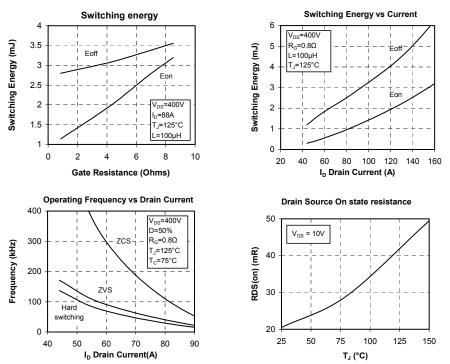
T: Thermistor temperature R_T: Thermistor value at T



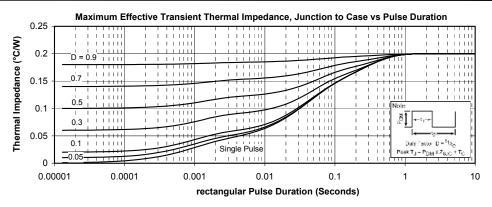


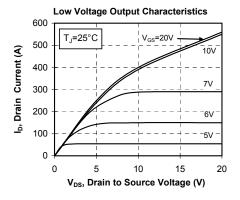
See application note 1902 - Mounting Instructions for SP6-P (12mm) Power Modules on www.microsemi.com

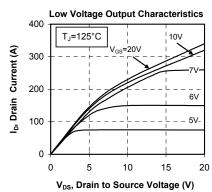
Typical CoolMOSTM Performance Curve

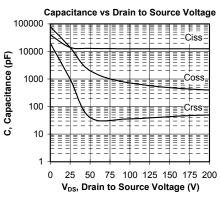


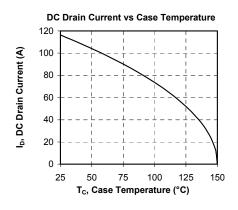


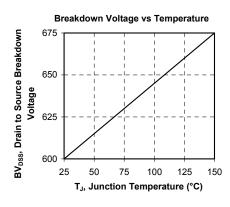


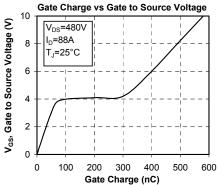






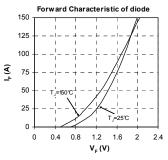


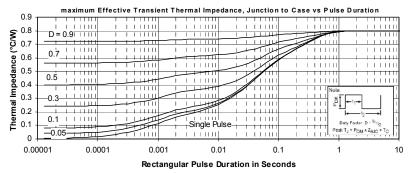




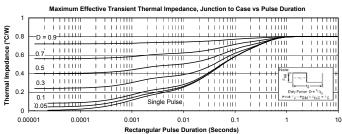


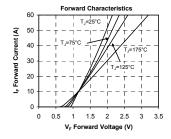
Typical series diode Performance Curve

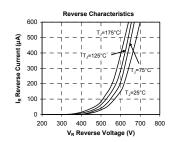


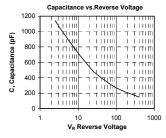


Typical SiC parallel diode Performance Curve



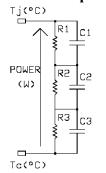








Thermal impedance; CoolMOSTM



RC Final Model

 $R1 = 0.044 \Omega$

 $R2 = 0.103 \Omega$

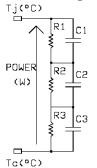
 $R3 = 0.053 \Omega$

C1 = 0.022 F

C2 = 0.347 F

C3 = 4.31 F

Thermal impedance; Series diode



RC Final Model

 $R1 = 0.176 \Omega$

 $R2 = 0.413 \Omega$

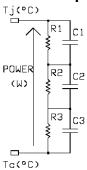
 $R3 = 0.211 \Omega$

C1 = 0.0055 F

C2 = 0.086 F

C3 = 1.07 F

Thermal impedance; SiC Parallel diode



RC Final Model

 $R1 = 0.176 \Omega$

 $R2 = 0.413 \Omega$

 $R3 = 0.211 \Omega$

C1 = 0.0055 F

C2 = 0.086 F

C3 = 1.07 F

DISCLAIMER

The information contained in the document (unless it is publicly available on the Web without access restrictions) is PROPRIETARY AND CONFIDENTIAL information of Microsemi and cannot be copied, published, uploaded, posted, transmitted, distributed or disclosed or used without the express duly signed written consent of Microsemi. If the recipient of this document has entered into a disclosure agreement with Microsemi, then the terms of such Agreement will also apply. This document and the information contained herein may not be modified, by any person other than authorized personnel of Microsemi. No license under any patent, copyright, trade secret or other intellectual property right is granted to or conferred upon you by disclosure or delivery of the information, either expressly, by implication, inducement, estoppels or otherwise. Any license under such intellectual property rights must be approved by Microsemi in writing signed by an officer of Microsemi.

Microsemi reserves the right to change the configuration, functionality and performance of its products at anytime without any notice. This product has been subject to limited testing and should not be used in conjunction with life-support or other mission-critical equipment or applications. Microsemi assumes no liability whatsoever, and Microsemi disclaims any express or implied warranty, relating to sale and/or use of Microsemi products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Any performance specifications believed to be reliable but are not verified and customer or user must conduct and complete all performance and other testing of this product as well as any user or customers final application. User or customer shall not rely on any data and performance specifications or parameters provided by Microsemi. It is the customer's and user's responsibility to independently determine suitability of any Microsemi product and to test and verify the same. The information contained herein is provided "AS IS, WHERE IS" and with all faults, and the entire risk associated with such information is entirely with the User. Microsemi specifically disclaims any liability of any kind including for consequential, incidental and punitive damages as well as lost profit. The product is subject to other terms and conditions which can be located on the web at http://www.microsemi.com/legal/tnc.asp

Life Support Application

Seller's Products are not designed, intended, or authorized for use as components in systems intended for space, aviation, surgical implant into the body, in other applications intended to support or sustain life, or for any other application in which the failure of the Seller's Product could create a situation where personal injury, death or property damage or loss may occur (collectively "Life Support Applications").

Buyer agrees not to use Products in any Life Support Applications and to the extent it does it shall conduct extensive testing of the Product in such applications and further agrees to indemnify and hold Seller, and its officers, employees, subsidiaries, affiliates, agents, sales representatives and distributors harmless against all claims, costs, damages and expenses, and attorneys' fees and costs arising, directly or directly, out of any claims of personal injury, death, damage or otherwise associated with the use of the goods in Life Support Applications, even if such claim includes allegations that Seller was negligent regarding the design or manufacture of the goods.

Buyer must notify Seller in writing before using Seller's Products in Life Support Applications. Seller will study with Buyer alternative solutions to meet Buyer application specification based on Sellers sales conditions applicable for the new proposed specific part.